# PDIUSBD12 USB 2.0 FULL-SPEED PERIPHERAL CONTROLLER

Improves performance and simplifies design in a wide range of systems that use a microcontroller or microprocessor

The PDIUSBD12 is a USB peripheral, designed for use with microcontroller and microprocessor-based systems. Its generic parallel interface allows designers to choose the the most suitable microcontroller or microprocessor for their system. The controller integrates multi configuration FIFO memory, and supports local DMA transfer.

The PDIUSBD12 is targeted at consumer and industrial segments.



## **KEY FEATURES**

- High-speed parallel interface
- Supports 12 Mbit/s data rate
- $\bullet~\mbox{V}_{\mbox{\scriptsize BUS}}$  sensing feature eliminates the need for a costly circuit to enable DP pull-up
- Fully compliant with USB Rev. 2.0 and most Device Class specifications
- Integrated 5.0 V-to-3.3 V voltage regulator for bus-power support
- Integrated clock multiplier PLL supporting low-cost 6 MHz crystal

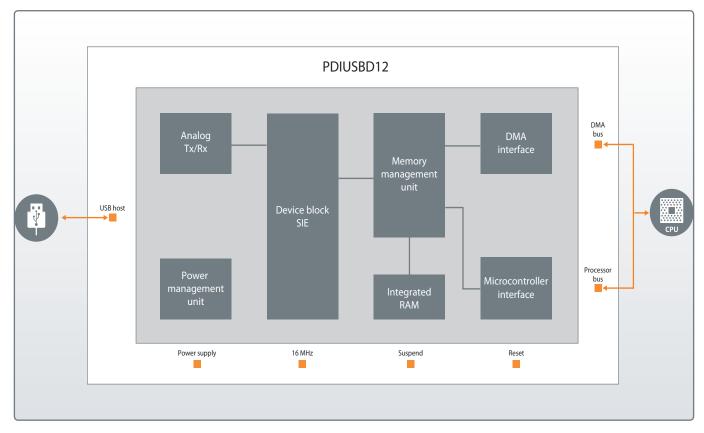
## **KEY BENEFITS**

- Allows designer to select the most appropriate chip for the chosen device
- Allow existing firmware to be re-used, which:
  - Shortens development time
  - Ensures robust stability
  - Minimizes overall cost

# **TARGETED APPLICATIONS**

- Weighing scale
- Router, modem
- Mass storage, such as Flash disks
- Printer, scanner
- Digital camera, PDA, PMP





#### PDIUSBD12 block diagram

The PDIUSBD12 integrates 320 bytes of multi-configuration FIFO memory and supports local DMA transfer. The double buffering scheme for endpoints ensures high throughput and ease of real-time data transfer. The PDIUSBD12 support  $GoodLink^{TM}$  and  $SoftConnect^{TM}$  features.

# **SUPPORTING TOOLS**

PDIUSBD12 evaluation kit.

#### **SUPPORT**

wired.support@stericcson.com

#### **WEBSITE**

www.stericsson.com

# LET'S CREATE IT

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