



# Development Kit User Guide

## AirPrime SL Series



**SIERRA**  
WIRELESS

WA\_DEV\_SL6087\_UGD\_003  
003  
September 24, 2010

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# Document History

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002	September 02, 2010	Removed Battery Charging information.
003	September 24, 2010	Updated for Revision 3.0 PCB – added information for SL80xx support.
		Added information for JTAG.

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# 1. Overview

This document describes the AirPrime SL Development Kit and how it integrates with the AirPrime SL Series Intelligent Embedded Module via a socket-up board. This document discusses the different interface and peripheral connections supported by the AirPrime SL Development Kit and provide schematics to facilitate the user's understanding and configuration of the development kit board for their own application use.

The AirPrime SL Development Kit Board may be used to develop both software and hardware applications based on the AirPrime SL Series embedded module. Note however that in order to use the development kit with the embedded module, the embedded module must be soldered-down to a corresponding socket-up board.

For more information about the AirPrime SL Series embedded modules and the Sierra Wireless Software Suite, refer to the documents listed in section 10 Reference Documents.

## 2. General Description

This section gives a brief overview of the AirPrime SL Development Kit and briefly describes the interfaces and special jumper pads available.

### 2.1. AirPrime SL Development Kit

The AirPrime SL Development Kit is used to allow users to create and define applications using the AirPrime SL Series embedded module.

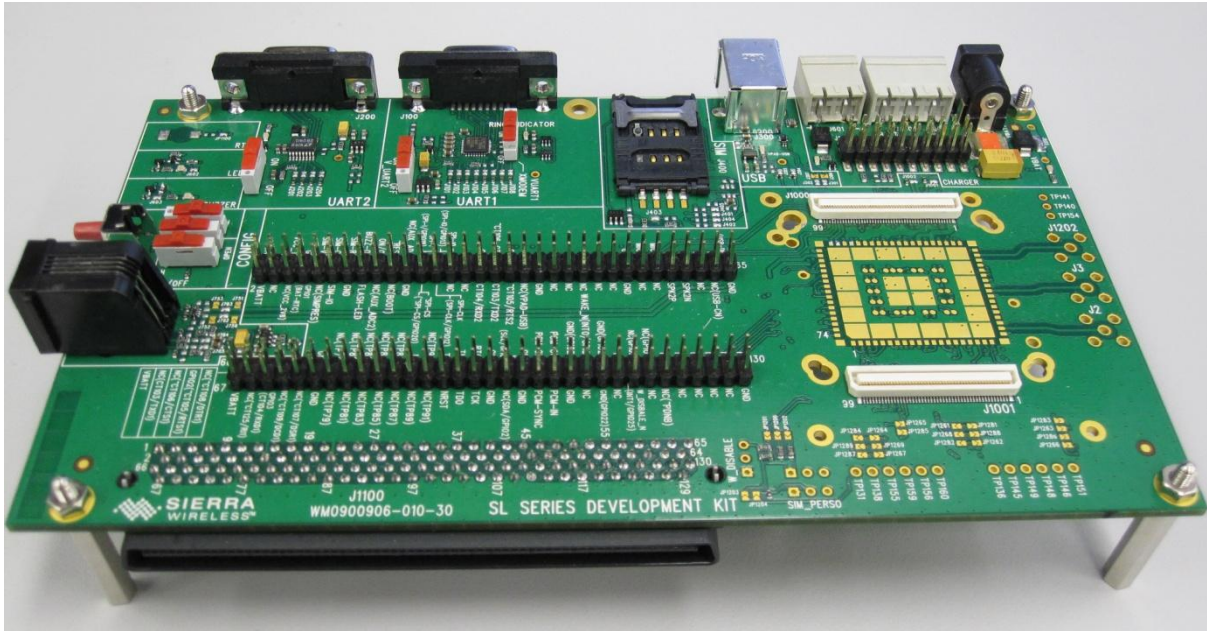


Figure 1. AirPrime SL Development Kit

## 2.1.1. Interfaces

Interfaces available on the AirPrime SL Development Kit which are supported on the various SL series embedded modules are listed in the table below.

**Table 1. Interfaces Available on the SL Series Embedded Module**

SL6087	SL8080	SL809x, SL8081, SL8083
External board to board connector and test point (TP) to access all signals for the adaptor board	External board to board connector and test point (TP) to access all signals for the adaptor board	External board to board connector and test point (TP) to access all signals for the adaptor board
Serial link RS232, UART1* with full signals	Serial link RS232, UART1* with four signals	Serial link RS232, UART1* with four signals
Ring Indicator	--	--
Serial link RS232, UART2* with four signals	--	--
Slave USB*, with SoftConnect™ signal. USB 2.0 Compliant (full speed)	Slave USB*, with SoftConnect™ signal. USB 2.0 Compliant (full speed)	Slave USB*, with SoftConnect™ signal. USB 2.0 Compliant (full speed)
SIM* (1.8/3V)	SIM* (1.8/3V)	SIM* (1.8/3V)
1 Audio connector (AUDIO2)	1 Audio connector (AUDIO2)	--
LEDs for several indications	LEDs for several indications	LEDs for several indications
Power supply connectors	Power supply connectors	Power supply connectors
RESET Pushbutton	RESET Pushbutton	RESET Pushbutton
ON/OFF switch	ON/OFF switch	ON/OFF switch
BOOT switch	--	--
Buzzer LED	Buzzer LED	Buzzer LED
Flash LED	Flash LED	Flash LED

\* These signals from the connector side can be electrically disconnected from the AirPrime if the related jumper pads are dissociated. Refer to section 2.1.3 Special Soldering for Jumper Pads for more information regarding jumper pads.

## 2.1.2. Default Soldering Configuration for Jumpers

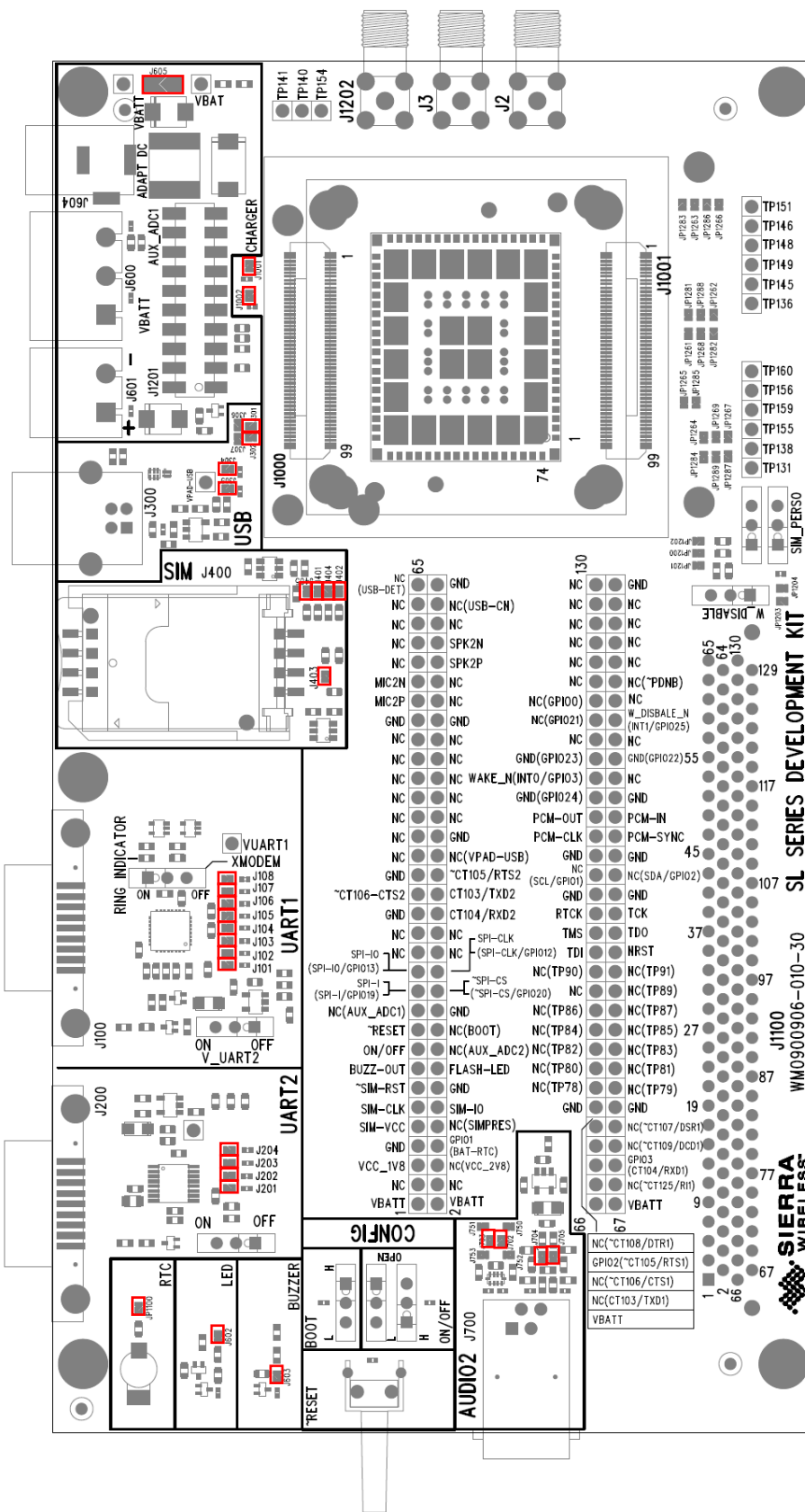


Figure 2. Default Soldering Configuration for Jumpers (Development Kit)

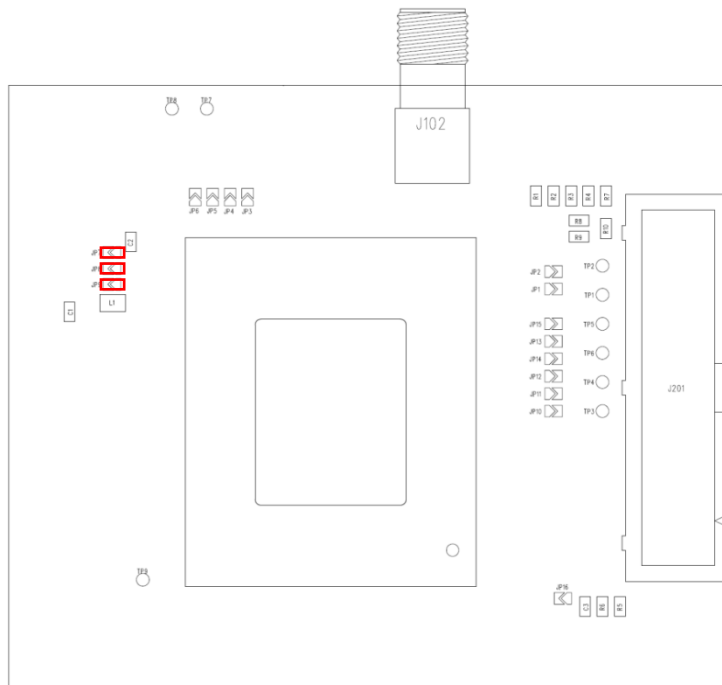


Figure 3. Default Soldering Configuration for Jumpers for the SL6087 Socket-Up Board

### 2.1.3. Special Soldering for Jumper Pads

PCB jumper prints are used to electrically connect or disconnect peripherals between the AirPrime SL series embedded module and the AirPrime SL Development Kit.

To connect signals between the AirPrime SL series embedded module (from J1000 and J1001) and the dedicated connectors on the AirPrime SL Development Kit, solder the PCB jumper prints specified in section 2.1.2 Default Soldering Configuration for Jumpers.

To connect signals between the socket-up board and the external board connector (J1100), it is recommended NOT to solder the PCB prints. Instead, a daughter board can be prototyped to connect the socket-up board directly through the board connector (J1100).



Figure 4. Jumper Solder Pad

The interfaces (and signals) listed below could be electrically removed by dissociating the following PCB jumper prints:

- Power supply of the AirPrime SL Development Kit interfaces (All components from J605, except for the AirPrime SL series embedded module. For more information, refer to section 9 Current Consumption Measurement.)
- SIM (from J401 to J405)
- UART1 (from J101 to J108)
- UART2 (from J201 to J204)
- USB (from J301 to J302)
- AUDIO2 (from J702 to J705)
- FLASH-LED signal (J602)
- BUZZ-OUT signal (J603)

## 2.2. RoHS Compliance

The AirPrime SL Development Kit board is compliant with RoHS (Restriction of Hazardous Substances in Electrical and Electronic Equipment) Directive 2002/95/EC which sets limits for the use of certain restricted hazardous substances. This directive states that “from 1st July 2006, new electrical and electronic equipment put on the market does not contain lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB) or polybrominated diphenyl ethers (PBDE)”.

The AirPrime SL series embedded module is also compliant with this directive and is identified by the RoHS logo on its label.



## 3. AirPrime SL Development Kit

This section provides photographs of both the top and bottom views of the AirPrime SL Development Kit that show the locations of the several ports and interfaces available; and lists all available test points on the development kit.

### 3.1. AirPrime SL Development Kit Top View

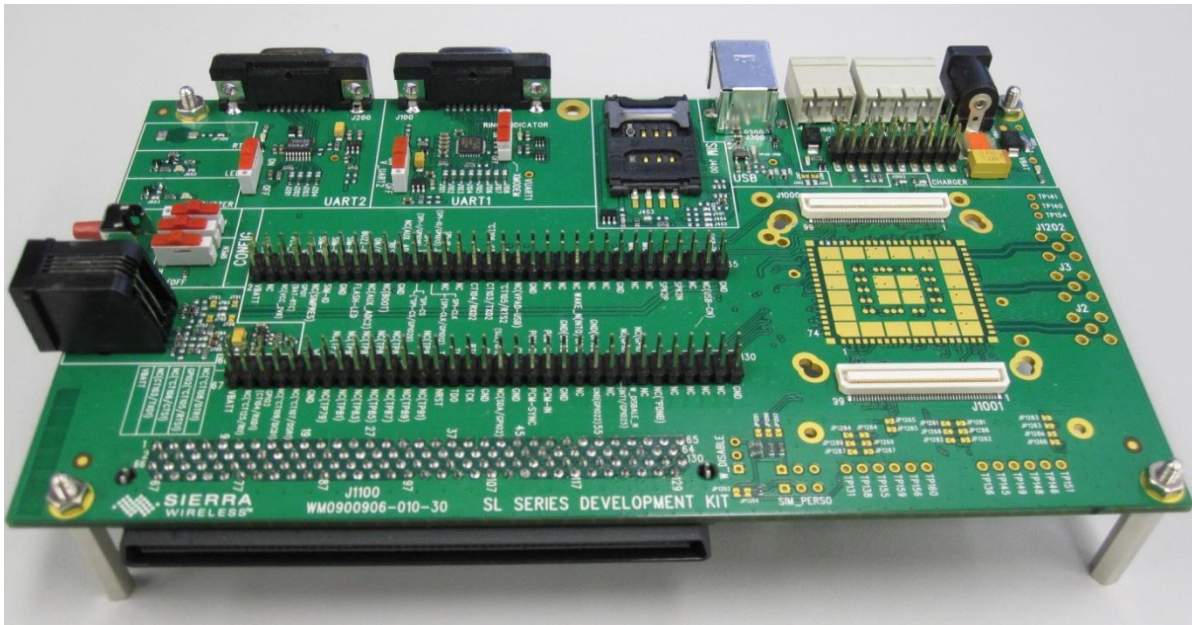


Figure 5. AirPrime SL Development Kit, Top View



## 3.2. AirPrime SL Development Kit Bottom View

The J1100 connector can be used as a daughter board interface when implementing applications with the AirPrime SL Series embedded module. Refer to document [3] AirPrime WMP100 Development Kit User Guide for more information about the external board connector pin assignments.

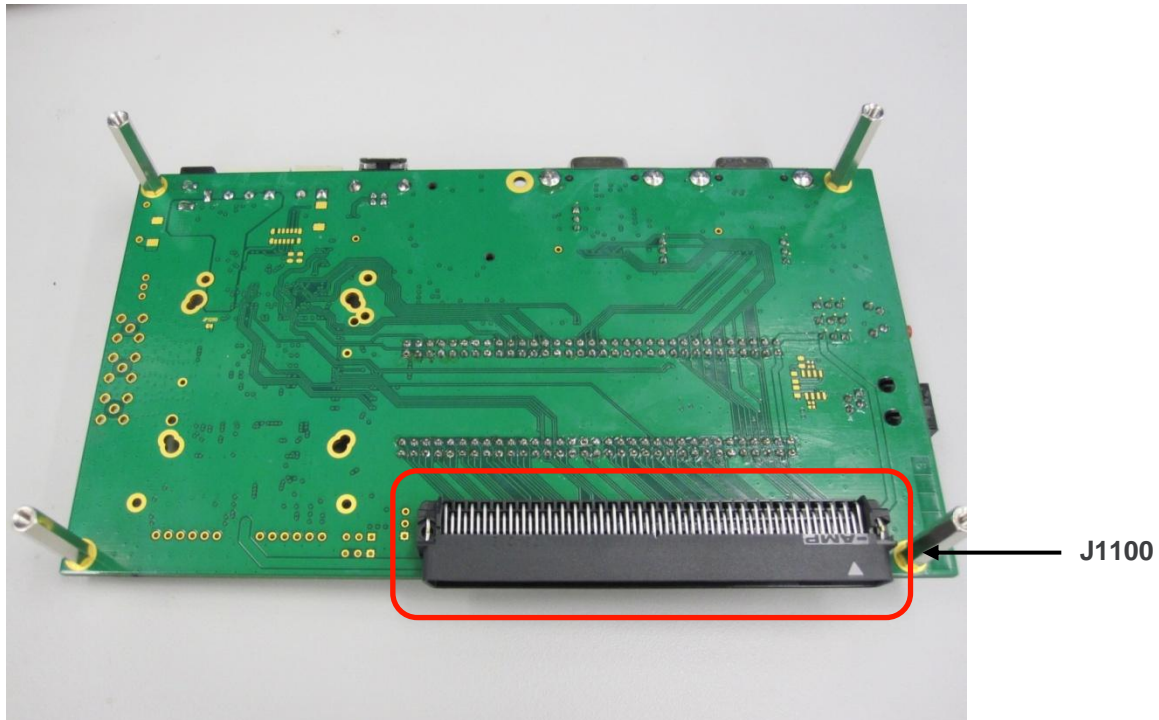


Figure 6. AirPrime SL Development Kit, Bottom View

## 3.3. Available Test Ports on the AirPrime SL Development Kit

There are a total of 130 test ports available in the AirPrime SL Development Kit. The following figure shows the location of these test ports in the AirPrime SL Development Kit and the table below lists their corresponding pin assignments.



Table 2. AirPrime SL Development Kit Test Ports

Test Port	Pin Assignment (Signal) for SL6087	Pin Assignment (Signal) for SL80xx
1	VBATT	VBATT
2	VBATT	VBATT
3	NC	NC
4	NC	NC
5	VCC_1V8	VCC_1V8
6	VCC_2V8	NC
7	GND	GND
8	BAT_RTC	GPIO1
9	SIM-VCC	SIM-VCC
10	SIMPRES	NC
11	SIM-CLK	SIM-CLK
12	SIM-IO	SIM-IO
13	~SIM-RST	~SIM-RST
14	GND	GND
15	BUZZ-OUT	BUZZ-OUT
16	FLASH-LED	FLASH-LED
17	ON/OFF	ON/OFF
18	AUX_ADC2	NC
19	~RESET	~RESET
20	BOOT	NC
21	AUX_ADC1	NC
22	GND	GND
23	SPI1-I/GPIO19	SPI1-I
24	~SPI1-CS/GPIO20	~SPI1-CS
25	SPI1-IO/GPIO13	SPI1-IO
26	SPI1-CLK/GPIO12	SPI1-CLK
27	NC	NC
28	NC	NC
29	NC	NC
30	NC	NC
31	GND	GND
32	CT104/RXD2	CT104/RXD2
33	~CT106/CTS2	~CT106/CTS2
34	CT103/TXD2	CT103/TXD2
35	GND	GND
36	~CT105/RTS2	~CT105/RTS2
37	NC	NC
38	VPAD-USB	NC
39	NC	NC
40	GND	GND
41	NC	NC
42	NC	NC

Test Port	Pin Assignment (Signal) for SL6087	Pin Assignment (Signal) for SL80xx
43	NC	NC
44	NC	NC
45	NC	NC
46	NC	NC
47	NC	NC
48	NC	NC
49	NC	NC
50	NC	NC
51	GND	GND
52	GND	GND
53	MIC2P	MIC2P
54	NC	NC
55	MIC2N	MIC2N
56	NC	NC
57	NC	NC
58	SPK2P	SPK2P
59	NC	NC
60	SPK2N	SPK2N
61	NC	NC
62	NC	NC
63	NC	NC
64	USB-CN	NC
65	USB-DET	NC
66	VBATT	VBATT
67	VBATT	VBATT
68	CT103/TXD1	NC
69	~CT125/RI1	NC
70	~CT106/CTS1	NC
71	CT104/RXD1	GPIO3
72	~CT105/RTS1	GPIO2
73	~CT109/DCD1	NC
74	~CT108/DTR1	NC
75	~CT107/DSR1	NC
76	GND	GND
77	GND	GND
78	TP78	NC
79	TP79	NC
80	TP80	NC
81	TP81	NC
82	TP82	NC
83	TP83	NC
84	TP84	NC
85	TP85	NC

Test Port	Pin Assignment (Signal) for SL6087	Pin Assignment (Signal) for SL80xx
86	TP86	NC
87	TP87	NC
88	NC	NC
89	TP89	NC
90	TP90	NC
91	TP91	NC
92	TDI	TDI
93	NRST	NRST
94	TMS	TMS
95	TDO	TDO
96	RTCK	RTCK
97	TCK	TCK
98	GND	GND
99	GND	GND
100	SCL/GPIO1	NC
101	SDA/GPIO2	NC
102	GND	GND
103	GND	GND
104	PCM-CLK	PCM-CLK
105	PCM-SYNC	PCM-SYNC
106	PCM-OUT	PCM-OUT
107	PCM-IN	PCM-IN
108	GPIO24	GND
109	GND	GND
110	INT0/GPIO3	WAKE_N
111	NC	NC
112	GPIO23	GND
113	GPIO22	GND
114	NC	NC
115	NC	NC
116	GPIO21	NC
117	INT1/GPIO25	W_DISABLE_N
118	GPIO0	NC
119	NC	NC
120	NC	NC
121	~PDNB	NC
122	NC	NC
123	NC	NC
124	NC	NC
125	NC	NC
126	NC	NC
127	NC	NC
128	NC	NC

---

Test Port	Pin Assignment (Signal) for SL6087	Pin Assignment (Signal) for SL80xx
129	NC	NC
130	NC	NC

## 4. Socket-Up Board

The socket-up board is used to interface the AirPrime SL Series embedded module with the SL Development Kit. The socket-up board provides a changeable interface for the SL series embedded modules using the same set of peripheral devices, and it varies depending on which SL series embedded module is used.

Refer to section 12 Appendix for further information about the AirPrime SL Development Kit and the available Socket-Up Boards.

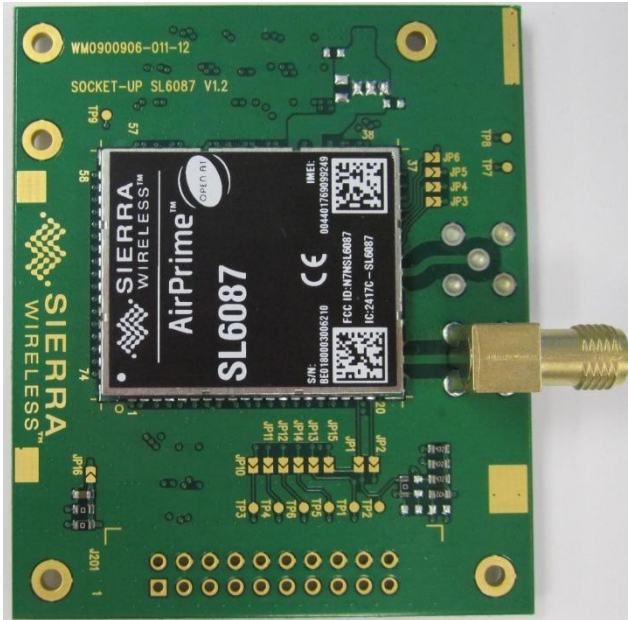


Figure 8. AirPrime SL6087 Socket-Up Board with the AirPrime SL6087 Embedded Module (Top View)

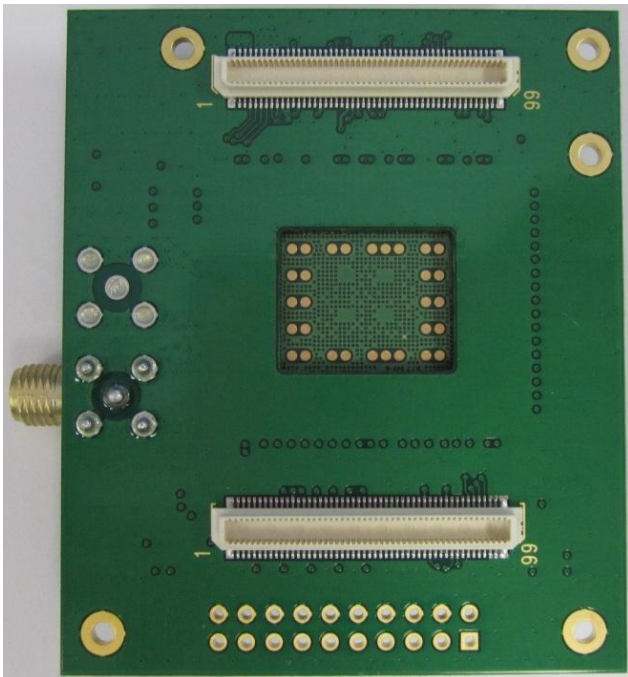


Figure 9. AirPrime SL6087 Socket-Up Board with the AirPrime SL6087 Embedded Module (Bottom View)

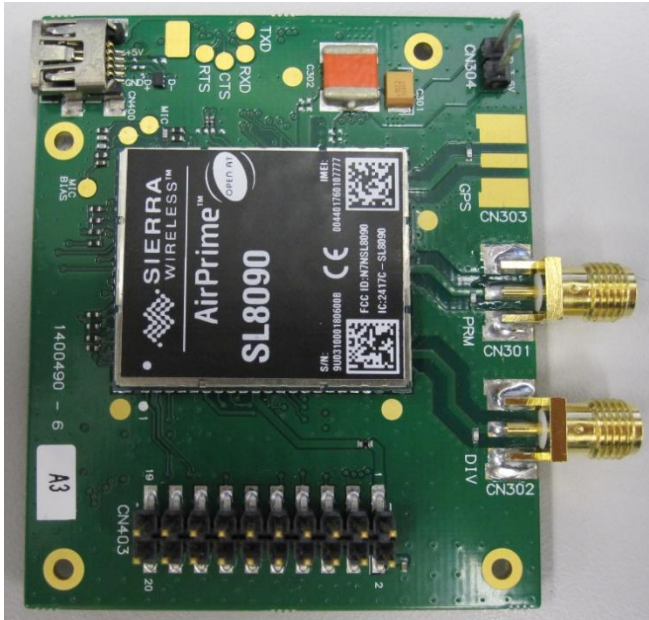


Figure 10. AirPrime SL8090 Socket-Up Board with the AirPrime SL8090 Embedded Module (Top View)

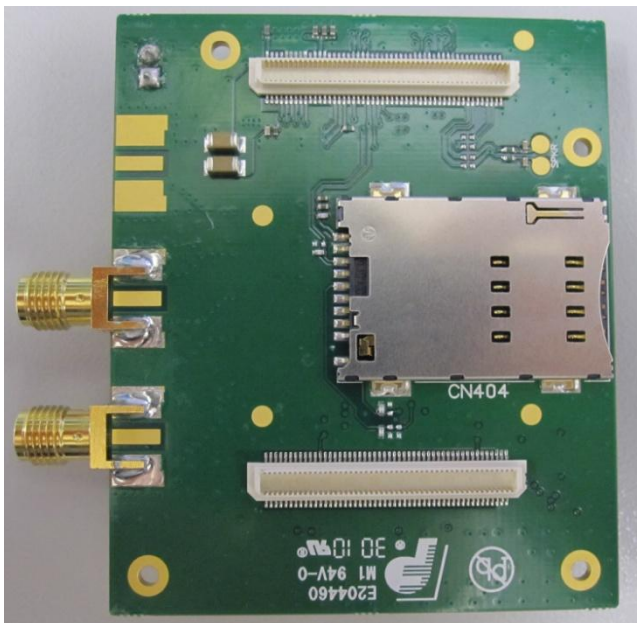


Figure 11. AirPrime SL8090 Socket-Up Board with the AirPrime SL8090 Embedded Module (Bottom View)



# 4.1. Socket-Up Board Component Placement Diagrams

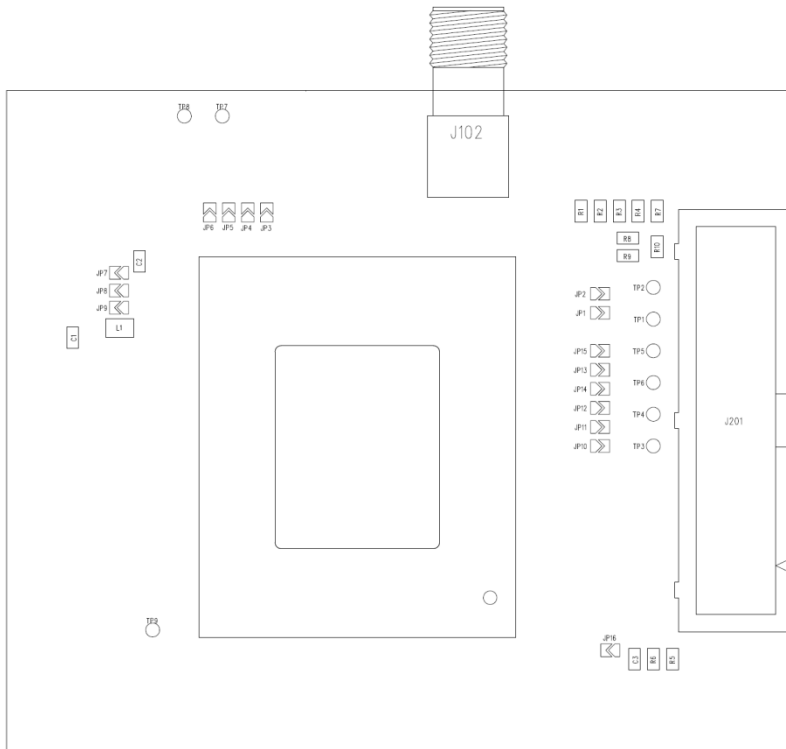


Figure 12. AirPrime SL6087 Socket-Up Board (Top View)

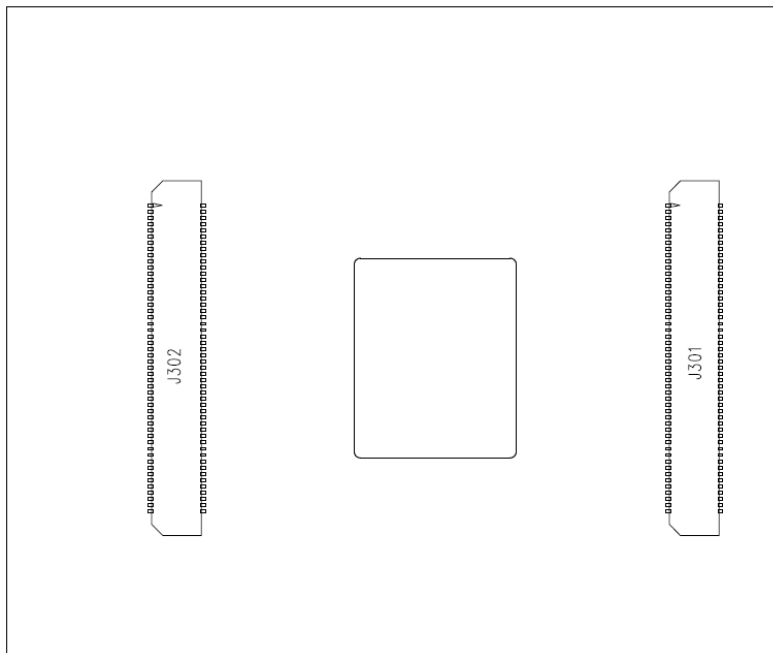


Figure 13. AirPrime SL6087 Socket-Up Board (Bottom View)

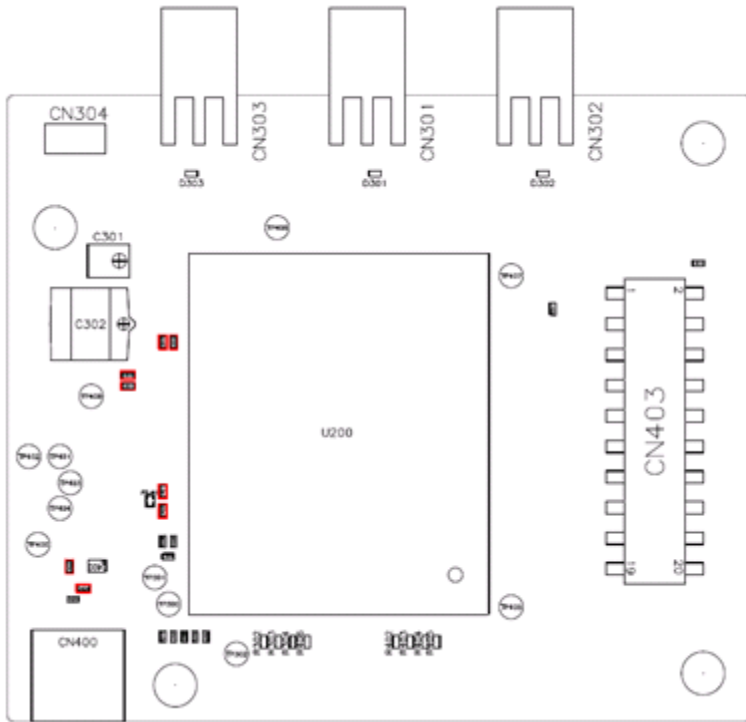


Figure 14. AirPrime SL8090 Socket-Up Board (Top View)

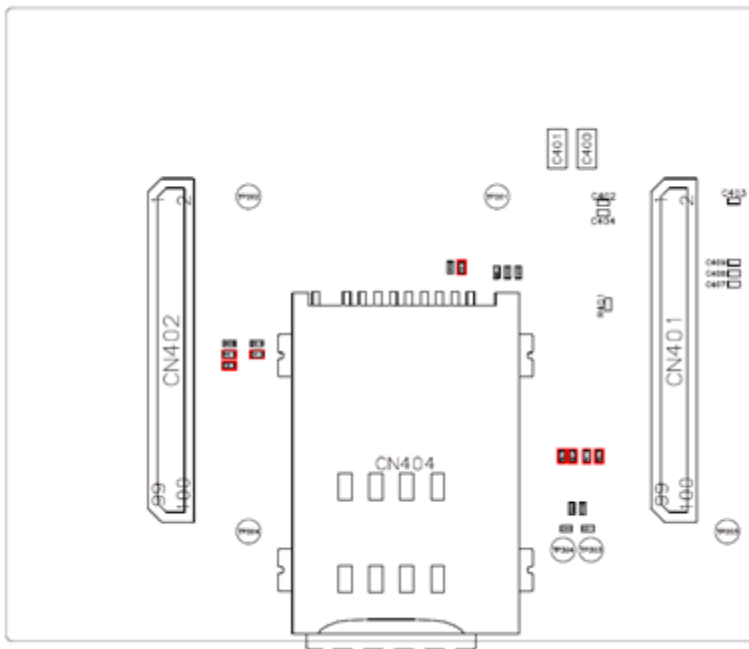


Figure 15. AirPrime SL8090 Socket-Up Board (Bottom View)



## 5. Setting Up the AirPrime SL Development Kit

The following section describes how the AirPrime SL Development Kit and the Socket-Up Board (with the AirPrime SL Series embedded module soldered-down) are setup. It also briefly describes how communication tests are done to ensure that the AirPrime SL Series Embedded Module has been properly connected to the AirPrime SL Development Kit.

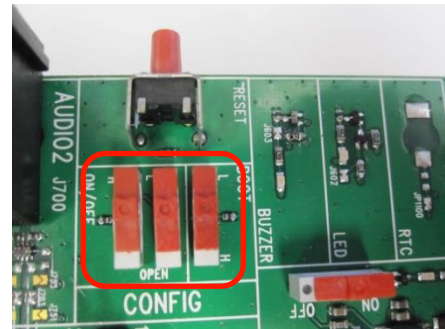
### 5.1. Setting Up the AirPrime SL Development Kit

Prepare the AirPrime SL Development Kit and the Socket-Up Board by following these instructions step by step.

1. Turn the ON/~OFF switches as follows:

Switch	For SL6087	For SL80xx
SW600	“H”	“OPEN”
SW601	“L”	“L”

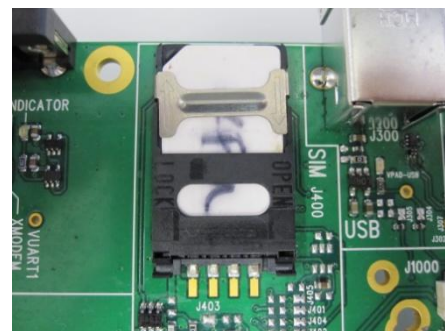
For SL6087, the BOOT switch (SW602) should also be turned to the “L” position.



2. Plug the Socket-Up Board onto the AirPrime SL Development Kit using board to board connectors J1000 and J1001.



3. Insert a SIM card into the SIM card holder, J400 (if communications is required).



---

**Note:** Note that all jumper pads are soldered by default. (Refer to section 2.1.3 Special Soldering for Jumper Pads for more information.) Retain these settings.

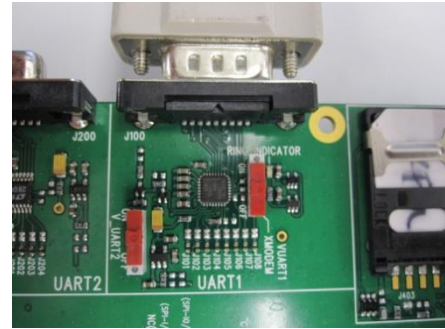
---

4. Connect the RS232 cable between the PC port and J100 of the AirPrime SL Development Kit and make sure that SW101 is in the "ON" position.

---

*Note:* By default,  
baud rate = 115200 kbps, data bits = 8, parity = none,  
and stop bits = 1.

---



5. If USB communications is required,
  - a. For SL6087:  
Connect the USB cable between the USB port and J300 of the AirPrime SL Development Kit.
  - b. For SL80xx:  
Connect the USB cable to the mini USB port on the Socket-Up Board directly.

---

*Note:* The provided USB driver should be also installed in the host computer.

---



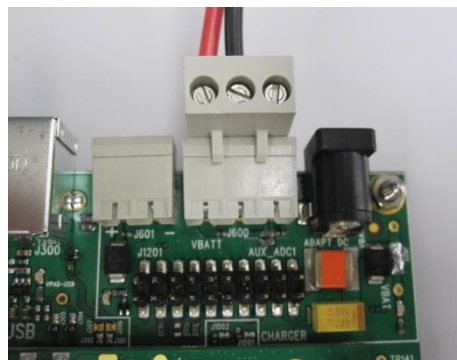
6. If RF communications is required, connect the SMA connector on the Socket-Up Board to an external antenna or a Radio Communication Tester using a coaxial cable.



7. If audio communications is required on the Analog Audio enabled AirPrime SL Series embedded module, connect the handset to the audio connector, J700.



- 8. Plug in the AC/DC power supply provided in the J604 connector; or connect it to an external DC power supply at 4V/2A (J600).



The AirPrime SL Development Kit should look like the following figure after it has been properly setup.

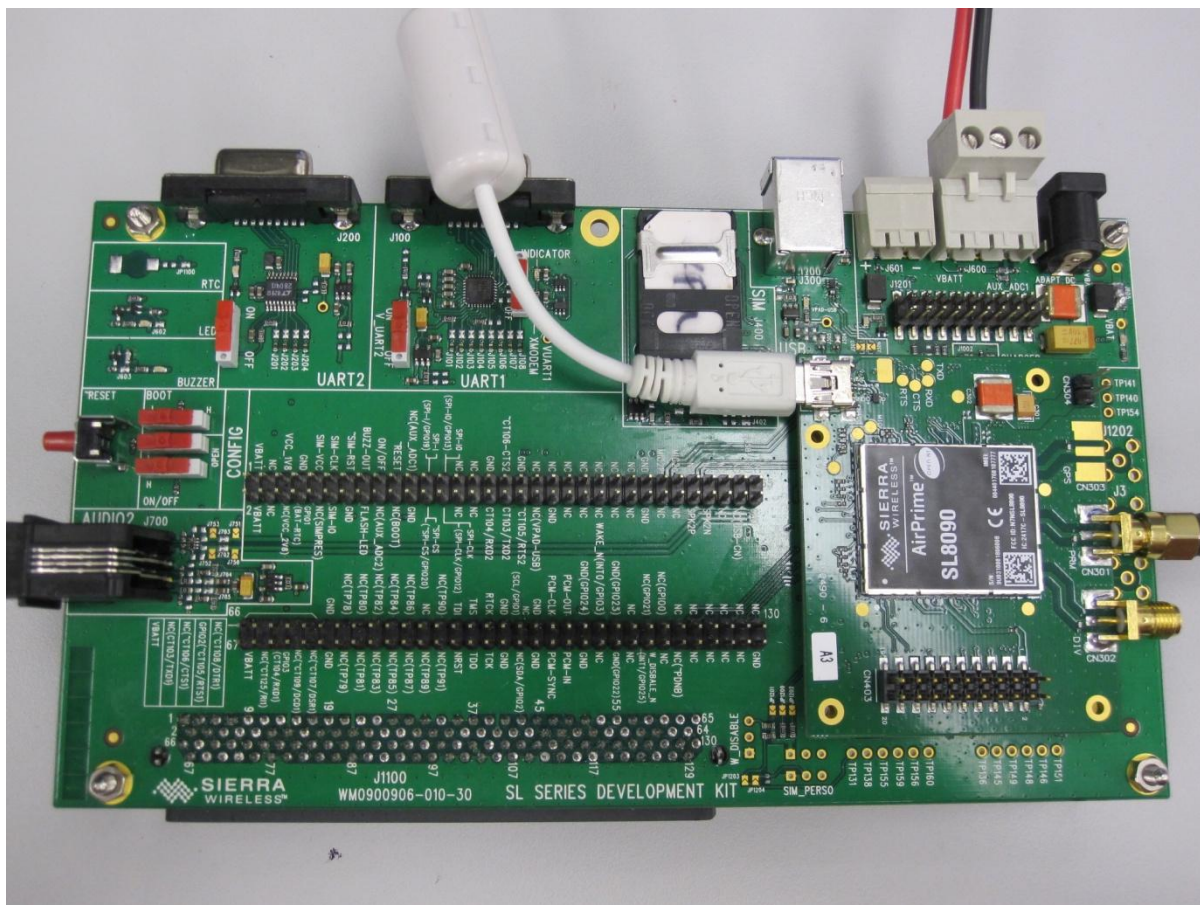


Figure 16. AirPrime SL Development Kit (with an SL8090 Socket-Up Board and Embedded Module)

## 5.2. Communications Test

To perform a communications test after setting the AirPrime SL Development Kit with the AirPrime SL Series embedded module, do the following:

1. Configure SW600 and SW601 as shown in the following table:

Switch	SL6087	SL80xx
SW600	"H"	"Open"
SW601	"L"	"L"

The AirPrime SL Series embedded module will be automatically switched ON once power is applied.

2. Using a PC terminal emulator, send the following command on a serial port to communicate with the AirPrime SL Series embedded module:

**AT↵**

3. When communications is established between the PC and the AirPrime SL Series embedded module, the embedded module replies with an "OK". Verify that the response is displayed in the terminal emulator window.

Refer to document [2] Firmware 7.4b AT Commands Manual (Sierra Wireless Software Suite 2.32) for more information about using AT Commands to communicate between a PC and an AirPrime SL Series embedded module.

## 6. Interfaces/Peripherals

This section describes the different interfaces/peripherals that are available in the AirPrime SL Development Kit.

### 6.1. Power Supplies

Two power supply sources are available on the AirPrime SL Development Kit:

- DC external supply (via J600)
- AC/DC adapter (via J604)

These power supplies are protected against electrostatic discharge (ESDs) and voltage or current transient surges by ESD diodes or varistors.

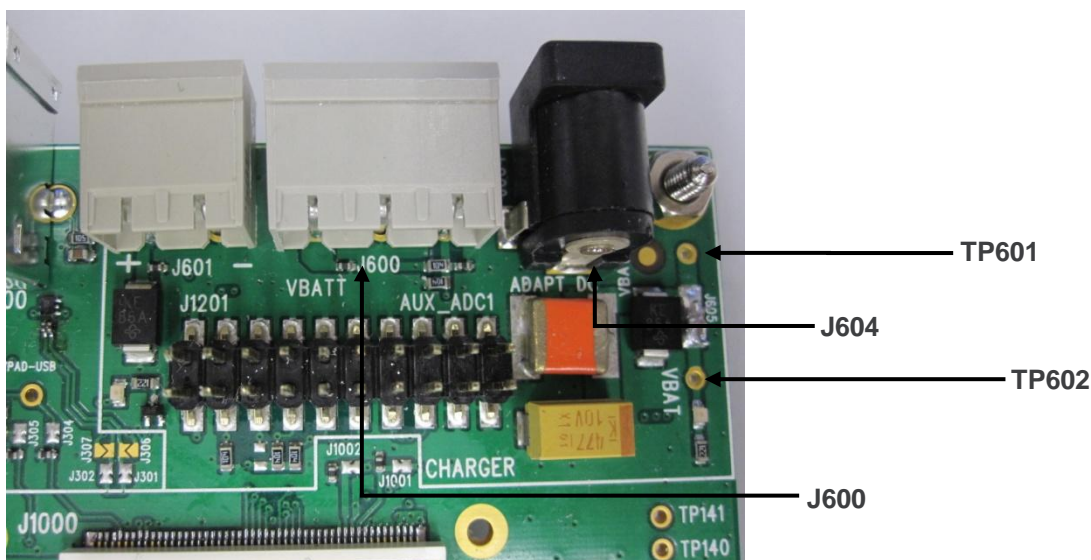


Figure 17. Power Supply Connectors (J604 and J600)

Either one these power supplies can be used for **both** the AirPrime SL Series embedded module and the peripherals on the AirPrime SL Development Kit.

Note that it is possible to separate the power supply for the Socket-Up Board (“VBATT” via TP601) and the power supply for the peripherals (“VBAT” via TP602) by unsoldering J605. Current measurement is therefore possible for the AirPrime SL Development Kit and for the AirPrime SL Series embedded module. Refer to section 9 Current Consumption Measurement for more information.



Figure 18. VBATT Connection (J605)

### 6.1.1. Main Supply Adapter

The J604 connector powers the AirPrime SL Development Kit using an AC/DC power supply cable.

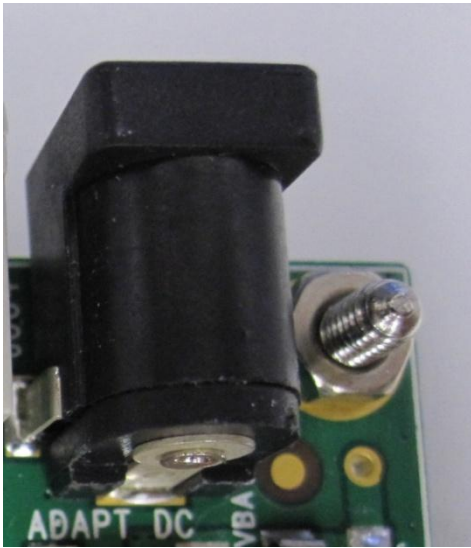


Figure 19. Main Supply Adapter

Details on the only supported adapter are listed in the following table.

Table 3. Supported Adapter

Manufacturer	Reference	Characteristics
SINPRO	SPU12C-101	4V DC / 2.5A

### 6.1.2. External Supply

The external supply is accessible through the J600 connector.

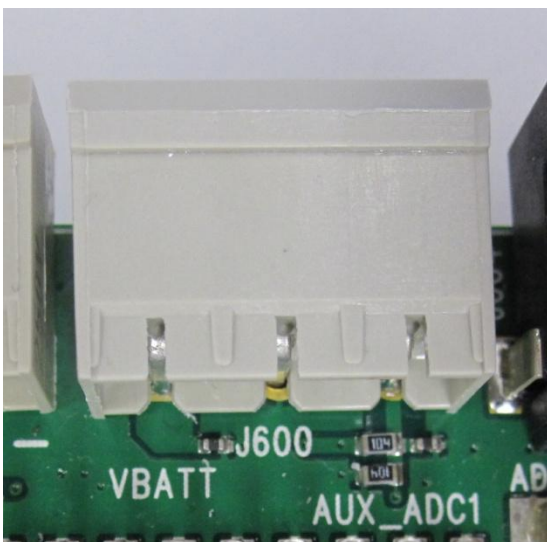


Figure 20. External Power Supply



The J600 connector has three pins:

- **Pins 1-2** are used to plug the power supply.
- **Pins 2-3** are used to plug in ADC1. (Refer to section 6.1.2.1 ADC1 for more information.)

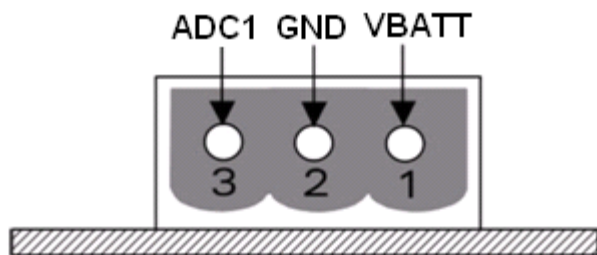


Figure 21. External Power Supply Pins

Refer to the following table for the electrical characteristics of VBATT.

Table 4. Electrical Characteristics of VBATT

VBATT <sup>1,2</sup>	V <sub>MIN</sub>	V <sub>NOM</sub>	V <sub>MAX</sub>
SL6087	3.2V	3.6V	4.8V
SL80xx	3.3V	3.6V	4.3V

- 1: This value has to be guaranteed during the burst (with 1.5A Peak in GSM or GPRS mode).
- 2: Maximum operating Voltage Stationary Wave Ratio (VSWR) is 2:1.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

### 6.1.2.1. ADC1

The ADC1 signal is an input (ADC) to the AirPrime SL Series embedded module.

Pins 2-3 of J600 allow the simulation of the temperature level from a sensor inside the battery. Refer to the following table for the electrical characteristics of ADC1.

Table 5. Electrical Characteristics of ADC1

	V <sub>MIN</sub>	V <sub>NOM</sub>	V <sub>MAX</sub>
ADC1	0	-	2V

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

### 6.1.3. LED Signalization for VBAT

The “VBAT” indicator is a green LED and it indicates the presence of a power supply at J600 or J604. Both the AirPrime SL Series embedded module and its peripherals are powered by this power source when J605 is soldered. If J605 is unsoldered, an extra external power supply should be connected to “VBAT” for the AirPrime SL Development Kit.

**Tip:** *It is recommended to always use both VBATT and VBAT simultaneously.*

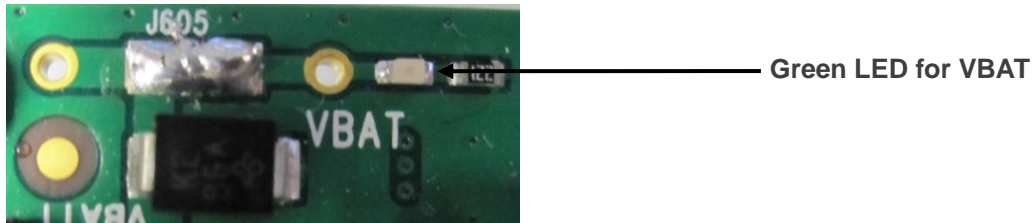


Figure 22. LED Signalization of VBAT

Refer to the following table for the VBATT and VBAT status depending on the LED state.

Table 6. Status of VBATT and VBAT

LED State	VBATT Status	VBAT Status
ON	ON	ON
OFF	ON when J605 is soldered; OFF when J605 is un-soldered	OFF

## 6.2. SIM

The SIM interface signals on the AirPrime SL Development Kit can be electrically disconnected from the AirPrime SL Series embedded module if the five solder pads, J401 to J405, are dissociated. Unsoldering them allows the SIM signals to transmit to an external interface board via the external connector (J1100), with R1122, R1123, R1124, R1125 and R1126 equipped with 0Ω resistors (0603 package resistors).

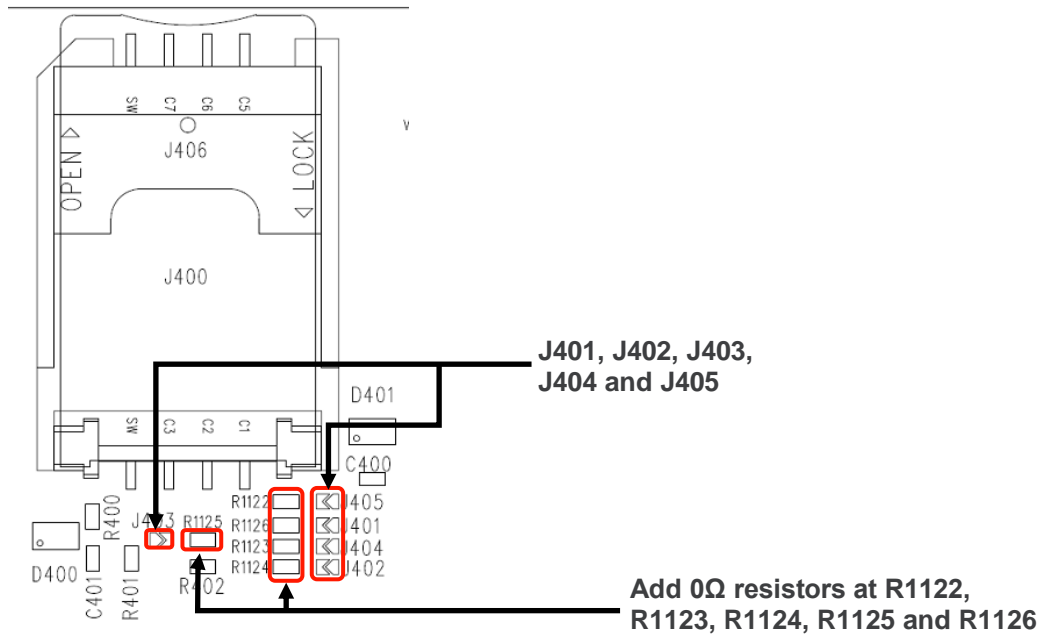


Figure 23. Solder Pads and Resistor Locations on the SIM Interface

By default, SIM is available on its dedicated connector, J400, with J401 to J405 soldered.

---

*Note:* ESD protection is available on all SIM signals.

### 6.2.1. SIM Connector (J400)

J400 is a standard 1V8 or 3V SIM socket.

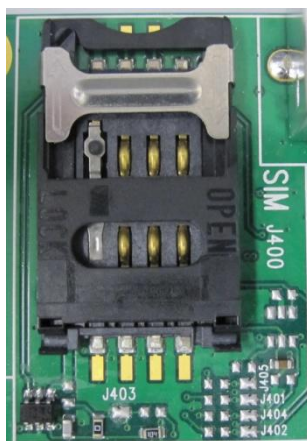


Figure 24. SIM Connector

Refer to the following table for the SIM connector pin description.

**Table 7. SIM Connector Pin Description**

Pin Number	Signal Name	I/O	I/O Type	Description
1	SIM-VCC	O	1V8 or 2V9	SIM Power Supply
2	SIM-RST	O	1V8 or 2V9	SIM Reset
3	SIM-CLK	O	1V8 or 2V9	SIM Clock
4	SIMPRES	I	2V8 max*	SIM Card Detect (Not used)
5	GND			Ground
6	VPP	Not used		
7	SIM-IO	I/O	1V8 or 2V9	SIM Data
8	CC8		2V8	SIMPRES signal supply (Not used)

\* For either 1V8 or 3V SIM cards.

## 6.3. UART1

UART1 of the AirPrime SL Development Kit is connected to the RS232 serial link interface of the AirPrime SL Series embedded module. The voltage level of UART1 is 2.8V from the AirPrime SL Development Kit side.

The eight UART1 signals in the AirPrime SL Development Kit could be electrically disconnected from the AirPrime SL Series embedded module if the eight tie pads, J101 to J108, are dissociated. Unsoldering them allows the UART1 signals to transmit to an external interface board via the external connector (J1100).

By default, UART1 is available on its dedicator connector, J100, with J101 to J108 soldered.

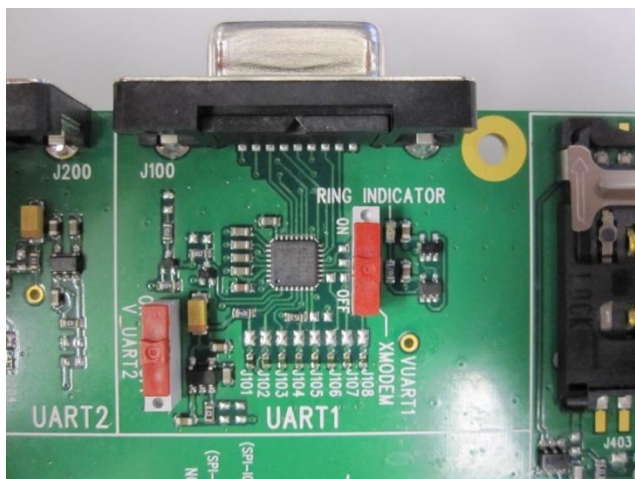


Figure 25. UART1 Connector

### 6.3.1. UART1 Connector (J100)

J100 is a SUB-D 9-pin female connector.

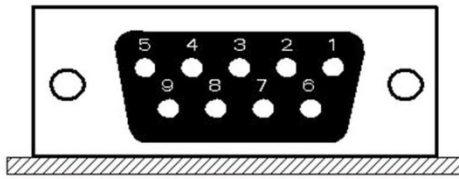


Figure 26. DB-9 Female Connector for UART1

Refer to the following table for the UART1 connector signal pin description.

Table 8. UART1 Connector Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	CT109 DCD	O	RS232 (V24/V28)	Data Carrier Detect
2	CT104 RXD	O	RS232 (V24/V28)	Receive serial data
3	CT103 TXD	I	RS232 (V24/V28)	Transmit serial data
4	CT108-2 DTR	I	RS232 (V24/V28)	Data Terminal Ready
5	GND			Ground
6	CT107 DSR	O	RS232 (V24/V28)	Data Set Ready
7	CT105 RTS	I	RS232 (V24/V28)	Request To Send
8	CT106 CTS	O	RS232 (V24/V28)	Clear To Send
9	CT125 RI	O	RS232 (V24/V28)	Ring Indicator

### 6.3.2. UART1 Configuration

The AirPrime SL Development Kit acts as a DCE and is connected to a DTE (PC or terminal) with a “straight cable”. This is a full UART.

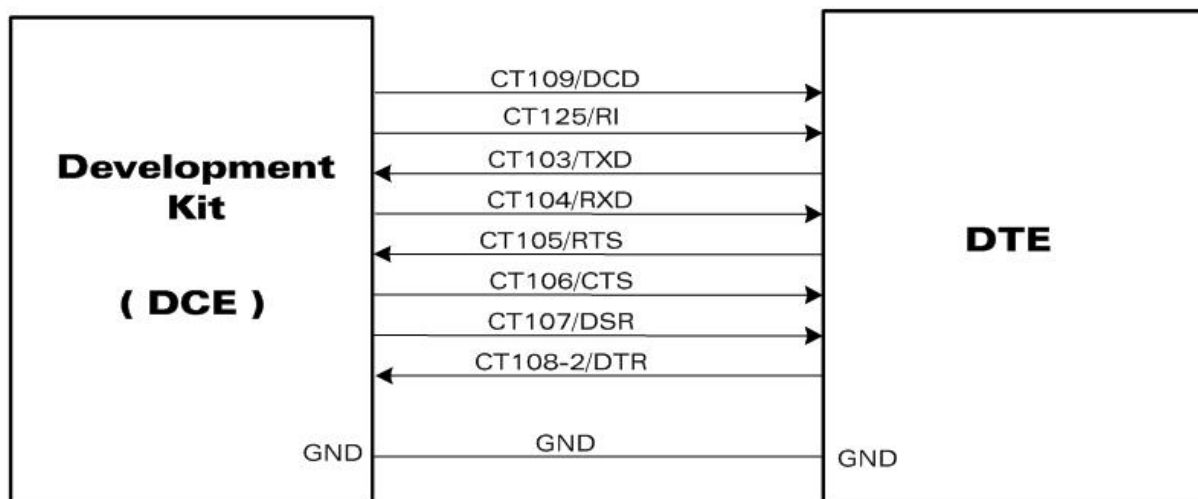


Figure 27. RS232 Main Serial Link

### 6.3.3. Enabling the UART1 Function (SW101)

The UART1 interface can be enabled by switching SW101 to the “ON” position.

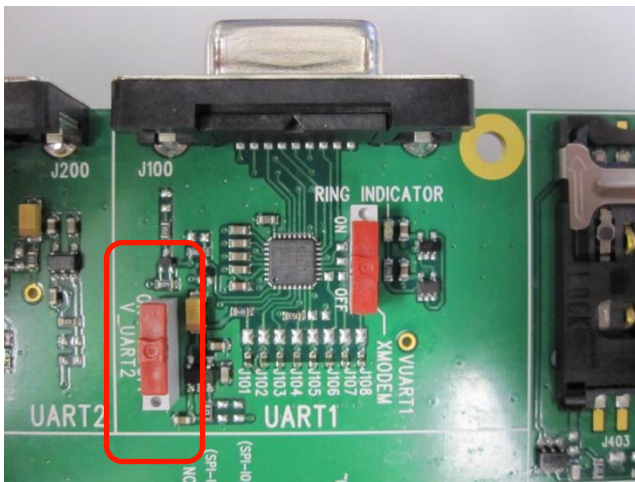


Figure 28. UART1 Switch Configuration

### 6.3.4. Switch “XMODEM” (SW100)

**Caution:** The “XMODEM” switch must always be in the “OFF” position, as shown in the figure below. Do not use this switch. If SW100 is in the wrong position (“ON”), UART1 will not work properly.

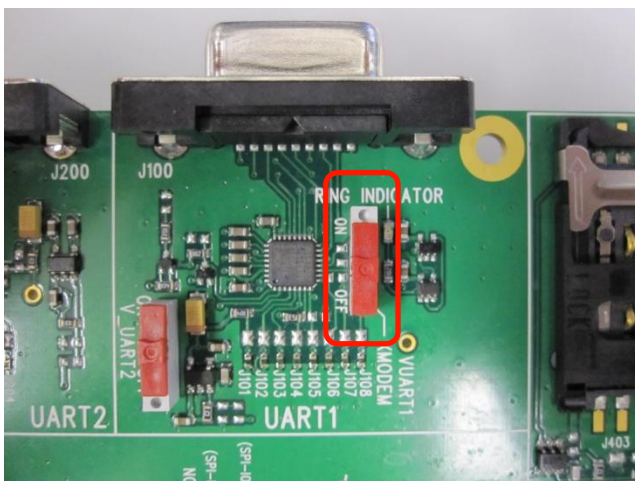


Figure 29. XMODEM Switch Configuration (SW100)

Refer to the following table for the XMODEM configuration.

Table 9. XMODEM Configuration

Mode	Description
Normal	OFF

### 6.3.5. LED Signalization for UART1 (D100, D101)

Two LEDs are available to indicate the state of UART1.

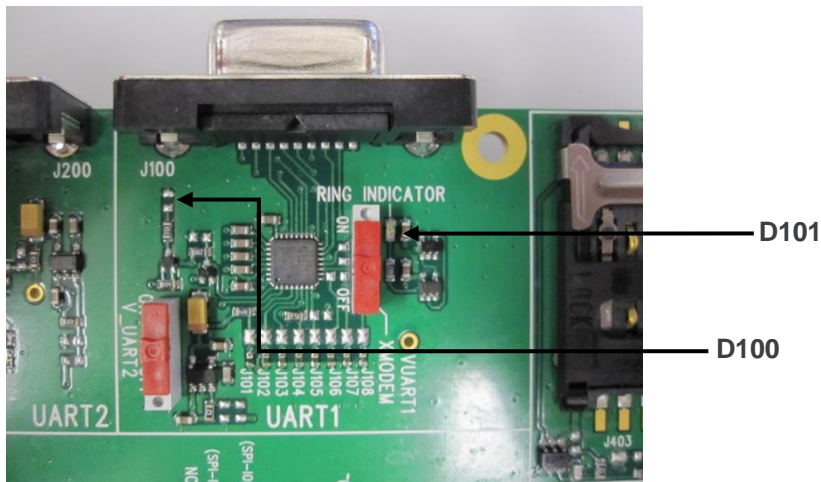


Figure 30. UART1 LED Location (D100, D101)

#### 6.3.5.1. LED “UART1” (D100)

This green LED indicates the power supply state of UART1.

The interface can be used when it is lit depending on which power supply is present (J600 or J604). Refer to section 6.1 Power Supplies for more information.

#### 6.3.5.2. LED “RING INDICATOR” (D101)

The “RING INDICATOR” indicator is a yellow LED controlled by the RI signal on the AirPrime SL Series embedded module.

When the AirPrime SL Series embedded module receives an incoming call, the RI signal goes from high to low for 0.5sec alternately; hence making the D101 LED blink.

## 6.4. UART2

UART2 of the AirPrime SL Development Kit is connected to the auxiliary RS232 serial link interface of the AirPrime SL Series embedded module at voltage level 1.8V.

The four UART2 signals from the Socket-Up Board could be disconnected by dissociating solder pads J201 to J204. Dissociating the four pads allows for the transmission of undisturbed signals to an external interface board via the external connector (J1100).

By default, UART2 is available on its dedicated connector, J200, with J201 to J204 soldered.

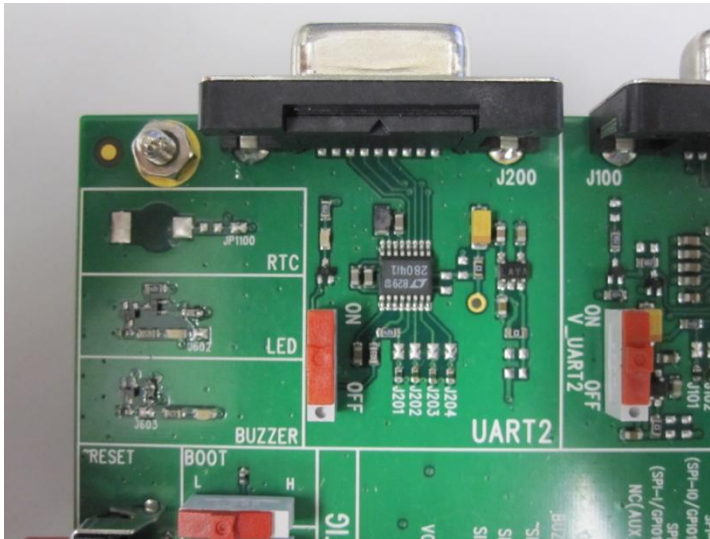


Figure 31. UART2 Connector

### 6.4.1. UART2 Connector (J200)

J200 is a SUB-D 9-pin female connector.

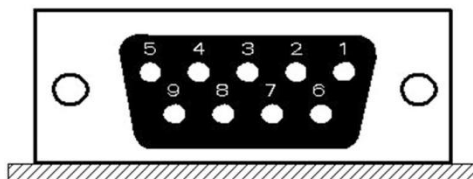


Figure 32. DB-9 Female Connector for UART2

Refer to the following table for the UART2 connector signal pin description.

Table 10. UART2 Connector Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	Not used*	-	-	-
2	CT104 RXD	O	RS232 (V24/V28)	Receive serial data
3	CT103 TXD	I	RS232 (V24/V28)	Transmit serial data
4	Not used*	-	-	-
5	GND			Ground
6	Not used*	-	-	-



Pin Number	Signal Name	I/O	I/O Type	Description
7	CT105 RTS	I	RS232 (V24/V28)	Request To Send
8	CT106 CTS	O	RS232 (V24/V28)	Clear To Send
9	Not used*	-	-	-

\* Only four signals are used on this connector.

### 6.4.2. UART2 Configuration

The AirPrime SL Development Kit acts as a DCE and is connected to a DTE (PC or terminal) with a “straight cable”. There are only 4 signals on the UART2 as shown in the figure below.

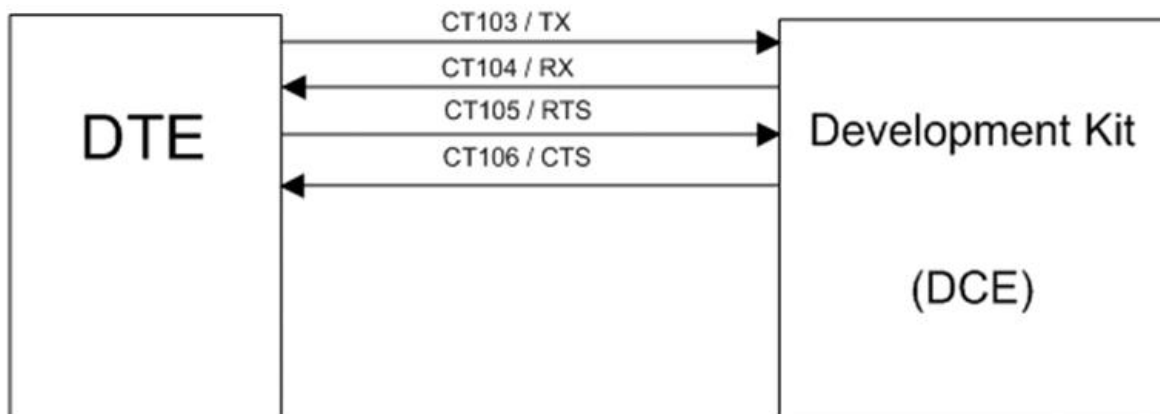


Figure 33. RS232 Auxiliary Serial Link

### 6.4.3. Enabling the UART2 Function (SW200)

The UART2 interface can be enabled by switching SW200 to the “ON” position.

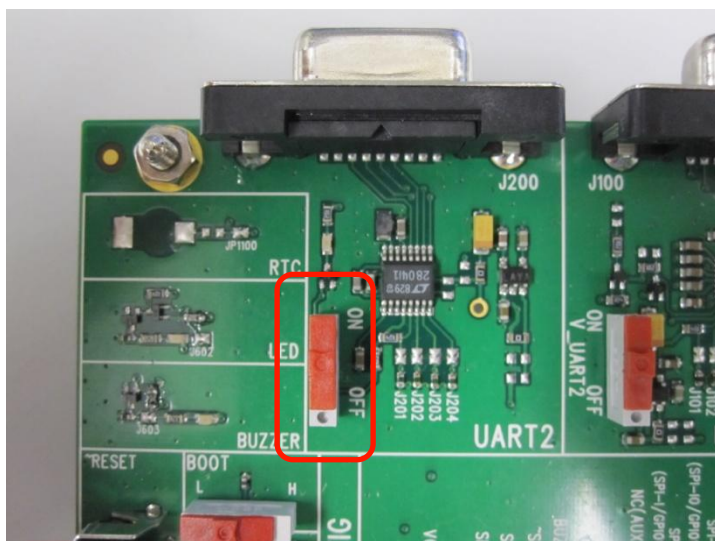


Figure 34. UART2 Switch Configuration

### 6.4.4. LED Signalization for UART2 (D200)

A green LED indicates the UART2 state. When this LED is lit, it indicates that the UART2 interface is available for use.

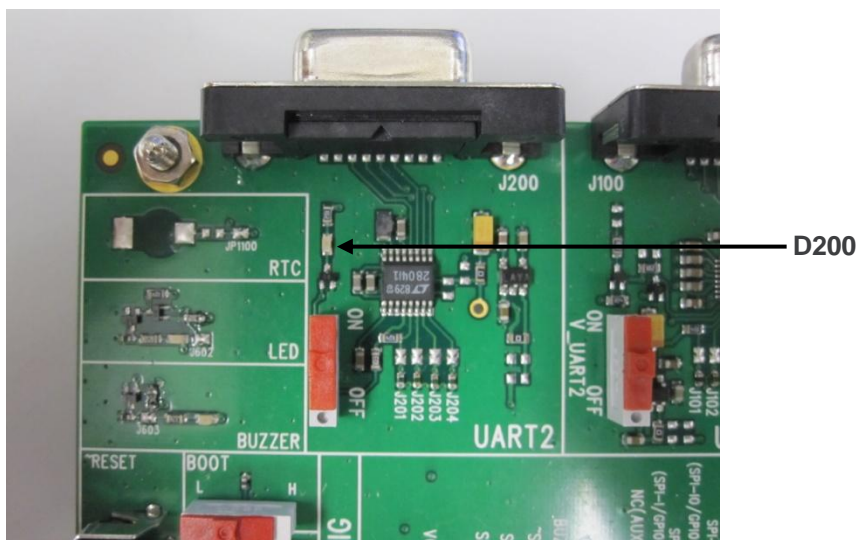


Figure 35. UART2 LED Location (D200)

## 6.5. USB

*Note:* This interface is for use with the AirPrime SL6087 embedded module. For USB connection with AirPrime SL80xx embedded modules, refer to section 6.11 Mini-USB.

The USB interface on the AirPrime SL Development Kit board could be disconnected by dissociating solder pads J301 to J305. Dissociating the five solder pads allows for the transmission of undisturbed signals to an external interface board via the external connector (J1100).

By default, USB is available on its dedicated connector, J300; with J301, J302 and J304 soldered.

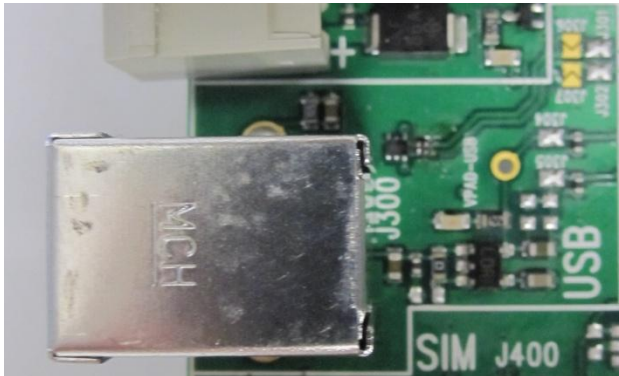


Figure 36. USB Connector

Note that ESD protection is used on the USB signals.

Refer to document [1] AirPrime SL6087 Product Technical Specification and Customer Design Guidelines for more information on the characteristics of the USB interface of the AirPrime SL6087 embedded module.

### 6.5.1. USB Connector (J300)

J300 is a USB B type receptacle.

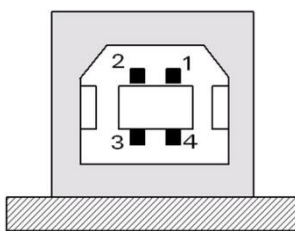


Figure 37. USB B Type Receptacle

Refer to the following table for the USB connector pin description.

Table 11. USB Connector Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	VBUS	I	Power supply	+ 5V DC
2	DM	I/O	Digital	USB negative line
3	DP	I/O	Digital	USB positive line
4	GND		Power supply	Ground
Shell	Shielding			

## 6.5.2. LED Signalization for USB (VPAD-USB)

The VPAD-USB is a green LED that is activated by the presence of VBUS (when plugged into the USB connector).

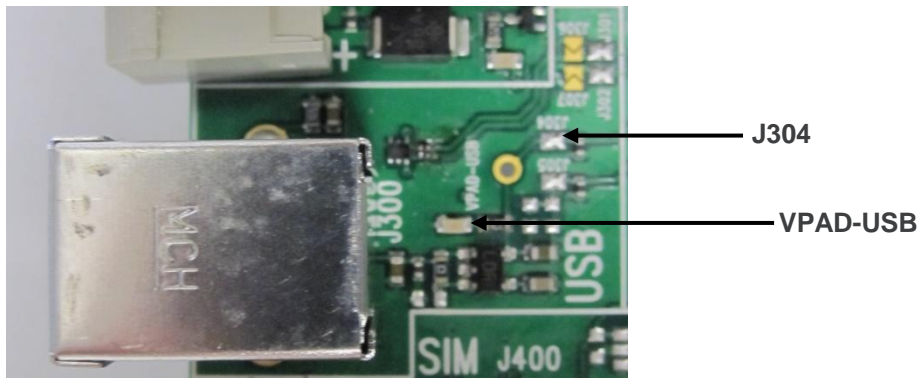


Figure 38. USB LED Location (VPAD-USB)

The power supply for the USB interface in the AirPrime SL Development Kit is provided by the USB connector. If a USB cable is plugged in, the LED and VPAD-USB (regulator, LDO, output) is at 3.3V.

---

**Note:** Solder pad J304 must be soldered to power the USB interface in the AirPrime SL6087 embedded module.

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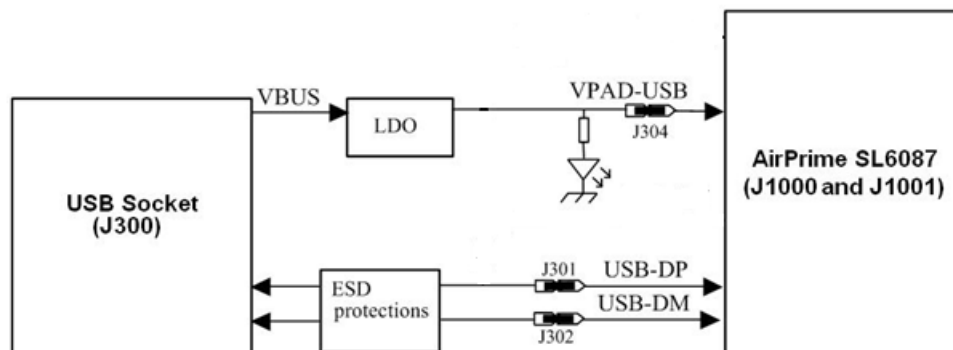


Figure 39. USB Electrical Diagram

## 6.6. Audio

There is one AUDIO interface in the AirPrime SL Series embedded module. Use J700 (AUDIO2) in the AirPrime SL Development Kit to connect to this audio interface.

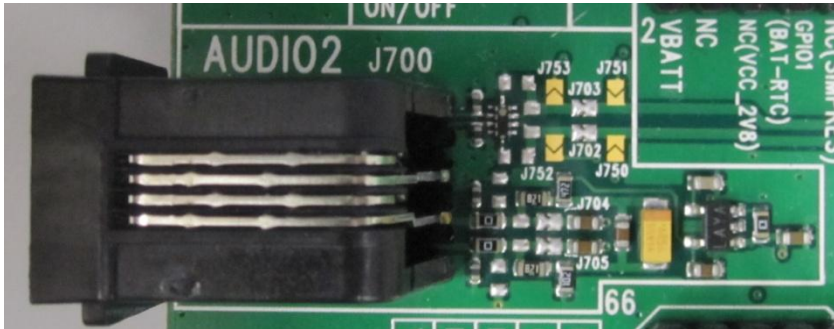


Figure 40. AUDIO Connectors

### 6.6.1. AUDIO2 Connector (J700)

The audio connector could be disconnected from the AirPrime SL Series embedded module when soldering pads J702 to J705 are dissociated. That is, having it unsoldered allows for the transmission of undisturbed signals to an external interface board via the external connector (J1100).

By default, AUDIO signals of the AirPrime SL Series embedded module are available on its dedicated connector J700 (AUDIO2) when J702 to J705 are soldered.

J700 is an RJ9 4-pin connector.

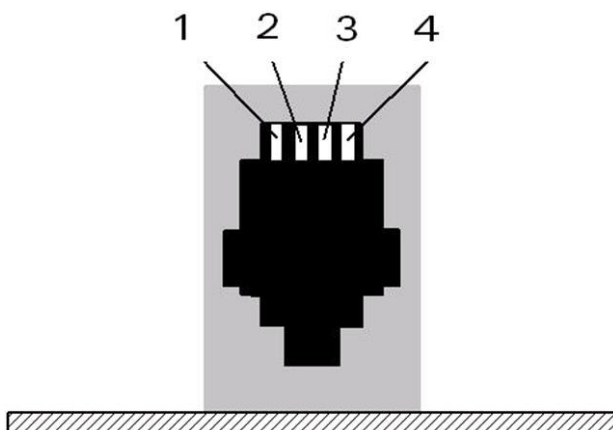


Figure 41. RJ9 4-pin Connector for AUDIO2

Refer to the following table for the AUDIO2 connector signal pin description.

Table 12. AUDIO2 Connector Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	MIC Ground	GND	Analog	Microphone ground
2	HSET_OUTN	O	Analog	Main speaker negative output
3	HSET_OUTP	O	Analog	Main speaker positive output
4	INTMIC_P	I	Analog	Main microphone positive input

Both microphone and speaker signals of the AirPrime SL Series embedded module can be configured in either single-ended or differential mode. By default, both microphone and speaker signals are set to differential mode on the AirPrime SL Development Kit. The following diagrams show how the AirPrime SL Development Kit should be configured for the differential microphone and speaker.

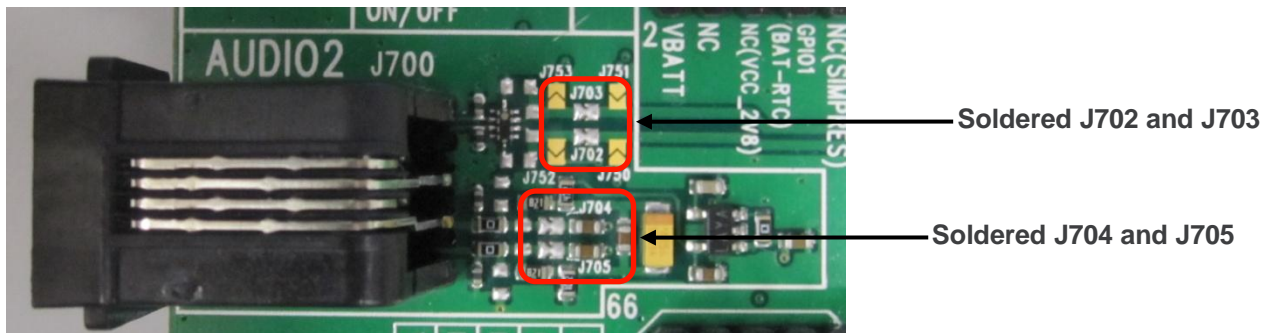


Figure 42. Settings for a Differential Microphone

For a single-ended microphone connection, the settings on the AirPrime SL Development Kit Board should be modified as follows. For more information about differential and single-ended audio configurations, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

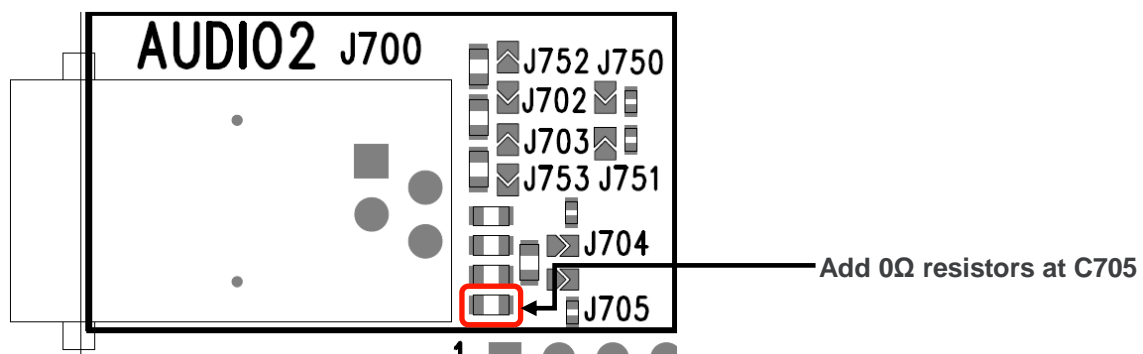


Figure 43. Settings for a Single-Ended Microphone

## 6.7. Flash LED (LED, D602)

The LED location is shown in the following figure.

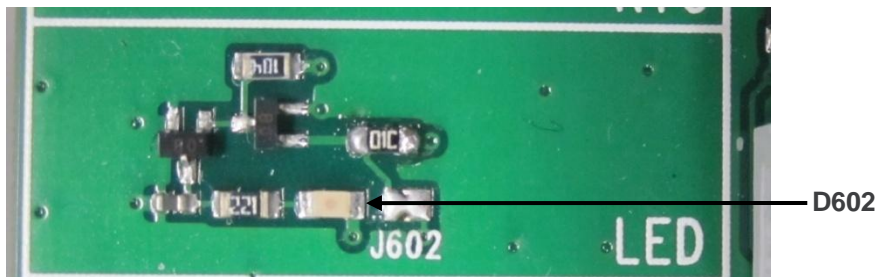


Figure 44. LED Location

The LED indicator, D602, is a green LED that indicates the network status.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

## 6.8. Buzzer LED (BUZZER, D600)

The BUZZER location is shown in the figure below.

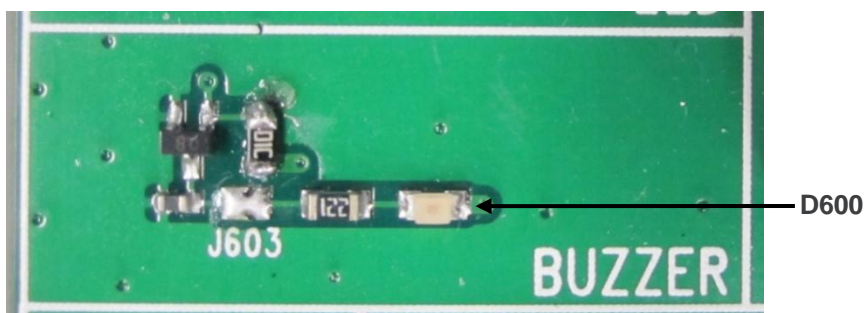


Figure 45. BUZZER Location

The BUZZER indicator, D600, is a green LED that is controlled by the BUZZ-OUT signal of the AirPrime SL Series embedded module.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

## 6.9. Other Interfaces

Other interfaces and signals available on the AirPrime SL Development Kit Board are available on both the test points at the center of the AirPrime SL Development Kit Board, and also on the external board connector J1100. The following sub-sections discuss these additional interfaces and signals.

Refer to sections 3.3 Available Test Ports on the AirPrime SL Development Kit and 6.13 External Board Connector (J1100) for the test point location and the pin description.

For further technical information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

### 6.9.1. Power Supply Function

These outputs (VCC\_1V8 and VCC\_2V8) from the AirPrime SL Series embedded module can be used to connect pull-up resistors. VCC\_2V8 is 2.8V. They must only be used as a reference supply.

### 6.9.2. Backup Battery Function

The AirPrime SL Series embedded module provides an input/output signal, BAT-RTC, for connecting a Real Time Clock power supply. This pin is used as a backup power supply to preserve the date and time when VBATT is switched OFF (no VBATT).

### 6.9.3. Serial Interface

The AirPrime SL Series embedded module may be connected to an LCD module driver through either an SPI bus (3 or 4-wire interface) or an I<sup>2</sup>C bus (2-wire interface).

#### 6.9.3.1. SPI Bus

The AirPrime SL Series embedded module provides one SPI bus with a maximum speed of 13Mb/s in Master mode operation using a 3 or 4-wire interface design.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

#### 6.9.3.2. I<sup>2</sup>C Bus

The AirPrime SL Series embedded module provides an I<sup>2</sup>C bus that includes a clock signal (CLK) and a data signal (SDA) with a maximum speed of 400Kb/s in Master mode operation.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

### 6.9.4. Digital Audio Interface (PCM)

The digital audio interface (PCM) allows connectivity with standard audio peripherals.

For more information, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.



## 6.9.5. ADC Function

The AirPrime SL Series embedded module provides two analog to digital converters, ADC1 and ADC2. These converters are 10-bit resolution ADCs ranging from 0V to 2V.

For more information about this interface, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

### 6.9.5.1. ADC1

This analog input signal can be used to monitor external (application) temperature.

### 6.9.5.2. ADC2

This input may be used for customer specific applications.

## 6.9.6. External Interrupt

The AirPrime SL Series embedded module provides two external interrupt inputs, INT0 and INT1, with differing voltages. INT0 is a 1V8-type input while INT1 is 2V8-type input.

For more information about this interface, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

## 6.10. JTAG

The JTAG interface is used for hardware debugging and product troubleshooting. When using an AirPrime SL6087 embedded module, this interface is connected through the JTAG connector on the AirPrime SL Development Kit; and when using an AirPrime SL80xx embedded module, this interface is available on the Socket-Up Board.

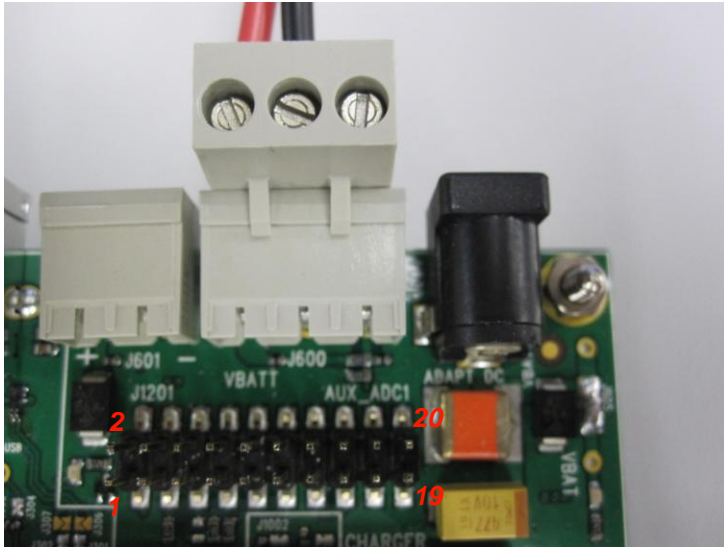


Figure 46. JTAG Connector on the SL Series Development Kit for AirPrime SL6087



Figure 47. JTAG Connector on the Socket-Up Board for AirPrime SL80xx

## 6.10.1. JTAG Connector

The JTAG connectors on both the SL Series Development Kit and on the SL80xx Socket-Up board are of the same type. Refer to the following table for the JTAG connector pin description.

Table 13. JTAG Connector Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	VCC_1V8	O	1V8	Digital Supply
2	VCC_1V8	O	1V8	Digital Supply
3	~TRST	I	1V8	JTAG asynchronous reset
4	GND	-	-	GROUND
5	TDI	I	1V8	JTAG input data
6	GND	-	-	GROUND
7	TMS	I	1V8	JTAG test mode select
8	GND	-	-	GROUND
9	TCK	I	1V8	JTAG scan clock
10	GND	-	-	GROUND
11	RTCK	O	1V8	JTAG return test clock from the ARM JTAG for external debug HW
12	GND	-	-	GROUND
13	TDO	O	1V8	JTAG output data
14	GND	-	-	GROUND
15	~RESET	I/O	Open Drain	Reset Input
16	GND	-	-	GROUND
17	GND	-	-	GROUND
18	GND	-	-	GROUND
19	GND	-	-	GROUND
20	GND	-	-	GROUND

## 6.11. Mini-USB

The mini-USB connector, available on the SL80xx Socket-Up Board, is used for USB connections for the AirPrime SL80xx embedded module.



Figure 48. Mini-USB Connector on the Socket-Up Board for AirPrime SL80xx

## 6.12. Antenna Function

The SMA connector, available on the Socket-Up Board, is used for customer applications.

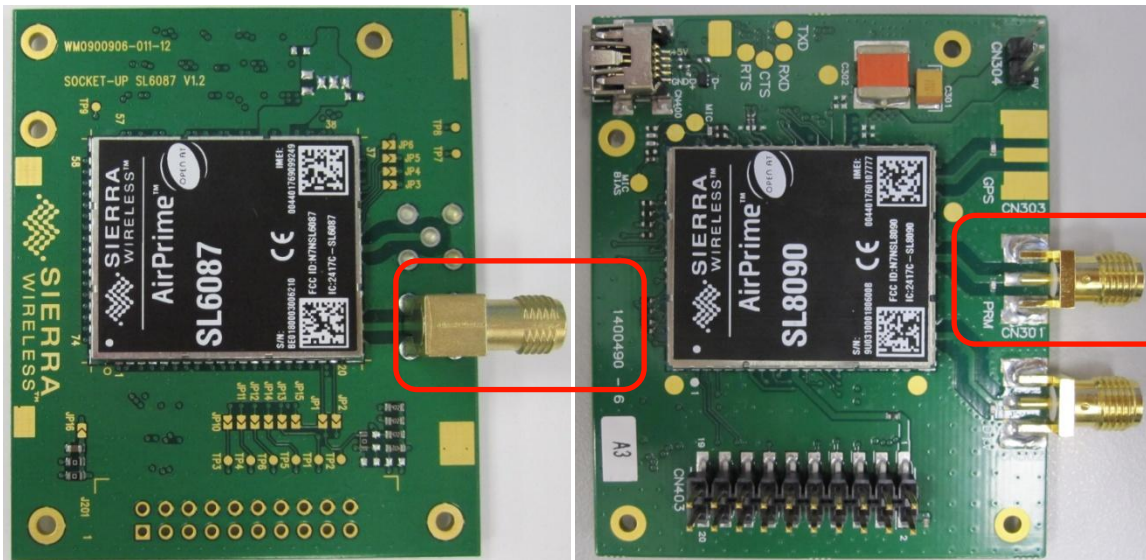


Figure 49. SMA Connector on the Socket-Up Board for AirPrime SL6087 and SL8090

## 6.13. External Board Connector (J1100)

The external board connector is an interface to connect an external test bench for testing or debugging.



Figure 50. External Board Connector

Most of the AirPrime Socket-Up Board signals are connected to the external board connector (J1100) and are available via TP from the center of the AirPrime SL Development Kit Board. Refer to the following table for the pin description of the J1100 connector.

For further information about the AirPrime SL Series embedded module signals and their corresponding multiplexed signals, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

Table 14. External Board Connector (J1100) Pin Description

Pin Number	Signal Name	I/O	I/O Type	Description
1	VBATT	I	Supply	Battery Input
2	VBATT	I	Supply	Battery Input
3	CHG-IN	I	Supply	Current source input
4	CHG-IN	I	Supply	Current source input
5	VCC_1V8	O	Supply	1.8V digital supply output
6	VCC_2V8	O	Supply	2.8V digital supply output
7	GND			Ground
8	BAT_RTC	I/O	Supply	RTC back-up supply
9	SIM-VCC	O	Supply	SIM card supply
10	SIMPRES	I/O	1V8	SIM card detect (Multiplexed)
11	SIM-CLK	O	1V8 / 2V9	SIM clock
12	SIM-IO	I/O	1V8 / 2V9	SIM data
13	~SIM-RST	O	1V8 / 2V9	SIM reset
14	GND			Ground
15	BUZZ-OUT	O	Open drain	PWM / Buzzer output
16	FLASH-LED	O	Open Drain	Network status
17	ON/~OFF	I	VBATT	Power ON / OFF
18	AUX_ADC	I	Analog	Auxiliary ADC3
19	~RESET	I/O	Open Drain	Reset Input
20	BOOT	I	1V8	SW download (with "DWLwin" software)
21	ADC1	I	Analog	ADC input (i.e. battery temperature measurement)

Pin Number	Signal Name	I/O	I/O Type	Description
22	GND			Ground
23	SPI1-I/GPIO19	I/O	2V8	SPI1 Serial input/GPIO30 (Multiplexed)
24	~SPI1-CS/GPIO20	I/O	2V8	SPI1 Enable/ GPIO31/INT5 (Multiplexed)
25	SPI1-IO/GPIO13	I/O	2V8	SPI1 Serial input/output/GPIO29
26	SPI1-CLK/GPIO12	I/O	2V8	SPI1 Serial Clock / GPIO28
27	N/C			Not connected
28	N/C			Not connected
29	N/C			Not connected
30	N/C			Not connected
31	GND			Ground
32	CT104/RXD2	I/O	1V8	Auxiliary RS232 Receive Serial Data / GPIO15 (According to PC view )
33	~CT106/CTS2	I/O	1V8	Auxiliary RS232 Clear To Send / GPIO16 (According to PC view)
34	CT103/TXD2	I/O	1V8	Auxiliary RS232 Transmit Serial Data / GPIO14 / INT6 (According to PC view)
35	GND			Ground
36	~CT105/RTS2	I/O	1V8	Auxiliary RS232 Request To Send / GPIO17 / INT7 (According to PC view)
37	USB-DP	I/O	3V3	Differential data interface positive
38	VPAD-USB	I	3V3	USB power supply
39	USB-DM	I/O	3V3	Differential data interface negative
40	GND			Ground
41	N/C			Not connected
42	N/C			Not connected
43	N/C			Not connected
44	N/C			Not connected
45	N/C			Not connected
46	N/C			Not connected
47	N/C			Not connected
48	N/C			Not connected
49	N/C			Not connected
50	N/C			Not connected
51	GND			Ground
52	GND			Ground
53	MIC2P	I	Analog	Microphone 2 positive input
54	N/C			Not connected
55	N/C			Not connected
56	SPK1N	O	Analog	Speaker 1 negative output
57	MIC1P	I	Analog	Microphone 1 positive input
58	N/C			Not connected

Pin Number	Signal Name	I/O	I/O Type	Description
59	MIC1N	I	Analog	Microphone 1 negative input
60	N/C			Not connected
61	N/C			Not connected
62	N/C			Not connected
63	N/C			Not connected
64	N/C			Not connected
65	N/C			Not connected
66	VBATT	I	Supply	Battery Input
67	VBATT	I	Supply	Battery Input
68	CT103/TXD1	I/O	2V8	Main RS232 Transmit Serial Data / GPIO36 (According to PC view)
69	~CT125/RI1	I/O	2V8	Main RS232 Ring indicator / GPIO42 (According to PC view)
70	~CT106/CTS1	I/O	2V8	Main RS232 Clear To Send / GPIO39 (According to PC view)
71	CT104/RXD1	I/O	2V8	Main RS232 Receive Serial Data / GPIO37 (According to PC view)
72	~CT105/RTS1	I/O	2V8	Main RS232 Request To Send / GPIO38 (According to PC view)
73	~CT109/DCD1	I/O	2V8	Main RS232 Data Carrier Detect / GPIO43 (According to PC view)
74	~CT108/DTR1	I/O	2V8	Main RS232 Data Terminal Ready / GPIO41 (According to PC view)
75	~CT107/DSR1	I/O	2V8	Main RS232 Data Set Ready / GPIO40 (According to PC view)
76	GND			Ground
77	GND			Ground
78	N/C			Not connected
79	N/C			Not connected
80	N/C			Not connected
81	N/C			Not connected
82	N/C			Not connected
83	N/C			Not connected
84	N/C			Not connected
85	N/C			Not connected
86	N/C			Not connected
87	N/C			Not connected
88	N/C			Not connected
89	N/C			Not connected
90	N/C			Not connected
91	N/C			Not connected
92	TDI			Not available
93	NTRS			Not available
94	TMS			Not available
95	TDO			Not available

Pin Number	Signal Name	I/O	I/O Type	Description
96	RTCK			Not available
97	TCK			Not available
98	GND			GROUND
99	GND			GROUND
100	SCL/GPIO1	I/O	Open drain	Serial Clock / GPIO26
101	SDA/GPIO2	I/O	Open drain	Serial Data / GPIO27
102	GND			GROUND
103	GND			GROUND
104	PCM-CLK	O	1V8	Data clock
105	PCM-SYNC	O	1V8	Frame synchronization 8KHz
106	PCM-OUT	O	1V8	Data output
107	PCM-IN	I	1V8	Data input
108	GPIO24	I/O	2V8	GPIO24
109	GND			GROUND
110	INT0/GPIO3	I/O	1V8	INT0 / GPIO3 / A26
111	N/C			Not connected
112	GPIO23	I/O	2V8	GPIO23
113	GPIO22	I/O	2V8	GPIO22
114	N/C			Not connected
115	N/C			Not connected
116	GPIO21	I/O	2V8	GPIO21
117	INT1/GPIO25	I/O	2V8	Interruption 1 / GPIO25
118	GPIO0	I/O	2V8	GPIO44
119	N/C			Not connected
120	N/C			Not connected
121	N/C			Not connected
122	N/C			Not connected
123	N/C			Not connected
124	N/C			Not connected
125	N/C			Not connected
126	N/C			Not connected
127	N/C			Not connected
128	N/C			Not connected
129	N/C			Not connected
130	N/C			Not connected



## 7. Control Functions

This section discusses the control functions available in the AirPrime SL Development Kit.

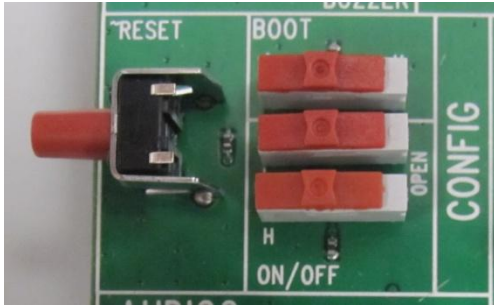


Figure 51. Control Functions

### 7.1. ON/OFF

When the AirPrime SL Development Kit is connected to an external power supply (via J600 or J604) and SW600 is set to the “H” position, a HIGH level signal is sent to the AirPrime SL Series embedded module through VBATT.

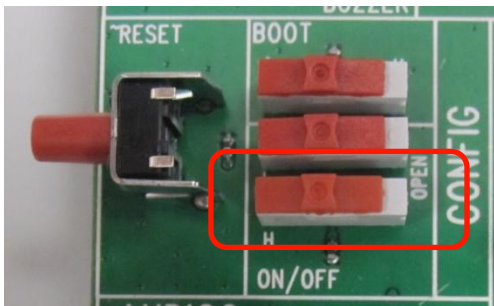


Figure 52. SW600 in the “H” Position

Switch SW601 is used together with switch SW600 for the ON or OFF control of the AirPrime SL Series embedded module. This switch should be set to the “L” position.

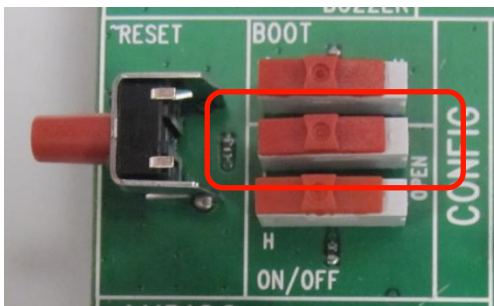


Figure 53. Switch SW601 in the “L” Position

Refer to the following table for the configuration of switches SW600 and SW601 and the corresponding ON/OFF state of the embedded module.

**Table 15. SW600 and SW601 Configuration**

<b>SW600 State</b>	<b>SW601 State</b>	<b>ON/OFF State</b>
H	Open	H
H	L	H
Open	L	L
Open	Open	Open

For more information about the ON/~OFF signal, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

## 7.2. ~RESET

The ~RESET button starts a general reset when it is pushed.

---

**Caution:** *A software reset is preferred to a hardware reset.*

---

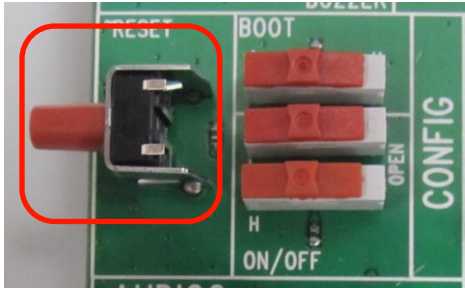


Figure 54. ~RESET Button

For more information about this signal, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

## 7.3. BOOT

The BOOT switch is only used when downloading new software to the AirPrime SL Series embedded module via UART1 using specific download software provided by Sierra Wireless. This switch is set to the “OFF” position by default.

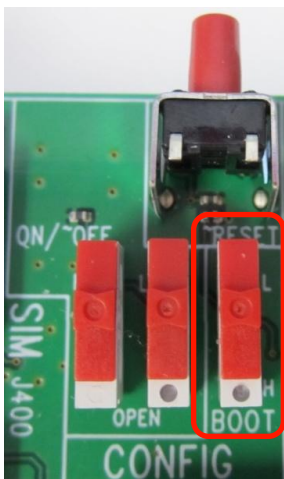


Figure 55. BOOT Switch in the “OFF” Position

Refer to the following table for the BOOT signal configuration.

Table 16. BOOT Configuration

Mode	BOOT	ON/~OFF
Normal	OFF	ON*
Backup download with specific download software	ON	ON

\* The BOOT switch can be switched to the “OFF” position within two seconds if the ON/~OFF signal is to be driven by software (via the AT+CPOF AT command). For more information about this AT command, refer to document [2] Firmware 7.4b AT Commands Manual (Sierra Wireless Software Suite 2.32).

## 8. ESD Protections

External ESD protections are available on the AirPrime SL Development Kit for the following signals:

- SIM interface signals:
  - SIM-VCC
  - SIM-IO
  - SIM-CLK
  - SIM-RST
- AUDIO2
- USB
  - USB-DP
  - USB-DM

Other interface signals protected on the AirPrime SL Series embedded module are as follows:

- UART1 signals with the ADM3307 transceiver
- UART2 signals with the LTC2804 transceiver

---

**Caution:** *As the test points at the center of the AirPrime SL Development Kit are not protected against ESD discharge and they are directly connected to the signal pins of the AirPrime SL Series embedded module, users must be careful when using these TP signals.*

---



## 9. Current Consumption Measurement

To measure the current consumption of the AirPrime SL Series embedded module, configure the AirPrime SL Development Kit as shown in the following sub-sections.

---

**Caution:** *Before making any of the adjustments below, ensure that the AirPrime SL Development Kit is disconnected from the power supply.*

---

Note that with the configuration described in the following sub-sections, the consumption current from VBATT is only\* that of the AirPrime SL Series embedded module soldered-down. For more information about the AirPrime SL Series embedded module and its current consumption, refer to the Product Technical Specification and Customer Design Guideline of the corresponding AirPrime SL Series embedded module.

---

**Note:** \* *Subtract a quiescent current of 50 $\mu$ A from T100.*

---

Also, make sure to take note of the following additional points:

- T100 used for UART1 enable will affect power consumption on 4V (VBATT) by an additional 50 $\mu$ A.
- C600 and D604 connected on 4V (VBATT) may affect power consumption on 4V (VBATT). Disconnect these 2 components if necessary.
- Flash LED (D602) and can affect power consumption and can be disconnected by opening jumper J602.

### 9.1. AirPrime SL Development Kit Board

Configure the AirPrime SL Development Kit Board as follows:

- Around the Power Supply area:
  - Unsolder jumper J605 to disconnect VBATT and VBAT.
- Around the UART2 area:
  - Disconnect UART2 from the AirPrime Series embedded module by dissociating J201 to J204.
- Around BAT-TEMP (VBATT area):
  - Remove R600, D603 and D604 in order to eliminate the current drawn by the application circuit on the AirPrime SL Development Kit.
  - Dissociate J605 in order to separate the power supplies of the AirPrime SL Development Kit and the AirPrime SL Series embedded module.
  - Connect a 4V external power supply to the test point TP602 (“VBAT”) and ground.

---

**Note:** *The current from J600 is supplied to the AirPrime SL Series embedded module; while the current from TP602 is supplied to the AirPrime SL Development Kit.*

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- Around the USB area:
  - Disconnect USB from the AirPrime SL Series embedded module by dissociating J301, J302, J303, J304 and J305.

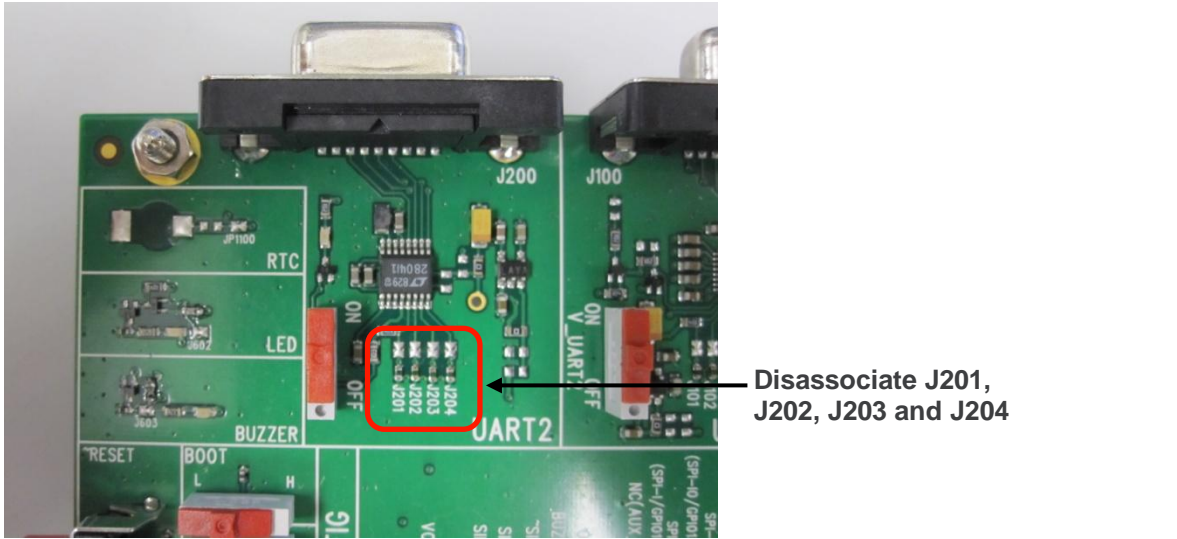


Figure 56. Configuration of UART2 for Current Consumption Measurement

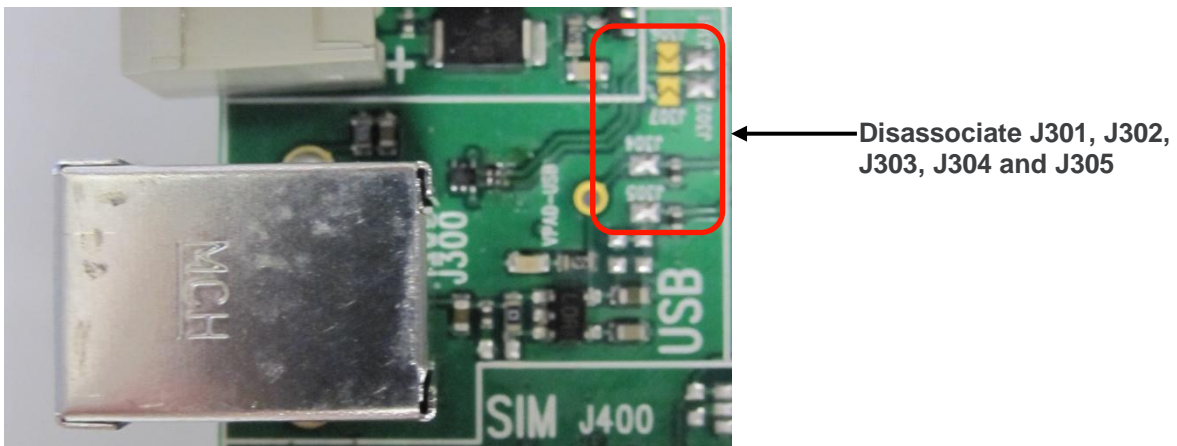


Figure 57. Configuration of USB for Current Consumption Measurement

## 9.2. Socket-Up Board

Keep the default settings on the AirPrime SL6087 Socket-Up Board.

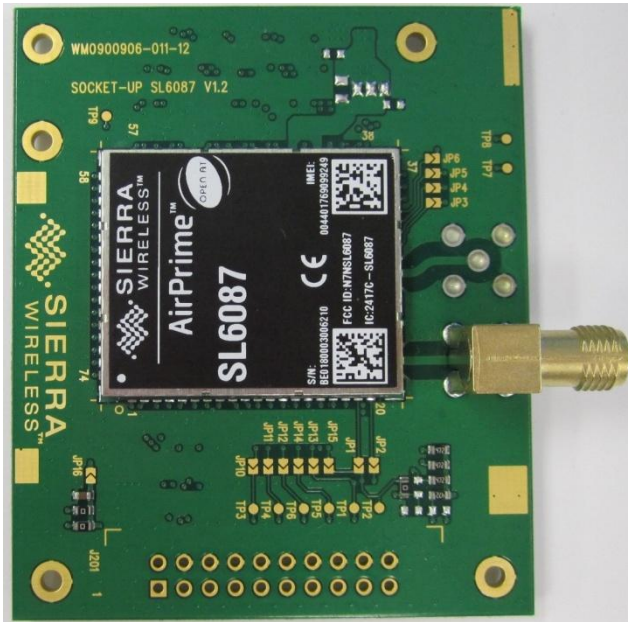


Figure 58. Configuration of the AirPrime SL6087 Socket-Up Board for Current Consumption Measurement



Figure 59. Configuration of the AirPrime SL80xx Socket-Up Board for Current Consumption Measurement



## 10. Reference Documents

- [1] AirPrime SL6087 Product Technical Specification and Customer Design Guidelines  
Reference: WA\_DEV\_SL6087\_PTS\_001
- [2] Firmware 7.4b AT Commands Manual (Sierra Wireless Software Suite 2.32)  
Reference: WM\_DEV\_OAT\_UGD\_079
- [3] AirPrime WMP100 Development Kit User Guide  
Reference: WM\_DEV\_WUP\_UGD\_001





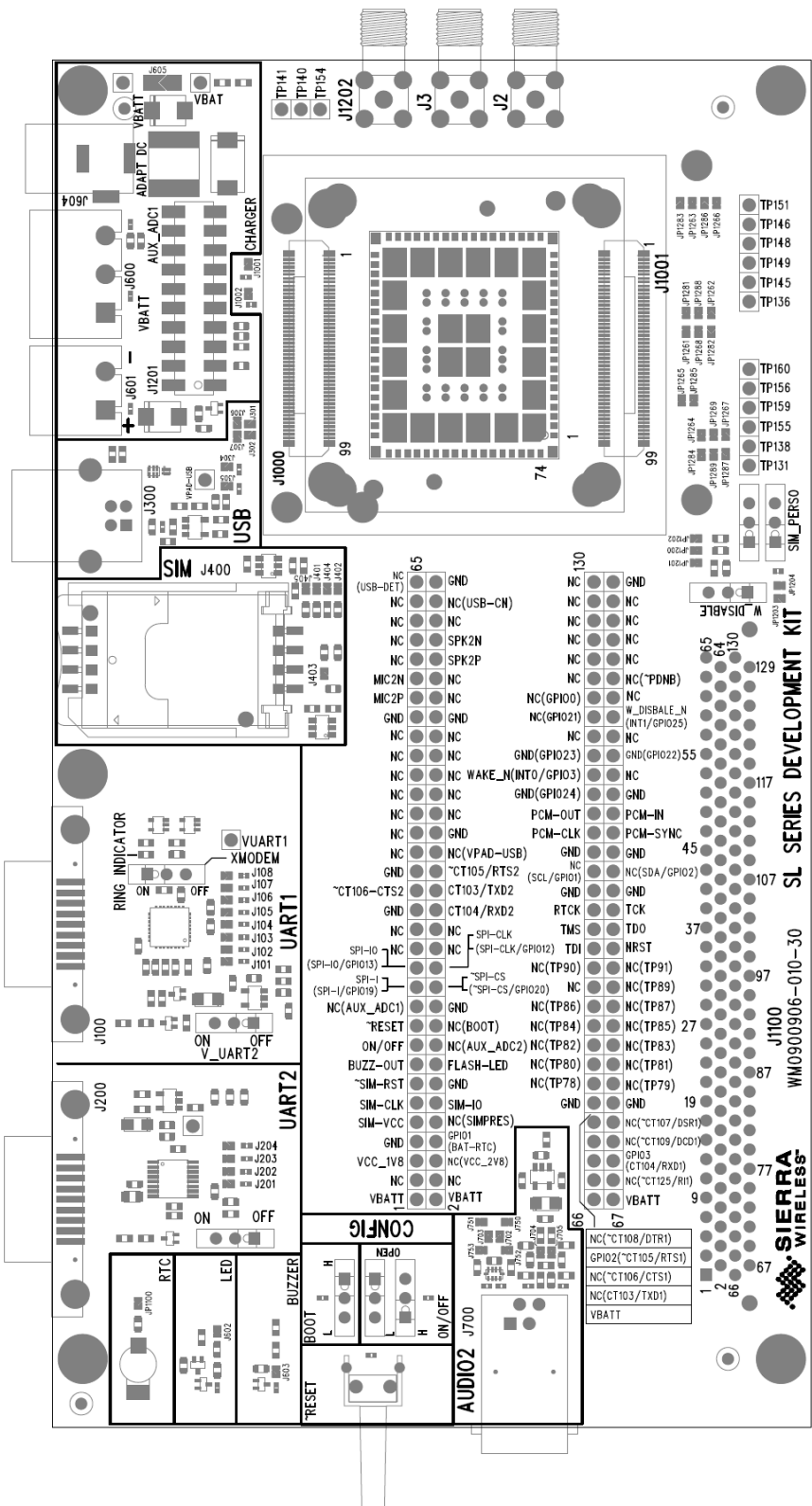
## 11. List of Abbreviations

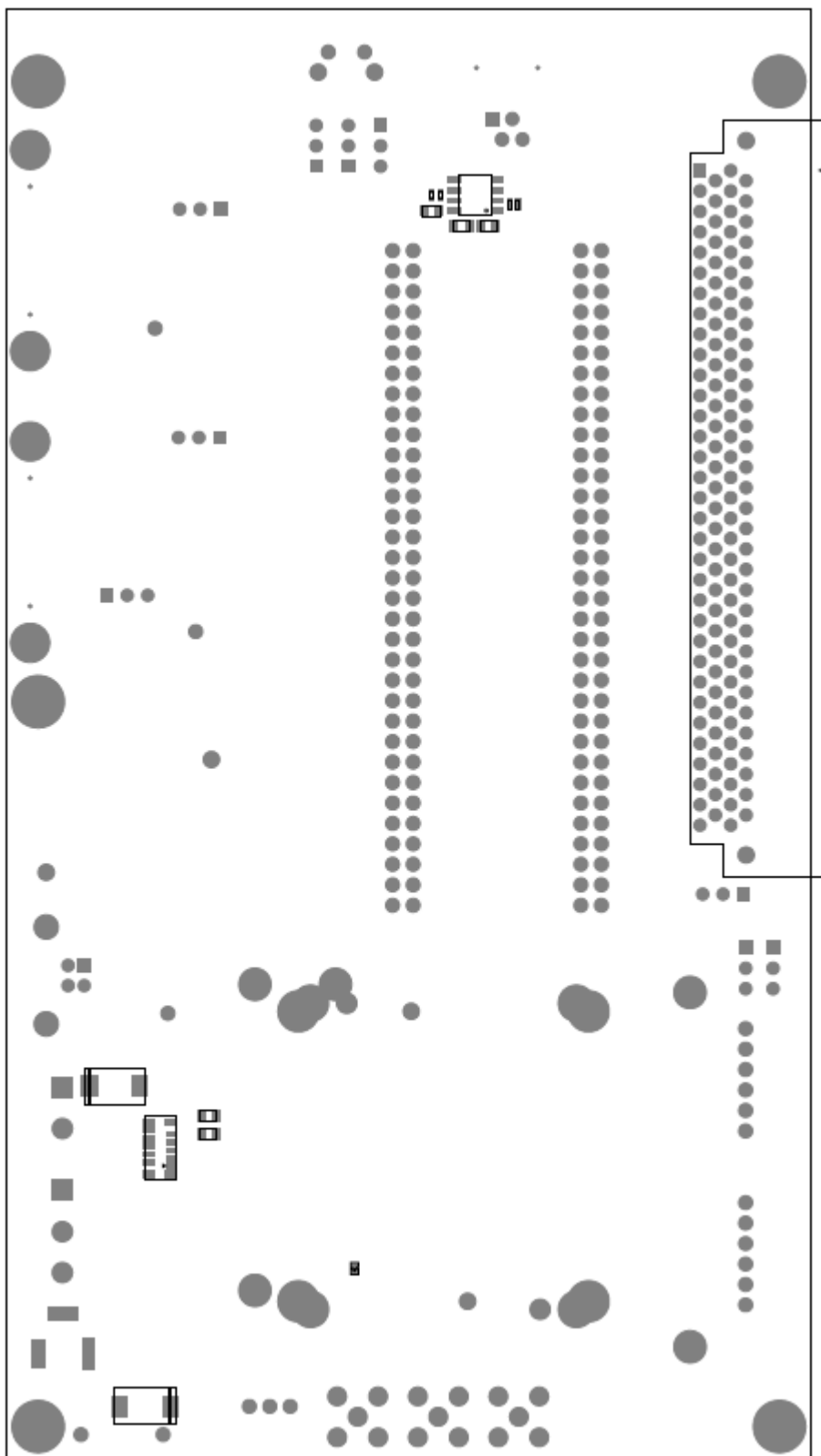
Abbreviation	Definition
ADC	Analog to Digital Converter
AUX	AUXiliary
CLK	CLock
CPU	Central Process Unit
CTS	Clear To Send
DAC	Digital to Analog Converter
DC	Direct Current
DCD	Data Carrier Detect
DCE	Data Communication Equipment
DSR	Data Set Ready
DTE	Data Terminal Equipment
DTR	Data Terminal Ready
ESD	ElectroStatic Discharges
GND	GrouND
GPI	General Purpose Input
GPIO	General Purpose Input Output
GPO	General Purpose Output
IIC (I2C)	Inter IC Control bus
I/O	Input / Output
MIC	MICrophone
PC	Personal Computer
PCB	Printed Circuit Board.
PCM	Pulse Code Modulation
PWM	Pulse Width Modulation
RF	Radio Frequency
RI	Ring Indicator
RTC	Real Time Clock
RTS	Request To Send
RXD	Receive Data
SIM	Subscriber Identity Module
SPI	Serial Peripheral Interface
SPK	SPEaKer
TP	Test Point
TXD	Transmit Data
UART	Universal Asynchronous Receiver-Transmitter
USB	Universal Serial Bus

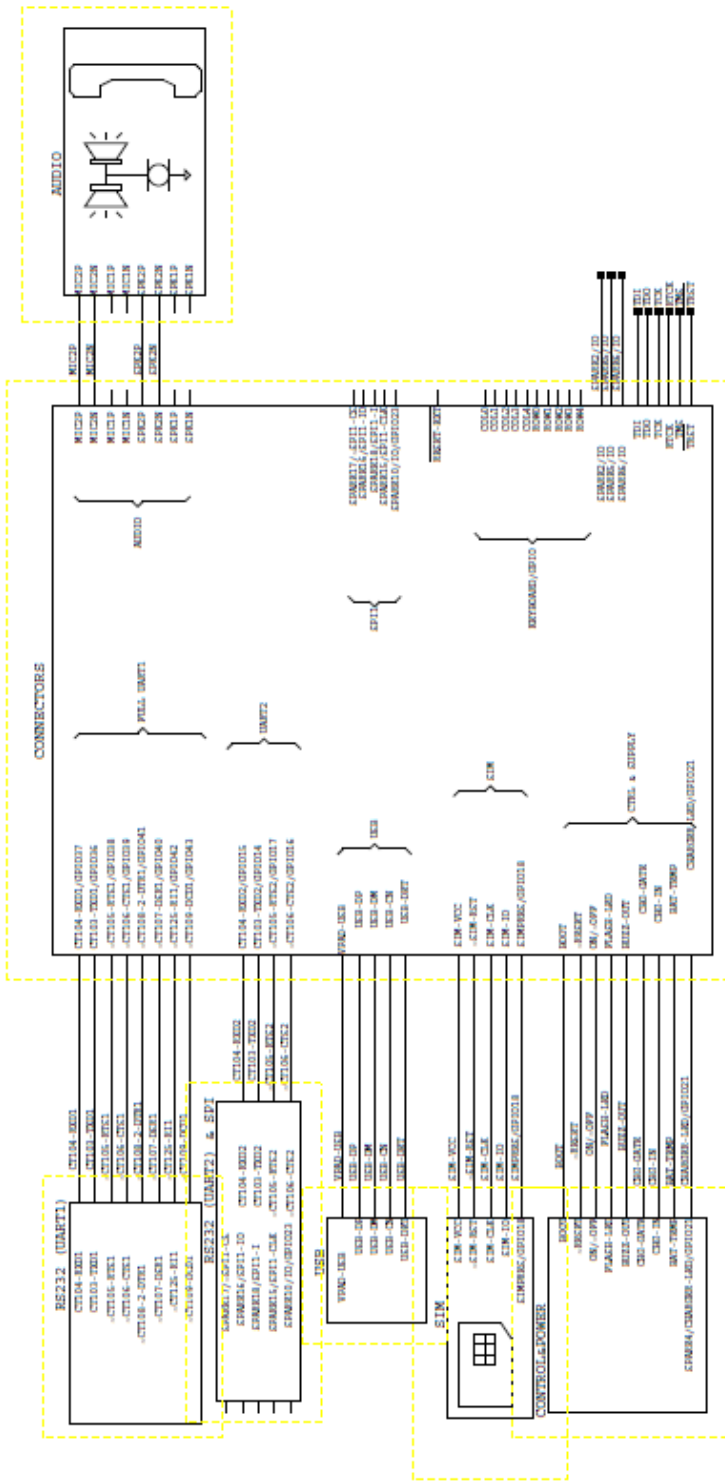
## 12. Appendix

This section contains schematic diagrams for the AirPrime SL Development Kit and Socket-Up Boards.

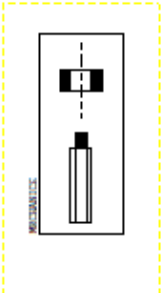
# 12.1. AirPrime SL Development Kit

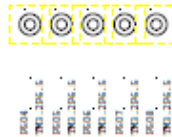
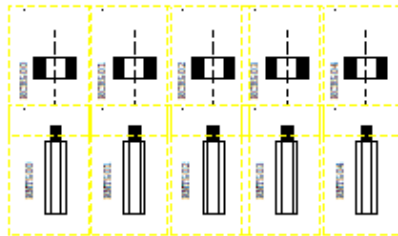
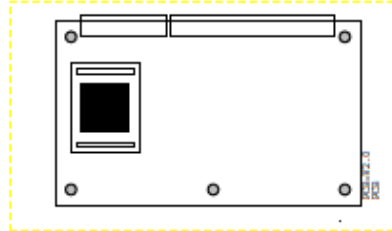




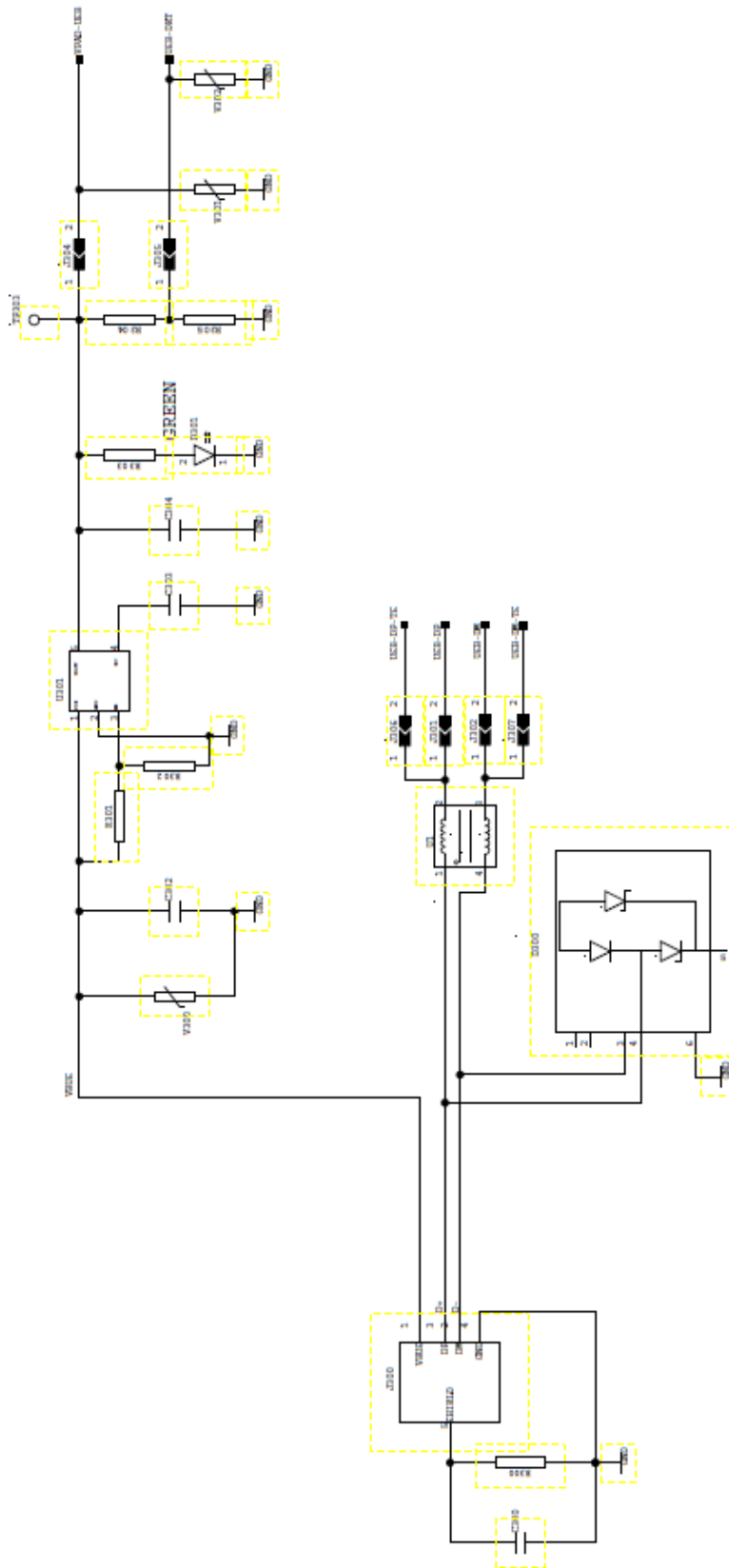


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Revisão No:	3.0	Date:	07/12



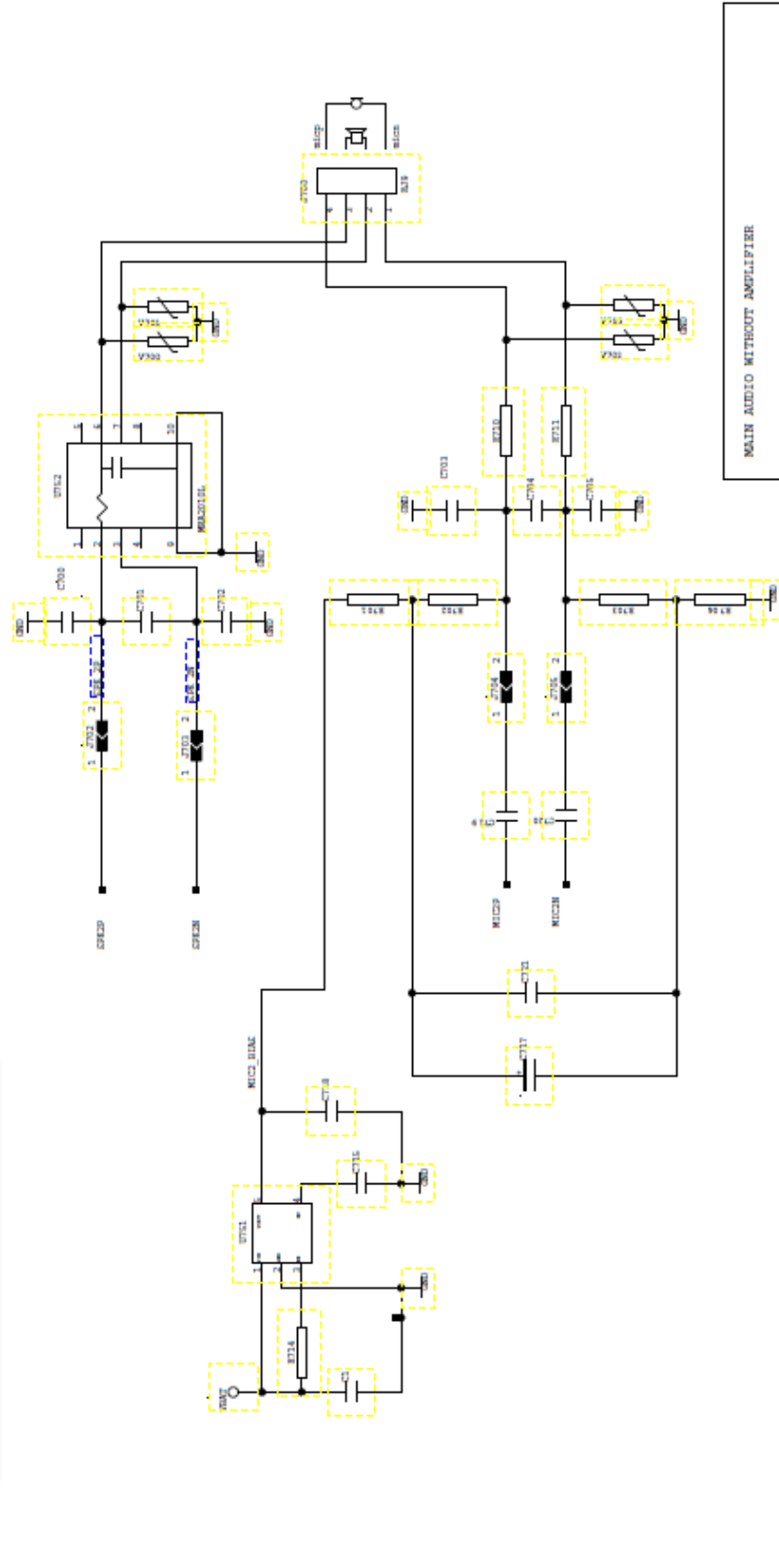


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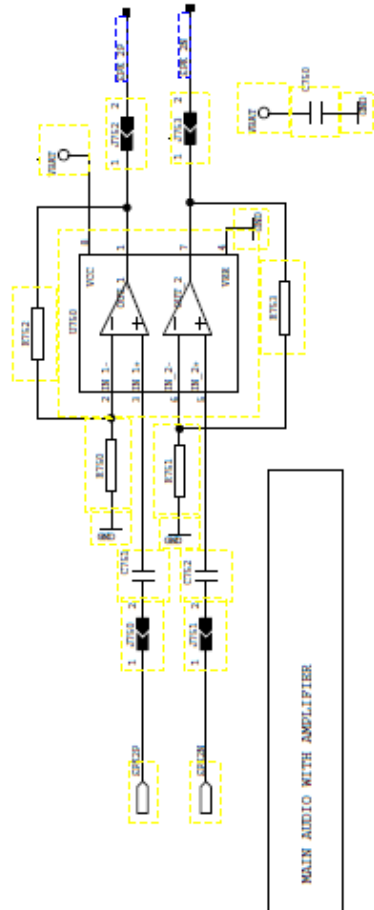


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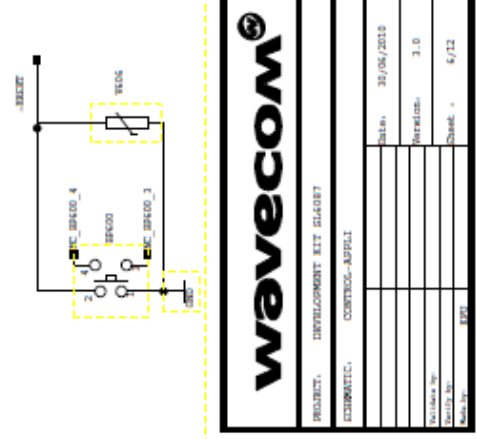
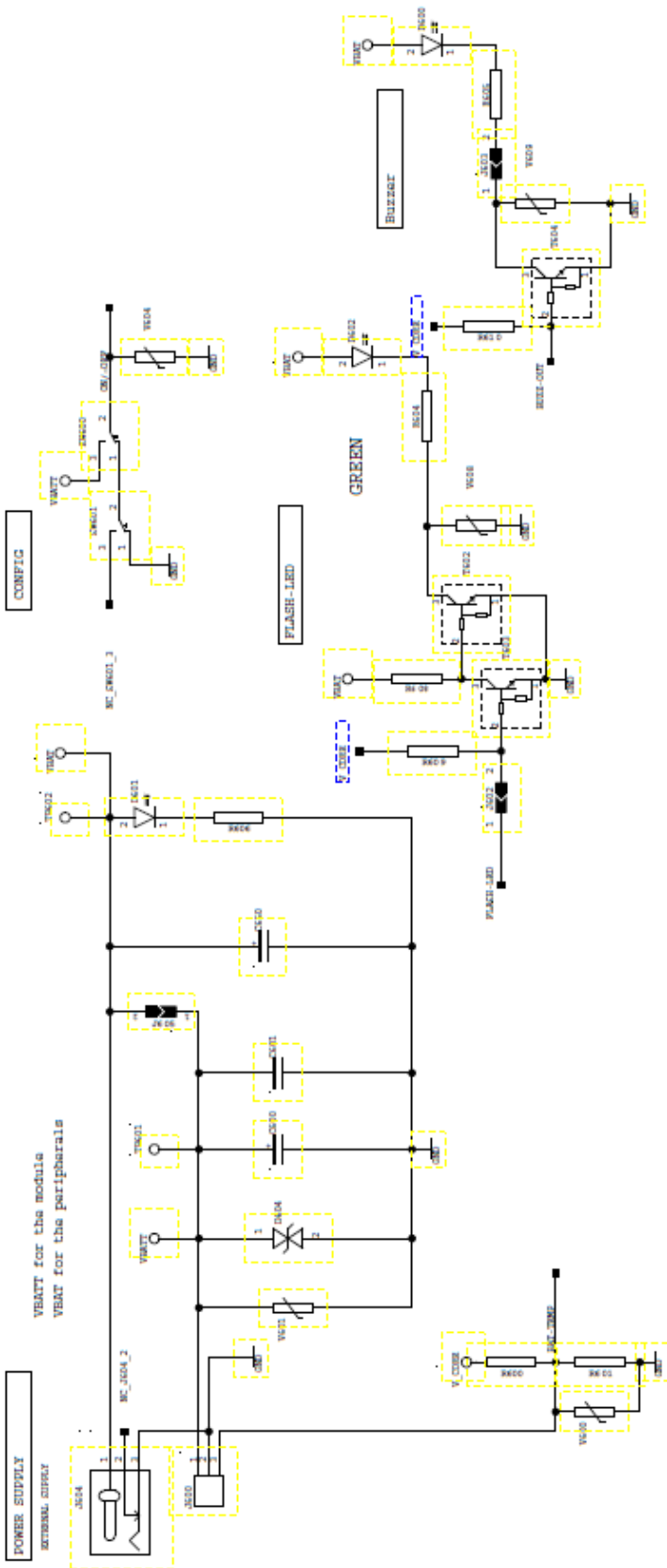
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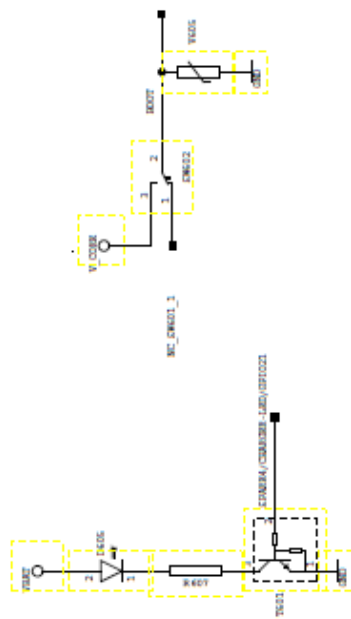
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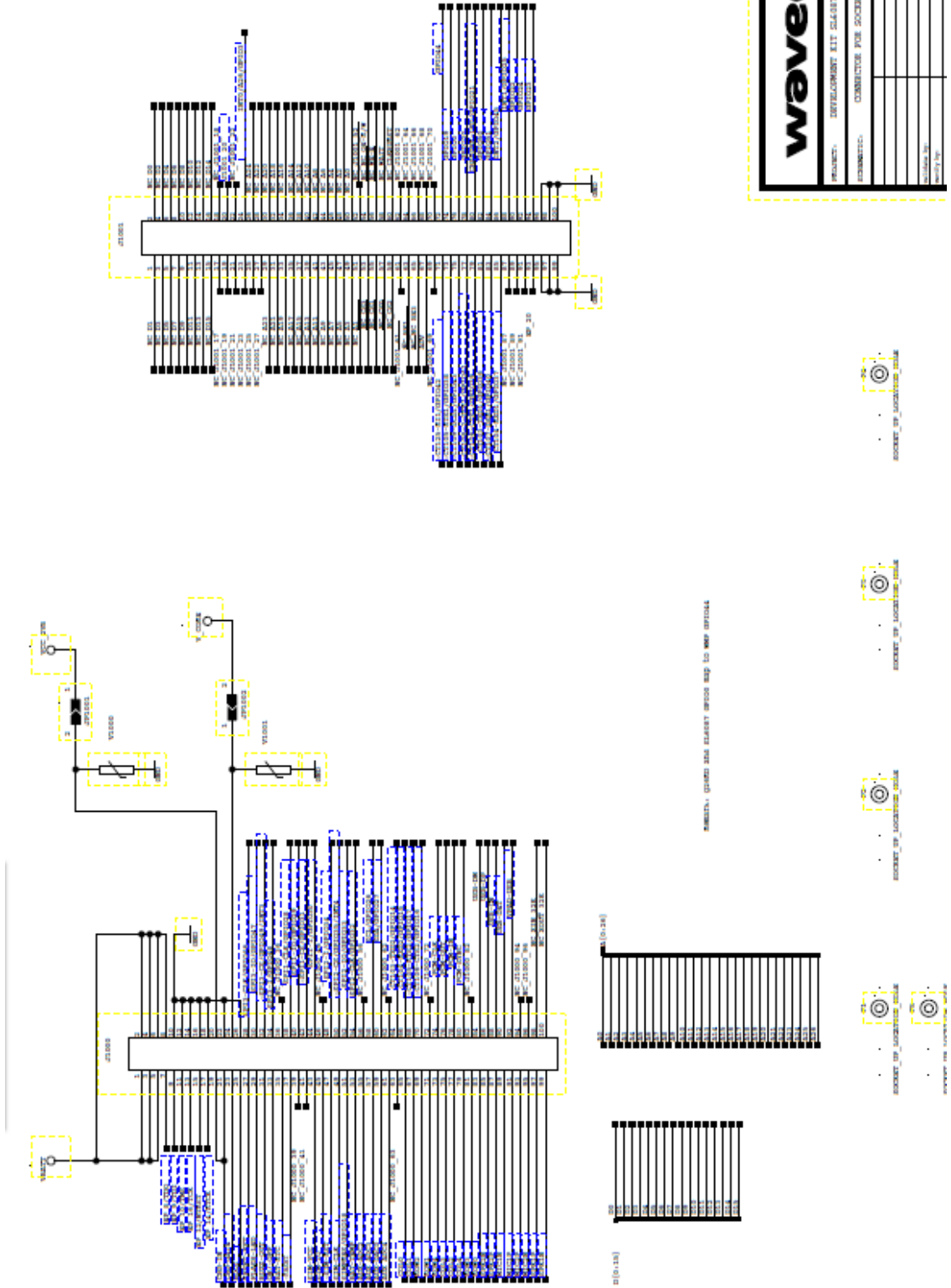


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PROJECT: DEVELOPMENT KIT SLASH

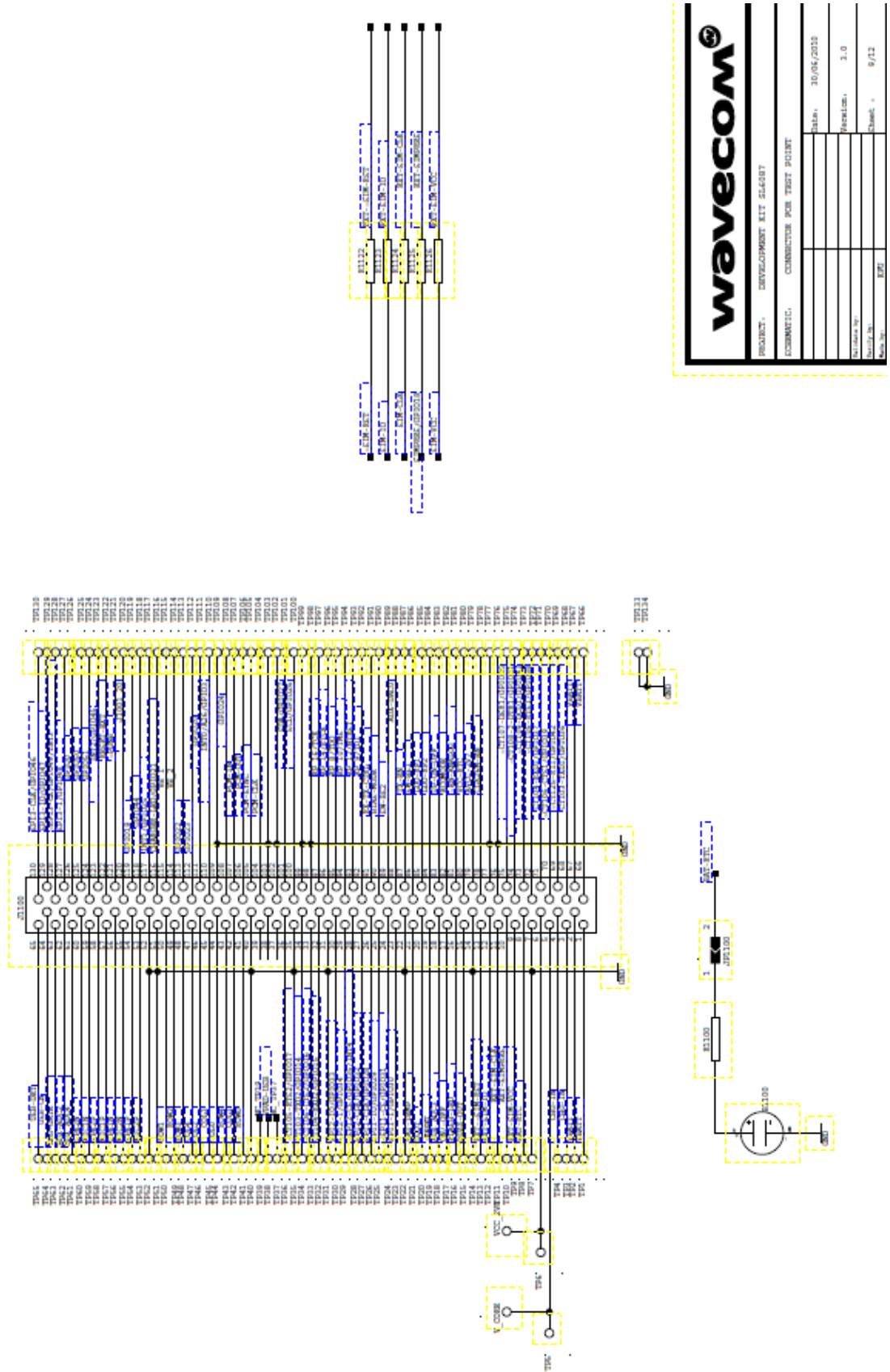
REVISION: CONNECTOR FOR SOCKET IP

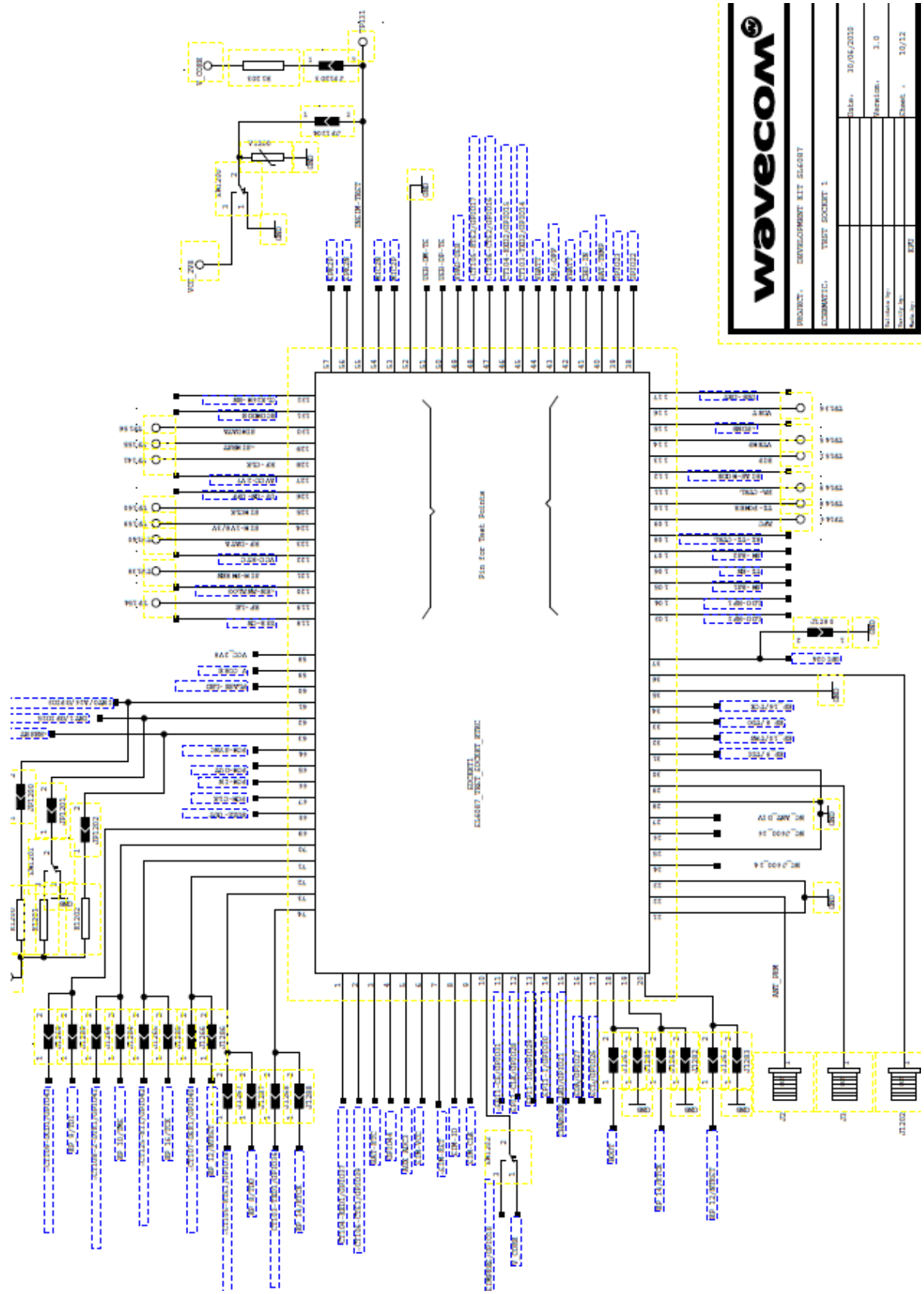
DATE: 30/04/2010

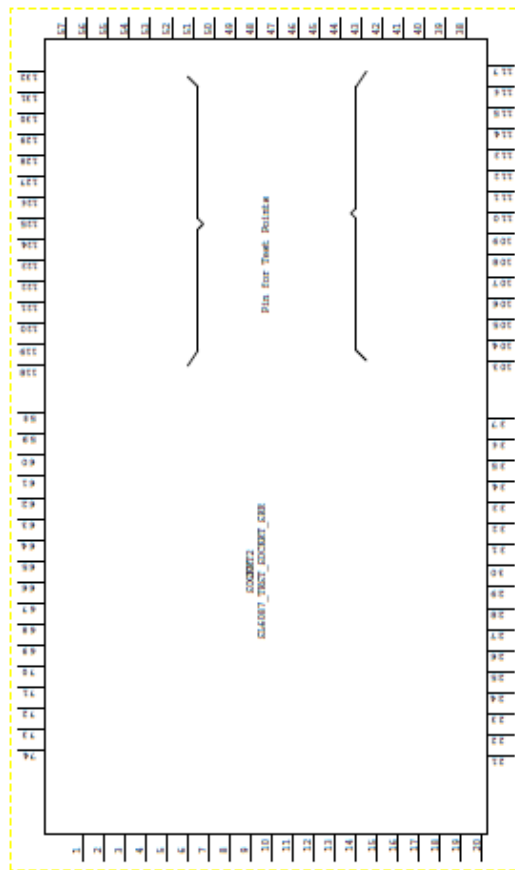
VERSION: 3.2

REVISED BY: [Name]

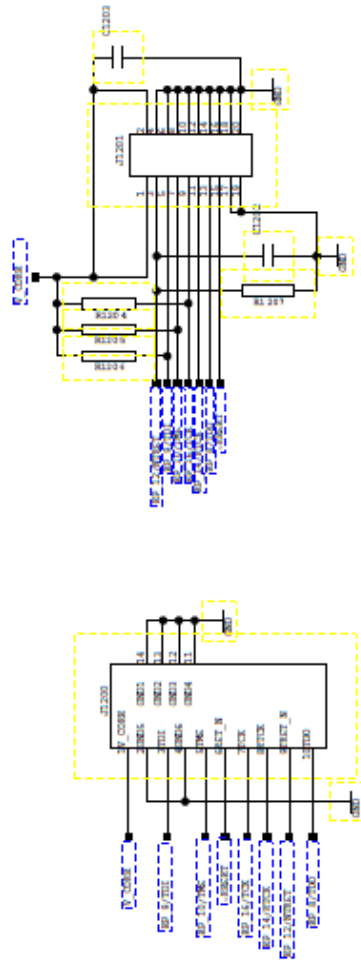
REVISED DATE: [Date]








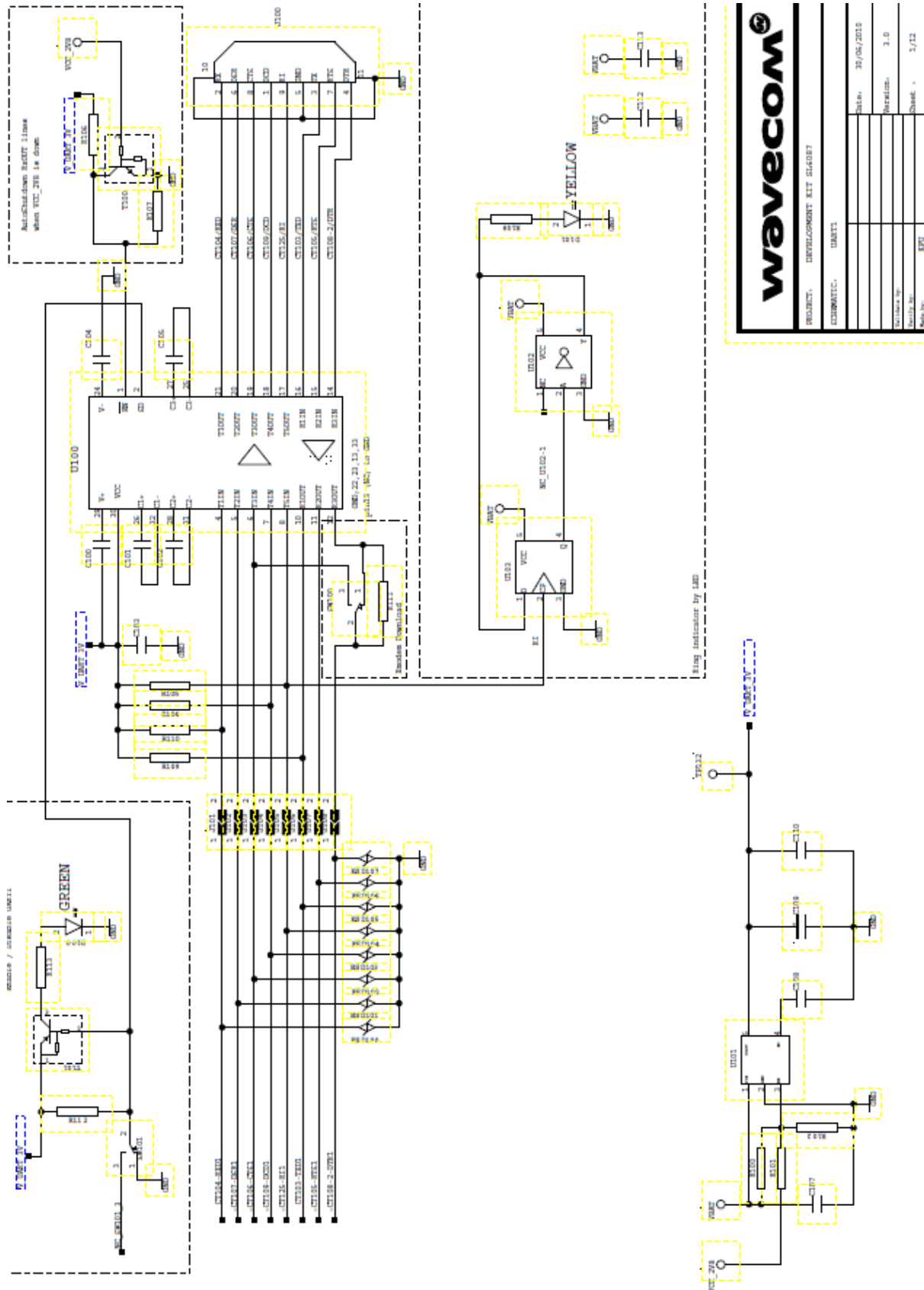
<b>wavecom</b>	
PROJECT: DEVELOPMENT KIT SL6087	
COMMIT: TEST SOCKET 2	
	DATE: 30/04/2010
	VERSION: 1.0
	DATE: 11/12
	REV: 1.0





PROJECT: DEVELOPMENT KIT SL687  
 DOCUMENT: CONNECTOR FOR J71201

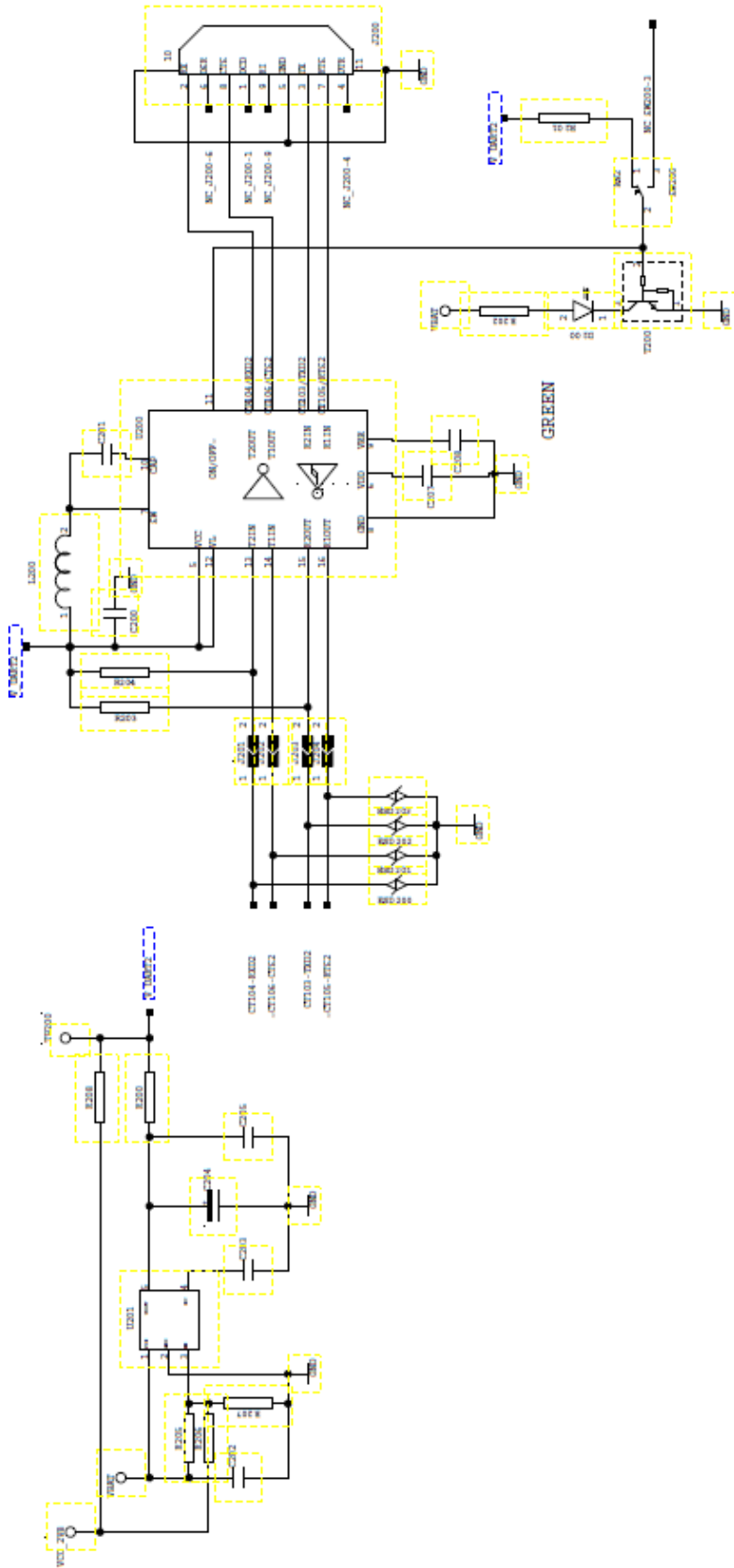
Date:	30/06/2010
Version:	3.0
Created By:	
Checked By:	
Rev. No.:	001
Change:	12/12




**wavecom**

PROJECT: DEVELOPMENT KIT SL6087  
 SCHEMATIC: U84T1

Version No:	3.0
Release No:	10/05/2010
Rev:	2/12
Author:	EPT





**PROJECT:** DEVELOPMENT KIT SL087

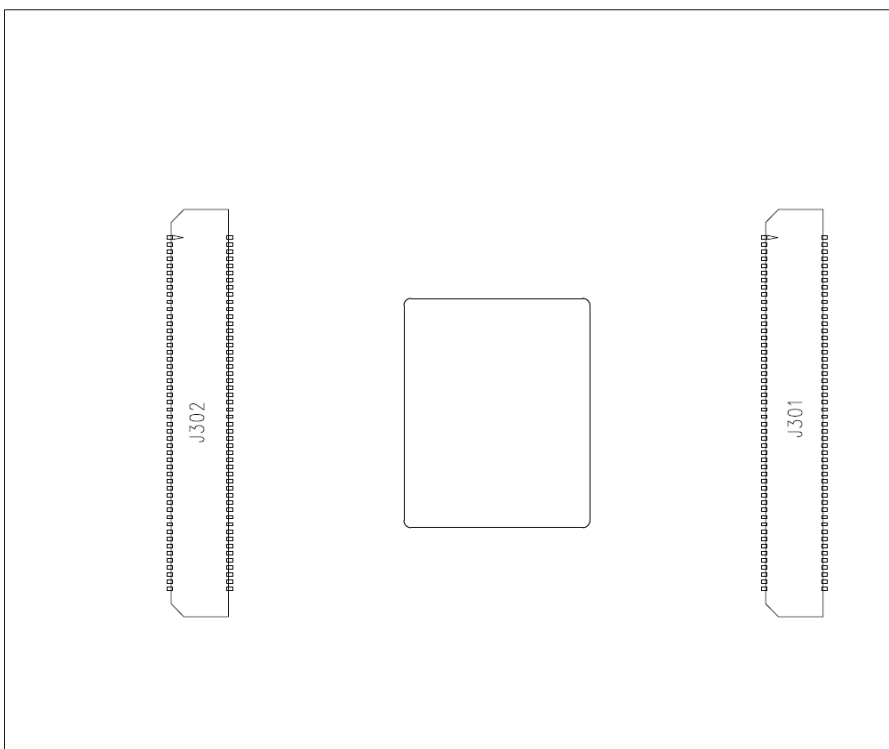
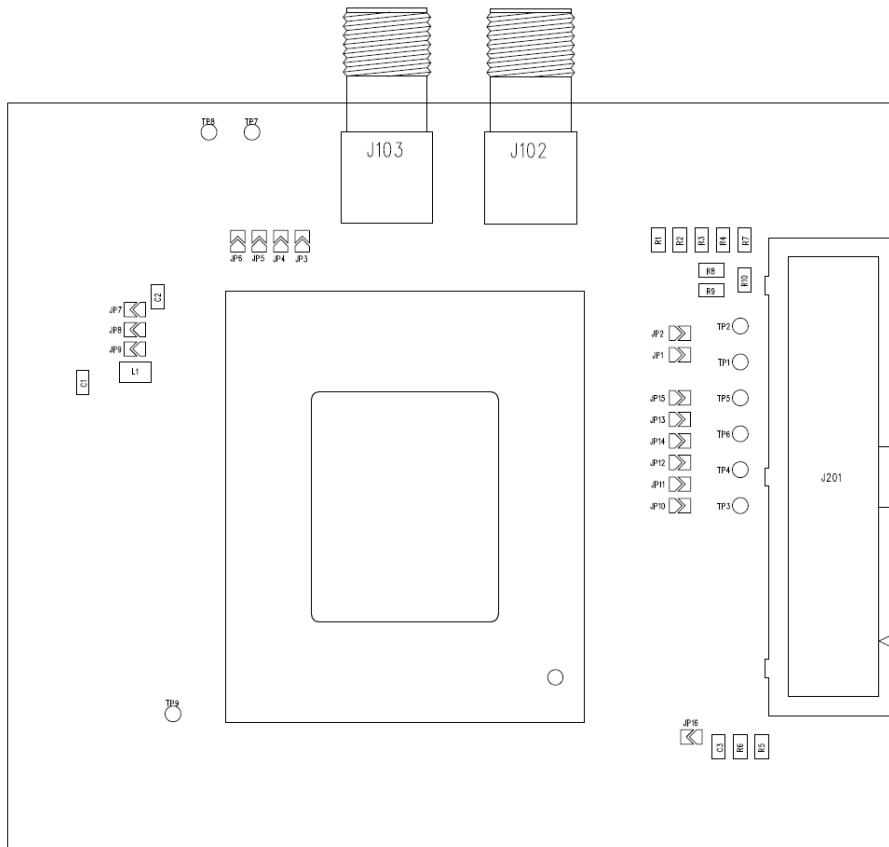
**DATE:** 10/05/2010

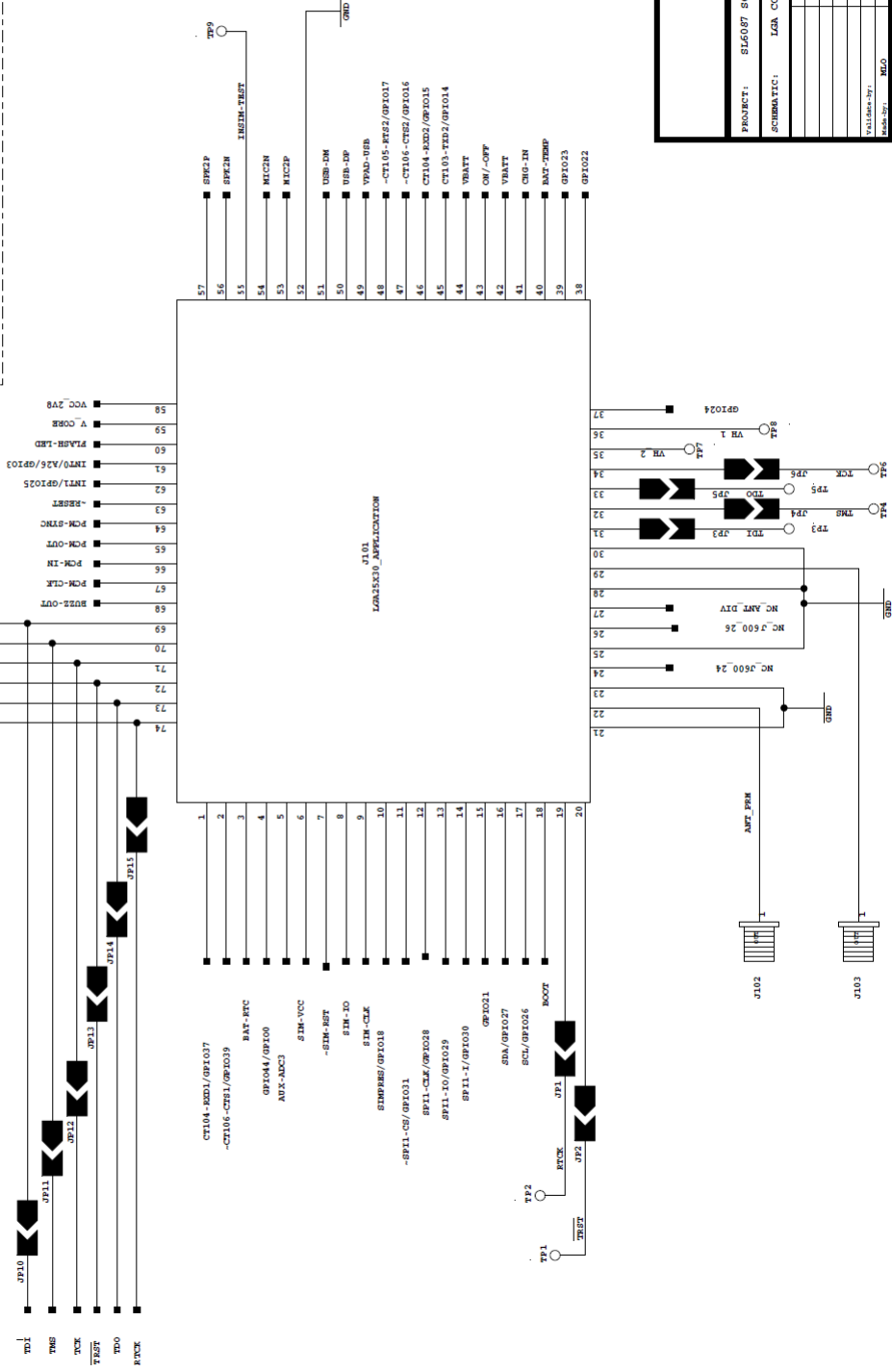
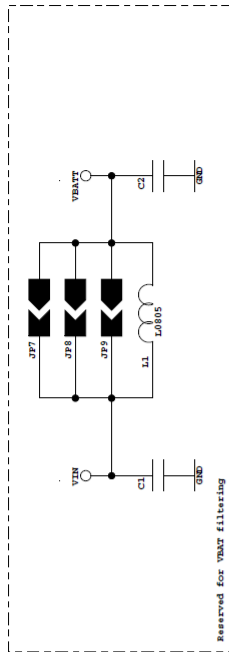
**VERSION:** 3.0

**REVISION:** 4/12

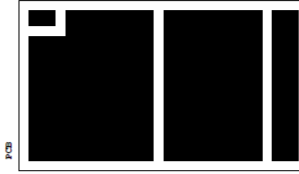
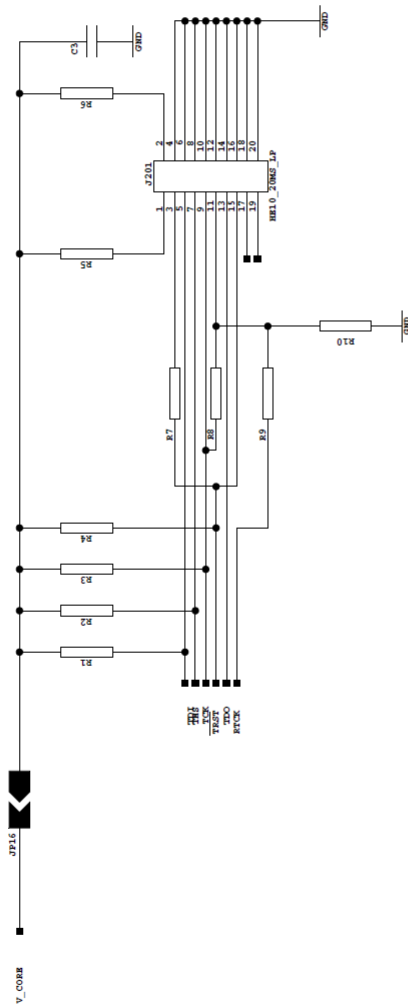


## 12.2. AirPrime SL6087 Socket-Up Board

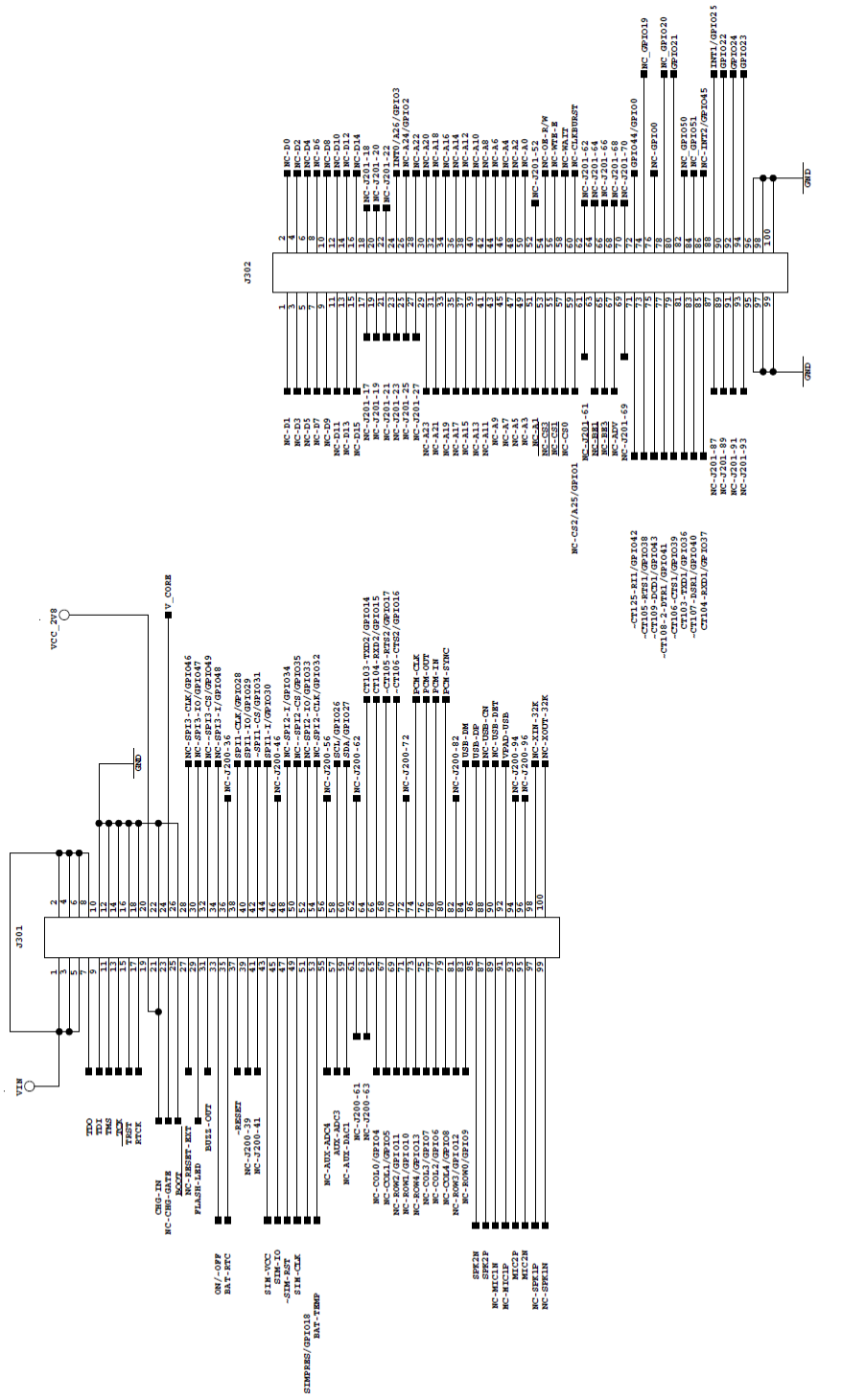




PROJECT: SL6087 SOCKET UP	
SCHEMATIC: LGA CONNECTOR	
Date:	07/01/2010
Version:	1.0
Package No:	
Sheet:	1/3



PROJECT: SL6087 SOCKET IP	
SCHEMATIC: PCB and JTAG	
Date:	07/01/2010
Version:	1.0
Number of Pages:	2/3
Author:	MSO



PRODUCT:	SL6087 SOCKET UP
SCHEMATIC:	WHEXIX DEV KIT CONNECTOR
Date:	07/01/2010
Version:	1.0
Author:	
Sheet:	3/3

P4 SOCKET UP\_LOCATION:R0L

P3 SOCKET UP\_LOCATION:R0L

P2 SOCKET UP\_LOCATION:R0L

P1 SOCKET UP\_LOCATION:R0L



**SIERRA**  
WIRELESS