

# RQK0604IGDQA

## Silicon N Channel MOS FET Power Switching

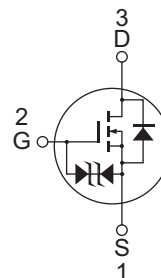
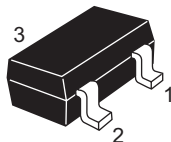
R07DS0308EJ0200  
(Previous: REJ03G1496-0100)  
Rev.2.00  
Mar 28, 2011

### Features

- Low on-resistance  
 $R_{DS(on)} = 111 \text{ m}\Omega$  typ.(at  $V_{GS} = 4.5 \text{ V}$ ,  $I_D = 1 \text{ A}$ )
- Low drive current
- High speed switching
- $V_{DSS} \geq 60 \text{ V}$  and capable of 2.5 V gate drive

### Outline

RENESAS Package code: PLSP0003ZB-A  
(Package name: MPAK)



1. Source
2. Gate
3. Drain

Note: Marking is "IG".

### Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	60	V
Gate to source voltage	$V_{GSS}$	$\pm 12$	V
Drain current	$I_D$	2	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	8	A
Body - drain diode reverse drain current	$I_{DR}$	2	A
Channel dissipation	$P_{ch}$ <sup>Note2</sup>	0.8	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

- Notes: 1.  $PW \leq 10 \mu\text{s}$ , Duty cycle  $\leq 1\%$   
2. When using the glass epoxy board (FR-4 40 × 40 × 1 mm)

## Electrical Characteristics

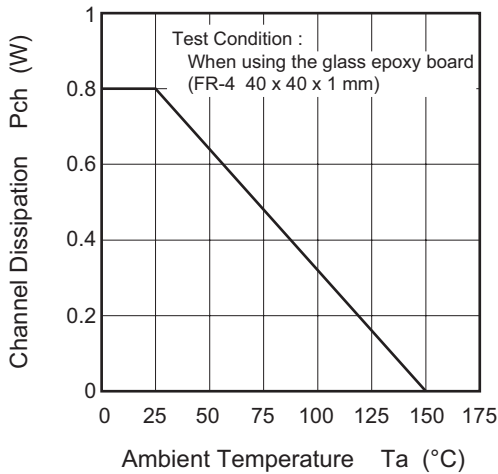
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+12	—	—	V	$I_G = +100 \text{ } \mu\text{A}$ , $V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-12	—	—	V	$I_G = -100 \text{ } \mu\text{A}$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	+10	$\mu\text{A}$	$V_{GS} = +10 \text{ V}$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	-10	$\mu\text{A}$	$V_{GS} = -10 \text{ V}$ , $V_{DS} = 0$
Drain to source leak current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 60 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	—	1.4	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Drain to source on state resistance	$R_{DS(on)}$	—	111	144	$\text{m}\Omega$	$I_D = 1 \text{ A}$ , $V_{GS} = 4.5 \text{ V}$ <sup>Note3</sup>
Drain to source on state resistance	$R_{DS(on)}$	—	129	180	$\text{m}\Omega$	$I_D = 1 \text{ A}$ , $V_{GS} = 2.5 \text{ V}$ <sup>Note3</sup>
Forward transfer admittance	$ y_{fs} $	3	6	—	S	$I_D = 1 \text{ A}$ , $V_{DS} = 10 \text{ V}$ <sup>Note3</sup>
Input capacitance	$C_{iss}$	—	320	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	$C_{oss}$	—	38	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	20	—	pF	$f = 1 \text{ MHz}$
Turn - on delay time	$t_{d(on)}$	—	12	—	ns	$I_D = 1 \text{ A}$
Rise time	$t_r$	—	35	—	ns	$V_{GS} = 10 \text{ V}$
Turn - off delay time	$t_{d(off)}$	—	36	—	ns	$R_L = 10 \text{ } \Omega$
Fall time	$t_f$	—	3.7	—	ns	$R_g = 4.7 \text{ } \Omega$
Total gate charge	$Q_g$	—	3.4	—	nC	$V_{DD} = 10 \text{ V}$
Gate to Source charge	$Q_{gs}$	—	0.6	—	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	$Q_{gd}$	—	1.0	—	nC	$I_D = 2 \text{ A}$
Body - drain diode forward voltage	$V_{DF}$	—	0.8	—	V	$I_F = 2 \text{ A}$ , $V_{GS} = 0$ <sup>Note3</sup>

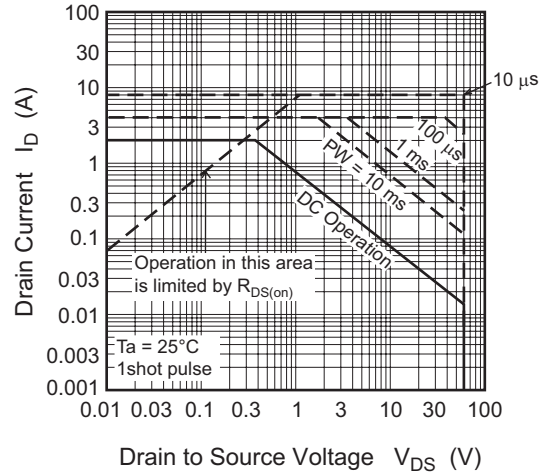
Notes: 3. Pulse test

Main Characteristics

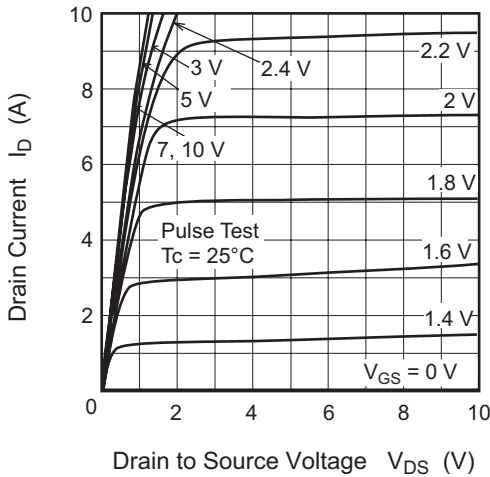
Power vs. Temperature Derating



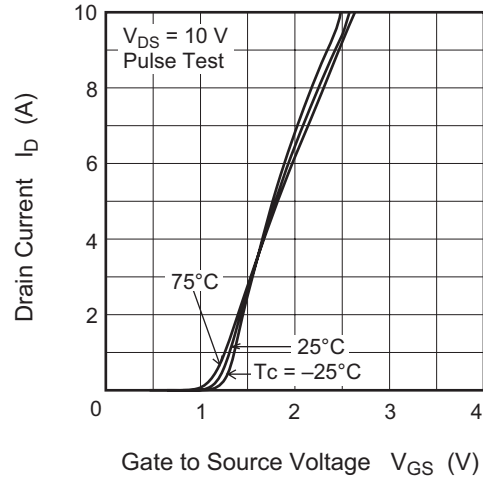
Maximum Safe Operation Area



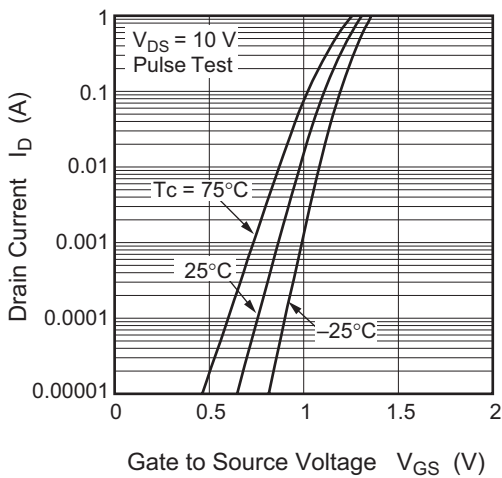
Typical Output Characteristics



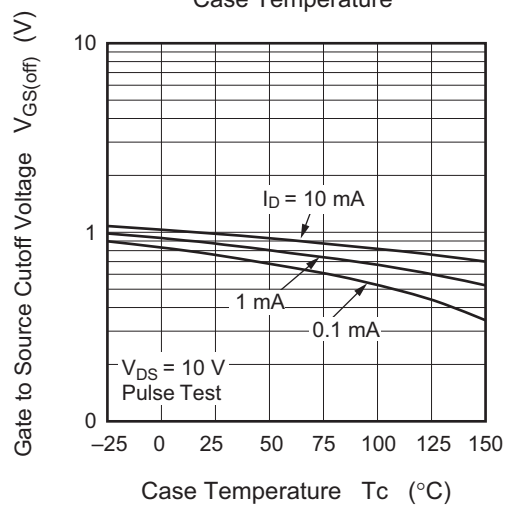
Typical Transfer Characteristics (1)

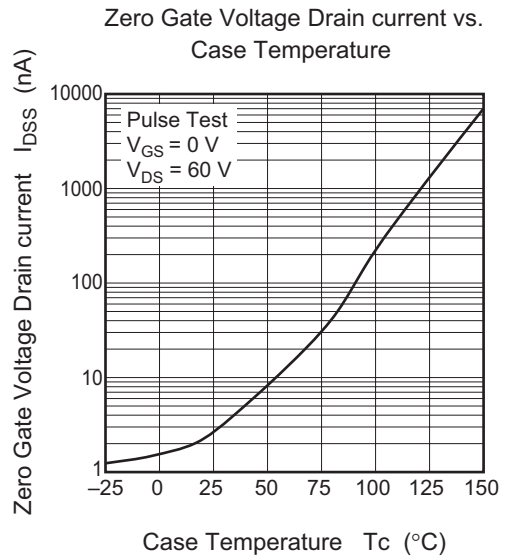
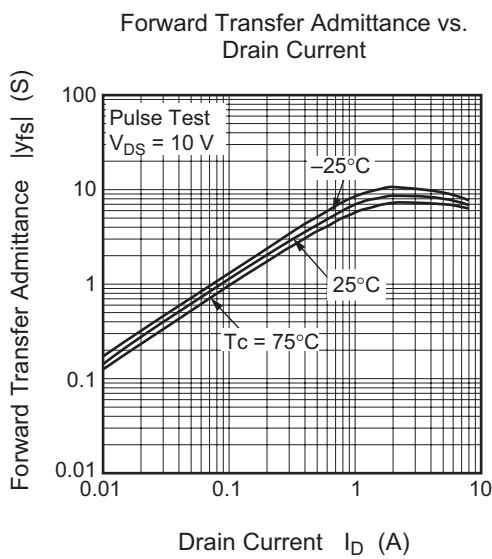
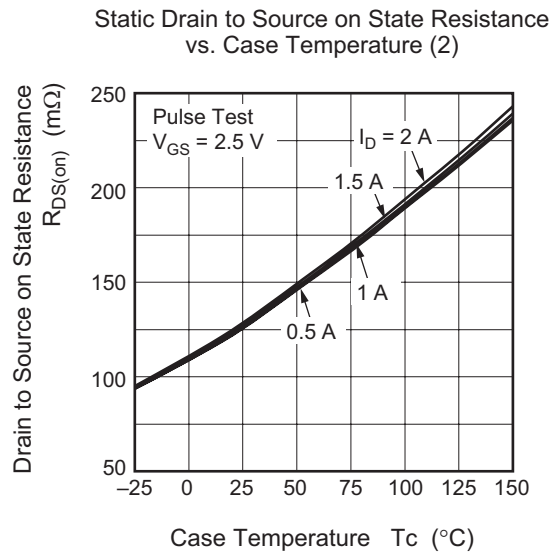
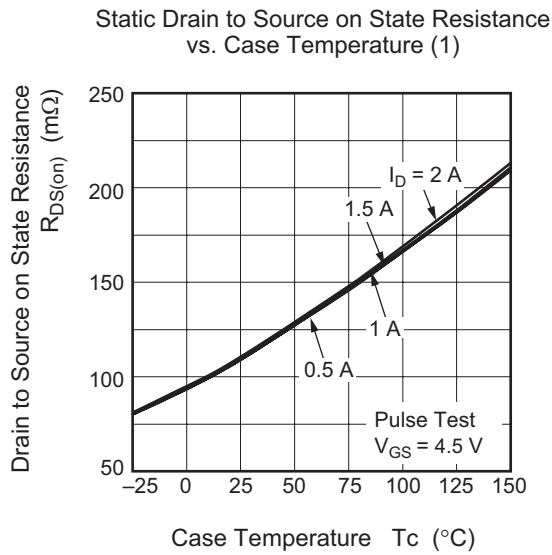
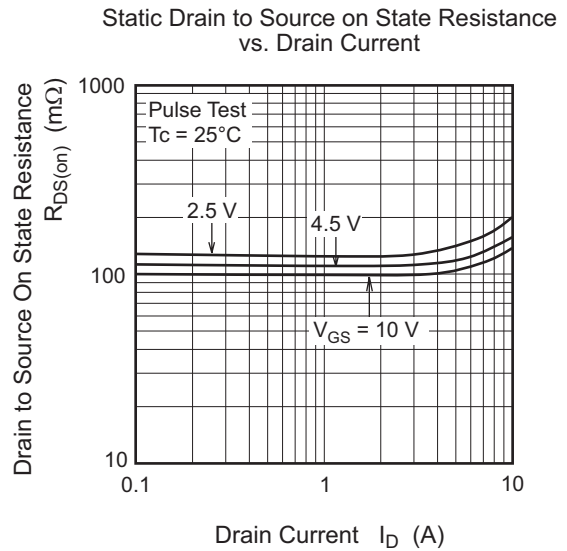
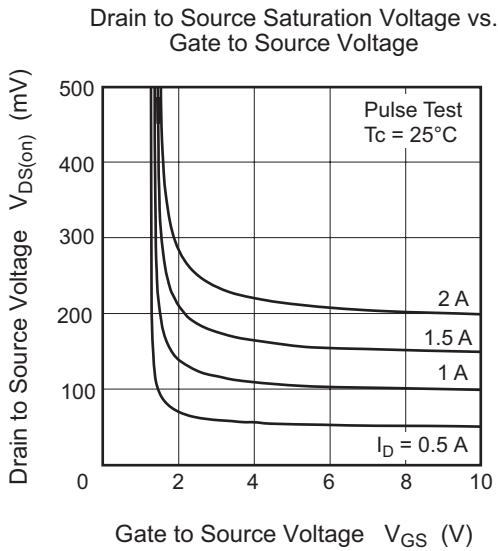


Typical Transfer Characteristics (2)

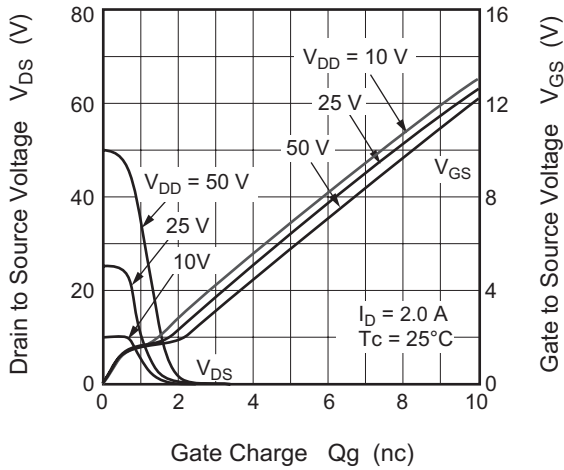


Gate to Source Cutoff Voltage vs. Case Temperature

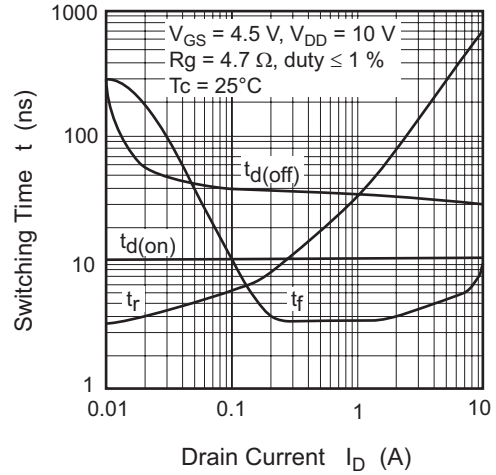




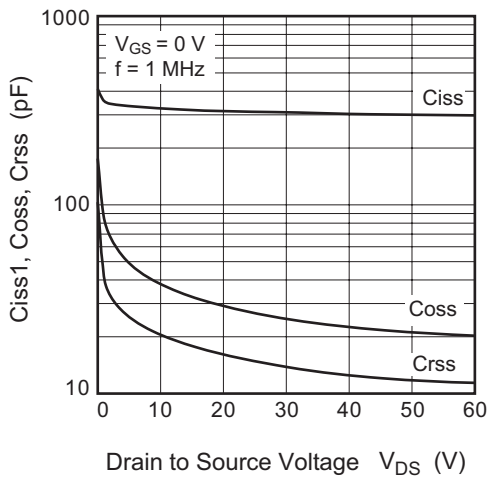
Dynamic Input Characteristics



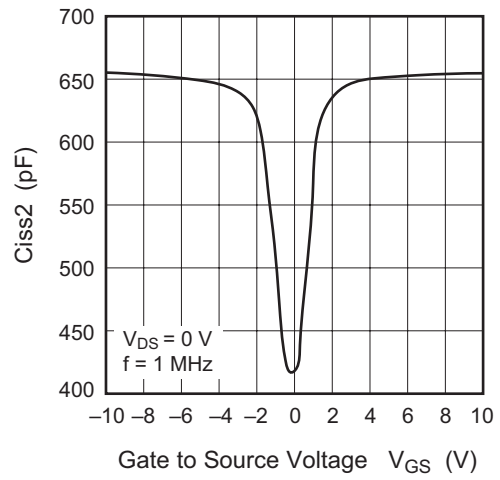
Switching Characteristics



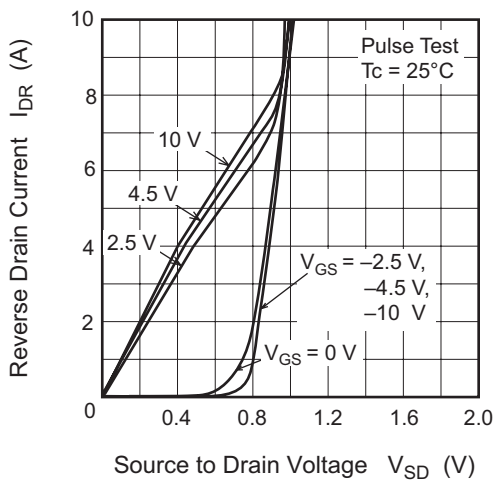
Typical Capacitance vs. Drain to Source Voltage



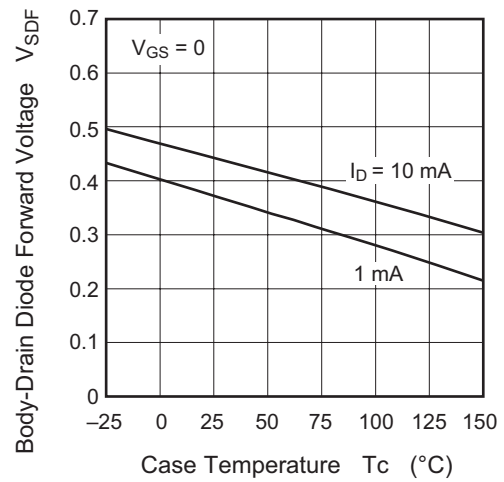
Input Capacitance vs. Gate to Source Voltage

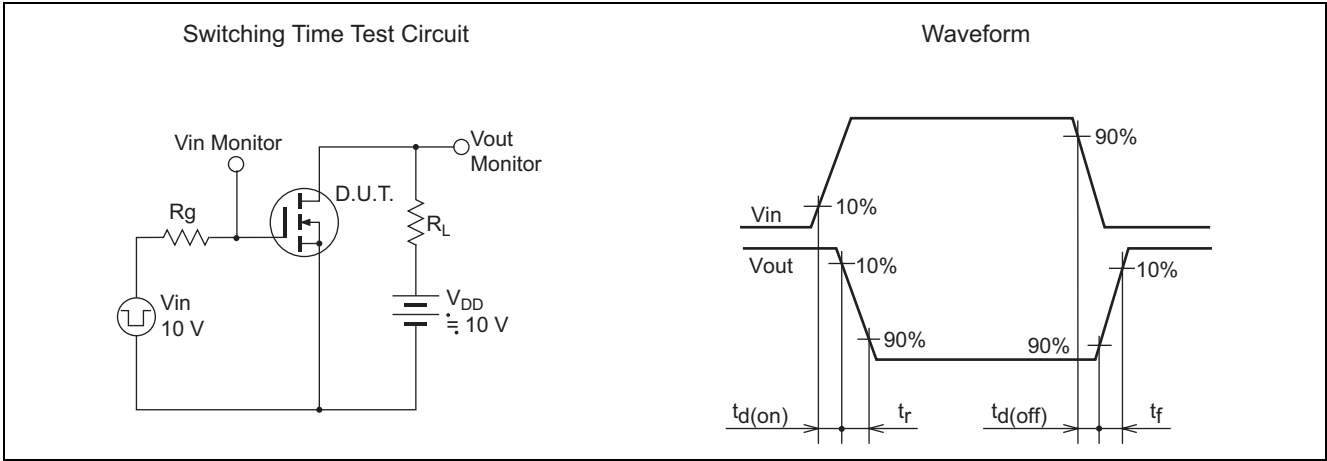


Reverse Drain Current vs. Source to Drain Voltage

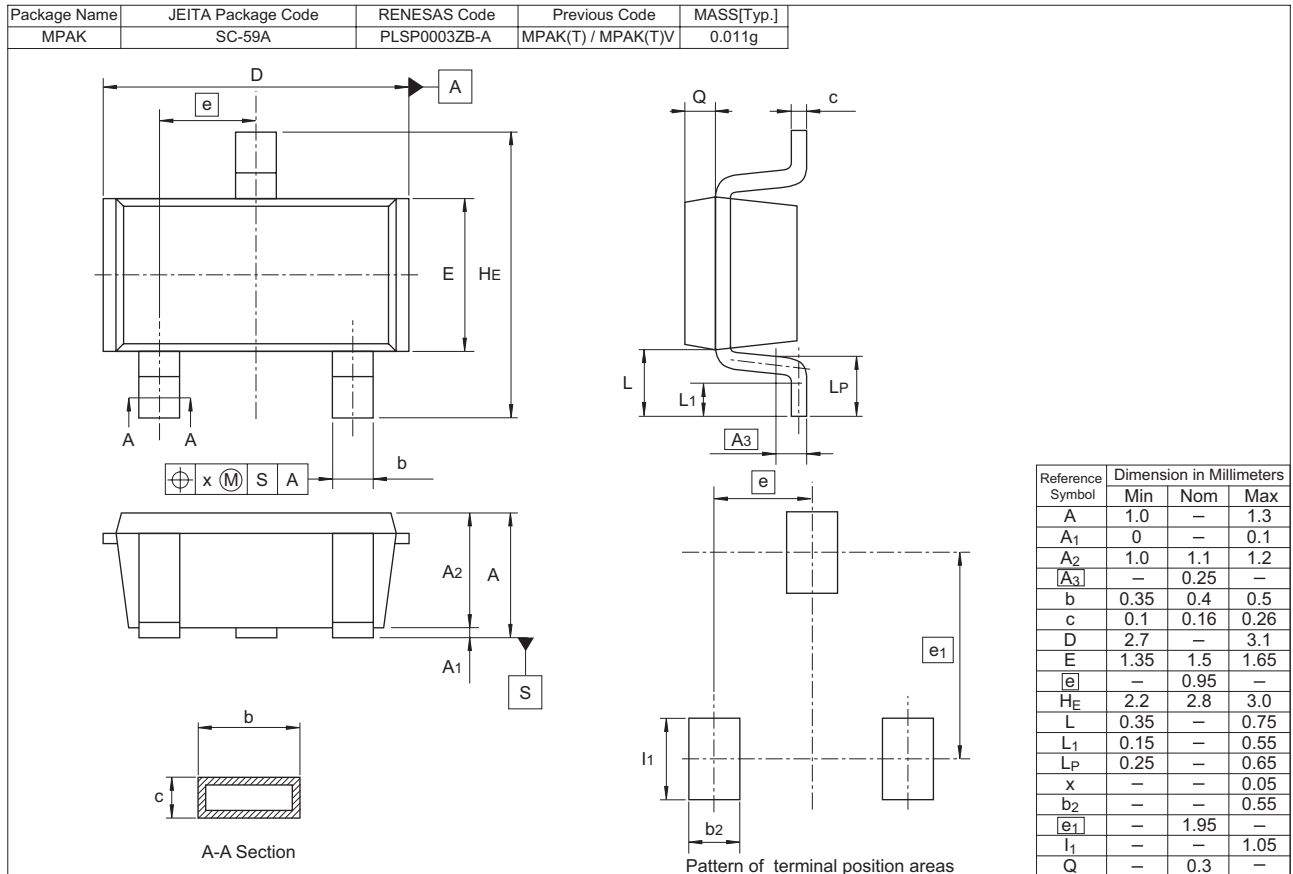


Body-Drain Diode Forward Voltage vs. Case Temperature





### Package Dimensions



### Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQK0604IGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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