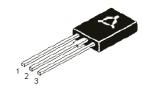
# Medium Power PNP Transistor multicomp







#### **Pin Configuration:**

- 1. Emitter
- 2. Collector
- 3. Base

#### Feature:

- PNP Plastic Medium Power Silicon Transistor
- Designed for use Line-Operated Applications Such as Low Power, Line Operated Series Pass and Switching Regulator Requiring PNP Capability

### **Absolute Maximum Ratings**

Description	Symbol	Rating	Unit	
Collector Base Voltage	V <sub>CBO</sub>	200		
Collector Emitter Voltage	V <sub>CEO</sub>	300	V	
Emitter Base Voltage	V <sub>EBO</sub>	3		
Collector Current Continuous	I <sub>C</sub>	500	mA	
Power Dissipation at T <sub>c</sub> = 25°C Derate above 25°C	P <sub>D</sub>	20 0.16	W mW/°C	
Operating and Storage Junction Temperature Range	$T_{j},T_{stg}$	-65 to +150	°C	

#### **Thermal Resistance**

Junction to Case	R <sub>th (j-c)</sub>	6.25	°C/W
Junction to Ambient	R <sub>th (j-a)</sub>	83.3	C/VV

## Electrical Characteristics ( $T_c = 25$ °C unless specified otherwise)

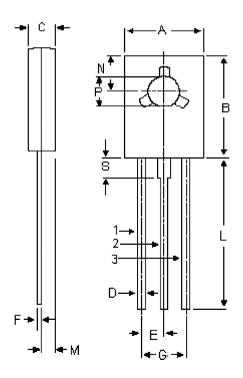
Parameter	Symbol	Test Condition	Minimum	Maximum	Unit
Collector Emitter Breakdown Voltage	V <sub>CEO (SUS)</sub>	$I_{\rm C} = 1  \text{mA}, I_{\rm B} = 0$	300	1	V
Collector Cut off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 300V, I <sub>E</sub> = 0	-	100	μΑ
Emitter Cut off Current	I <sub>EBO</sub>	$V_{EB} = 3V$ , $I_{C} = 0$	-	100	
DC Current Gain	h <sub>FE</sub>	$I_{\rm C} = 0.05 A, V_{\rm CE} = 10 V$	30	240	-

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Dimensions	Min.	Max.
А	7.4	7.8
В	10.5	10.8
С	2.4	2.7
D	0.7	0.9
E	2.25 (Typical)	
F	0.49	0.75
G	4.5 (Typical)	
L	15.7 (Typical)	
M	1.27 (Typical)	
N	3.75 (Typical)	
Р	3	3.2
S	2.5 (Typical)	

**Dimensions: Millimetres** 

#### **Pin Configuration:**

- 1. Emitter
- 2. Collector
- 3. Base

### **Part Number Table**

Description	Part Number	
Transistor, PNP, TO-126	MJE350	

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