



Description:

The MJ15024 powerbase power transistors designed for high power audio, disk head positioners and other linear applications.

Features:

- · High safe operating area (100% tested) 2A at 80V
- High DC current gain h_{FF} = 15 (min.) at I_{C} = 8A DC
- · Pb-free packages

Mamimum Ratings

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V _{CEO}	250	V DC	
Collector-Base Voltage	V _{CBO}	400		
Emitter-Base Voltage	V _{EBO}	5		
Collector-Emitter Voltage	V _{CEX}	400	A DC	
Collector Current-Continuous -Peak (Note 1)	I _C	16 30		
Base Current-Continuous	I _B	5		
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	250 1.43	W W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-65 to +200	°C	

Thermal Characteristics

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{_{ hetaJC}}$	0.70	°C/W

Max. ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Pulse test: pulse width = 5ms, duty cycle ≤10%.

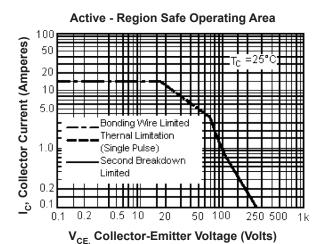




Electrical Characteristics (T_c = 25°C unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
Off Characteristics				
Collector-Emitter Sustaining Voltage (Note 2) (I _C = 100mA DC, I _B = 0)	V _{EO (sus)}	250	-	-
Collector Cut off Current (V _{CE} = 250V DC, V _{BE (off)} = 1.5V DC)	I _{CEX}	-	250	
Collector Cut off Current ($V_{CE} = 200V DC, I_B = 0$)	I _{CEO}	-	μA DC	μA DC
Emitter Cut off Current ($V_{CE} = 5V DC I_B = 0$)	I _{EBO}	-	500	
Second Breakdown				
Second Breakdown Collector Current with Base Forward Biased (V_{CE} = 50V DC, t = 0.5s (Non-repetitive)) (V_{CE} = 80V DC, t = 0.5s (non-repetitive))	I _{S/b}	5 2	-	A DC
On Characteristic				
DC Current Gain $(I_C = 8A DC, V_{CE} = 4V DC)$ $(I_C = 16A DC, V_{CE} = 4V DC)$	h _{FE}	15 5	60 -	-
Collector-Emitter Saturation Voltage ($I_C = 8A DC$, $I_B = 0.8A DC$) ($I_C = 16A DC$, $I_B = 3.2A DC$)	V _{CE (sat)}	-	1.4 4	V DC
Base-Emitter On Voltage (I _C = 8A DC, V _{CE} = 4V DC)	V _{BE (on)}	-	2.2	
Dynamic Characteristics				
Current-Gain - Bandwidth Product ($I_C = 1A DC, V_{CE} = 10V DC, f_{test} = 1MHz$)	f _T	4	-	MHz
Output Capacitance $(V_{CB} = 10V DC, I_{E} = 0, f_{test} = 1MHz)$	C _{ob}	-	500	pF

2. Pulse Test : Pulse Width = 300 μ s, Duty Cycle \leq 2%.



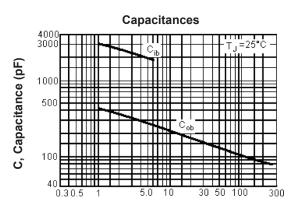
There are two limitation on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_{C} - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than curves indicate. The data is based on $T_{\text{J}(\text{PK})}$ = 200°C; TC is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values lon than the limitations imposed by second breakdown.

www.element14.com www.farnell.com www.newark.com



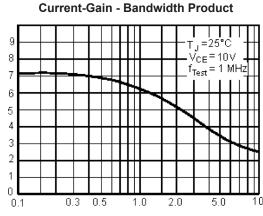


Typical Characteristics

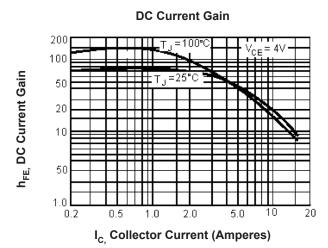


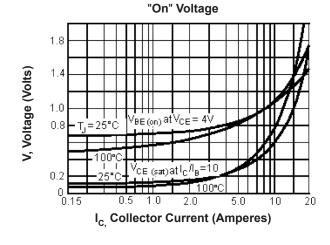
V_R, Reverse Voltage (Volts)

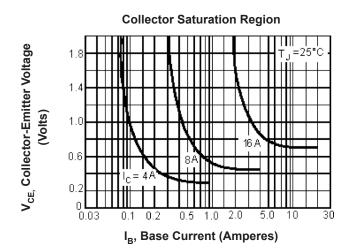
ff, Current-Gain - Bandwidth Product (MHz)



IC, Collector Current (Amperes)





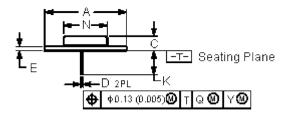


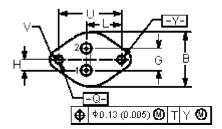
www.element14.com www.farnell.com www.newark.com





Dimensions:





Pin Configuration

Pin 1. Base

2. Emitter Collector (Case)

Dimensions	Min.	Max.
А	1.55 (39.37) Reference	
В	-	1.05 (26.67)
С	0.25 (6.35)	0.335 (8.51)
D	0.038 (0.97)	0.043 (1.09)
E	0.055 (1.4)	0.07 (1.77)
G	0.43 (10.92) BSC	
Н	0.215 (5.46) BSC	
K	0.44 (11.18)	0.48 (12.19)
L	0.665 (16.89) BSC	
N	-	0.83 (21.08)
Q	0.151 (3.84)	0.165 (4.19)
U	1.187 (30.15) BSC	
V	0.131 (3.33)	0.188 (4.77)

Dimensions: Inches (Millimetres)

Part Number Table

Description	Part Number	
Transistor, NPN, TO-3	MJ15024	

Important Notice: This data sheet and its contents (the "Information") belong to the members of the Premier Farnell group of companies (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheet should check for themselves the Information and the suitability of the products for their purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. Multicomp is the registered trademark of the Group. © Premier Farnell plc 2012.

www.element14.com www.farnell.com www.newark.com

