



PRODUCT BULLETIN

Generic Copy

ISSUE DATE: 07-Sep-2012
NOTIFICATION: 15328
TITLE: MPC5606S ERRATA UPDATE
EFFECTIVE DATE: 08-Sep-2012

DEVICE(S)

MPN
MPXD1005VLQ64
MPXD1010VLQ64
MPXD1010VLU64
SPC5602SF2VLQ6
SPC5602SF2VLQ6R
SPC5604SF2VLQ6
SPC5604SF2VLQ6R
SPC5606SF1VLQ6
SPC5606SF1VLQ6R
SPC5606SF2CLU6
SPC5606SF2VLQ6
SPC5606SF2VLQ6R
SPC5606SF2VLU6
SPC5606SF2VLU6R

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The MPC5606S errata documentation has been updated to include additional errata that have been identified.

The following errata have been added to the MPC5606S errata documentation:

4168 ADC: "Abort switch" aborts the ongoing injected channel as well as the upcoming normal channel.

3321 MC_RGM: A non-monotonic ramp on the VDDR line can cause the RGM module to clear all flags in the DES register, meaning that code cannot tell if a POR has occurred.

The updated errata documentation is attached to this notice and can be found at <http://cache.freescale.com/files/32bit/doc/errata/MPC5606S1M25V.pdf>

REASON FOR CHANGE

The errata documentation for MPC5606S has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes have been made to the current production device. The errata describe existing conditions identified on current production devices. There are potential hardware and/or software implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 17-Aug-2012

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:
[15328_MPC560XS_1M25V.pdf](#)

