

AN-2164 LMR12010 Demo Board

1 Introduction

The LMR12010 is a step down DC-DC regulator that operates at the following switching frequencies: the LMR12010X at 1.6MHz and the LMR12010Y at 3MHz. The demo board is configured to convert 5V input to 1.8V output at 1A load current using the Y version. The tiny low profile thin SOT23 package allows the design to use less than 1 square inch of board area.

The circuit is configured with the boost diode connected to V_{IN} . V_{IN} must not exceed the maximum operating limit of $5.5V + V_{ID2}$ using this configuration. This ensures that the voltage between the Boost and SW pins, $V_{BOOST} - V_{SW}$, does not exceed 5.5V for proper operation. For more information regarding this requirement, see the *LMR12010 SIMPLE SWITCHER® 20Vin, 1A Step-Down Voltage Regulator in SOT-23 Data Sheet* ([SNVS731](#)).

The schematics at the end of this document show how to reconfigure this demo board for various input and output conditions as discussed in the *LMR12010 SIMPLE SWITCHER® 20Vin, 1A Step-Down Voltage Regulator in SOT-23 Data Sheet* ([SNVS731](#)). Short or leave open the connections as indicated in the schematics. The above restrictions for the input voltage are valid only for the demo board as shipped with the demo board schematic shown in [Figure 3](#)

2 Features

- 3.0V to 5V Input Voltage Range (as shipped)
- 1.8V Output Voltage
- Up to 1A Output Current
- Switching Frequency of 3MHz
- Small Solution Size (17mm x 17mm)

3 Enable Operation

The demo board includes a spot for a pull-up resistor R5 (not stuffed) to enable the device once V_{IN} has exceeded 1.8V (typ). A typical value for this resistor is 100K Ω . Otherwise use the EN post to apply a logic signal to test startup and shutdown of the device. Never allow the EN voltage to exceed V_{IN} .

4 Adjusting the Output Voltage

The output voltage can be changed from 1.8V to another voltage by adjusting the feedback resistors using [Equation 1](#):

$$V_{OUT} = V_{FB}(1+(RFBT/RFBB)) \quad (1)$$

Where V_{FB} is 0.8V.

For more information on component selection and features, see the device-specific data sheet.

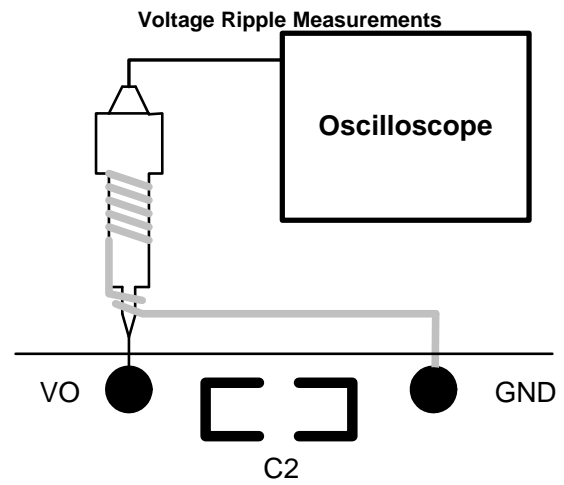
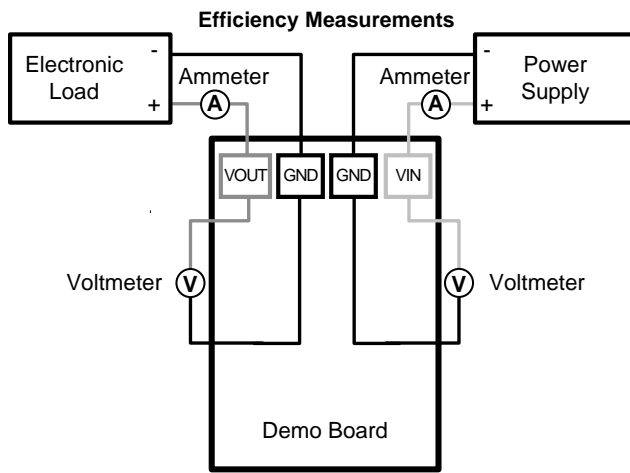
Table 1. Bill of Materials (BOM) for LMR12010Y

Part ID	Part Value	Manufacturer	Part Number	Package Type
U1	1A Buck Regulator	Texas Instruments	LMR12010	Thin SOT23-6
C1, Input Cap	10 μ F, 10V, X5R	Murata	GRM316R61A475KE19D	1206
C2, Output Cap	10 μ F, 6.3V, X5R	Murata	GRM319R60J106KE01D	1206
C3, Boost Cap	0.01 μ F	Vishay	VJ0805Y103KXAAC	0805
D1, Catch Diode	0.45Vf Schottky 1A, 20VR	Vishay	MBRA120TRPBF	SMA
D2, Boost Diode	1Vf @ 50mA Diode	Diodes, Inc.	1N4148W-7-F	SOD-123
L1	2.7 μ H, 1.8A, 33m Ω	TDK	VLCF5028T-2R7N1R8-2	6028
R1	12.4k Ω , 1%	Vishay	CRCW080512K4FKEA	0805
R2	10k Ω , 1%	Vishay	CRCW080510K0FKEA	0805
R3	0 Ω	Vishay	CRCW08050000Z0EA	0805
D3, C4, R4, R5	Open			

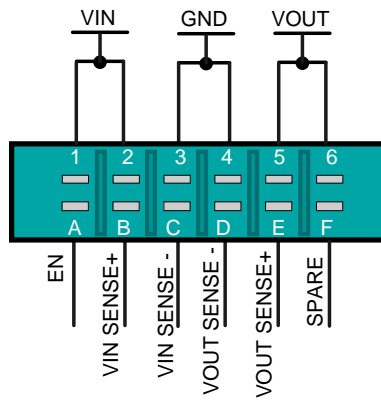
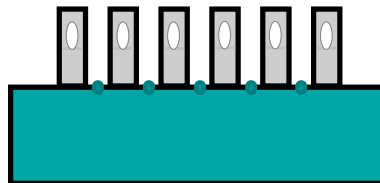
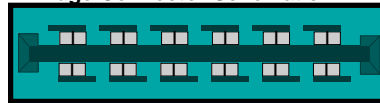
5 Test Setup

Table 2. Demonstration Board Quick Setup Procedures

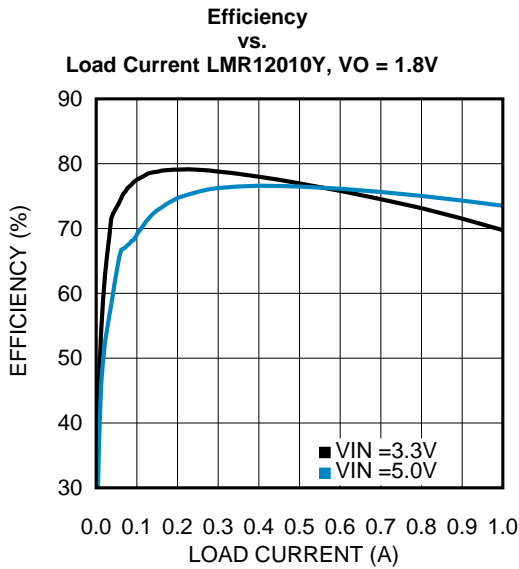
Step	Description	Notes
1	Connect a power supply to VIN terminals	
2	Connect a load to VO terminals	
3	EN should be connected to VIN for normal operation. Short this to ground to shutdown the part.	
4	Set $V_{IN} = 5V$, with 0A load applied, check V_O with a voltmeter	Nominal 1.8V
5	Apply a 1A load and check V_O	Nominal 1.8V



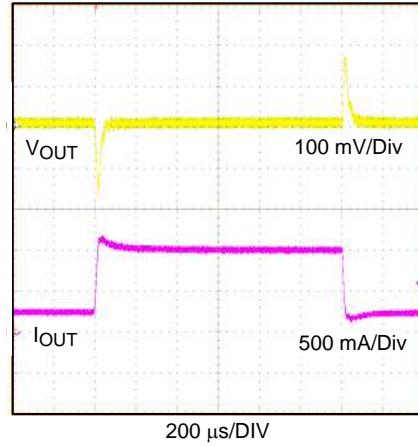
Edge Connector Schematic



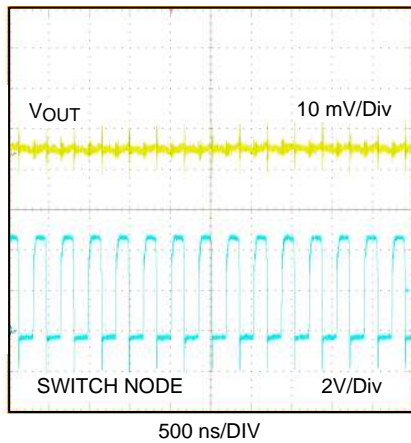
6 Typical Performance Characteristics



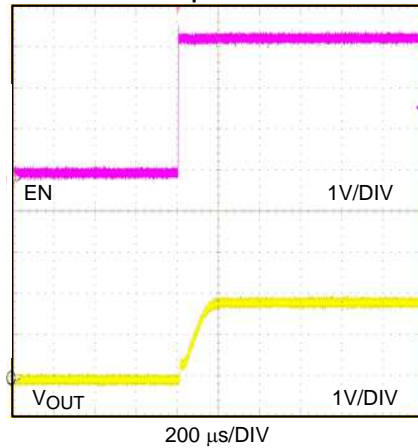
Load Transient Waveforms LMR12010Y
 $I_{OUT} = 100$ to $1000mA$



Switching Node and Output Voltage Waveforms



Startup Waveform



7 Layout

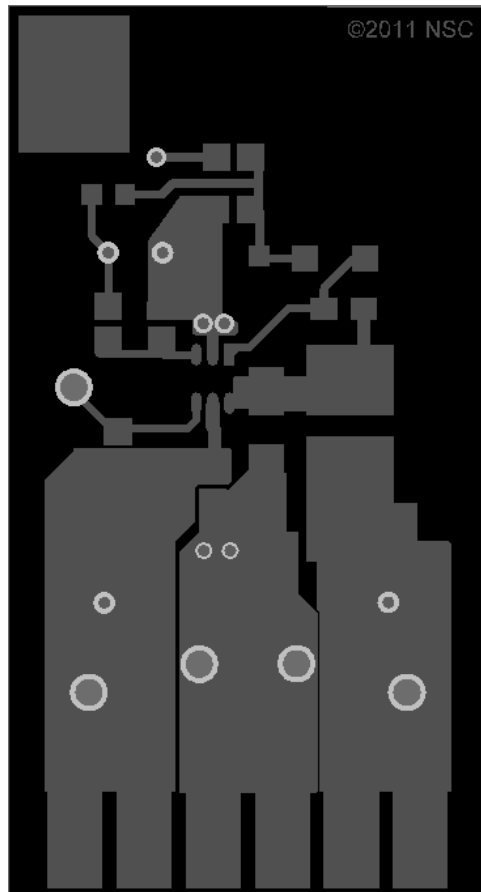


Figure 4. Top Layer

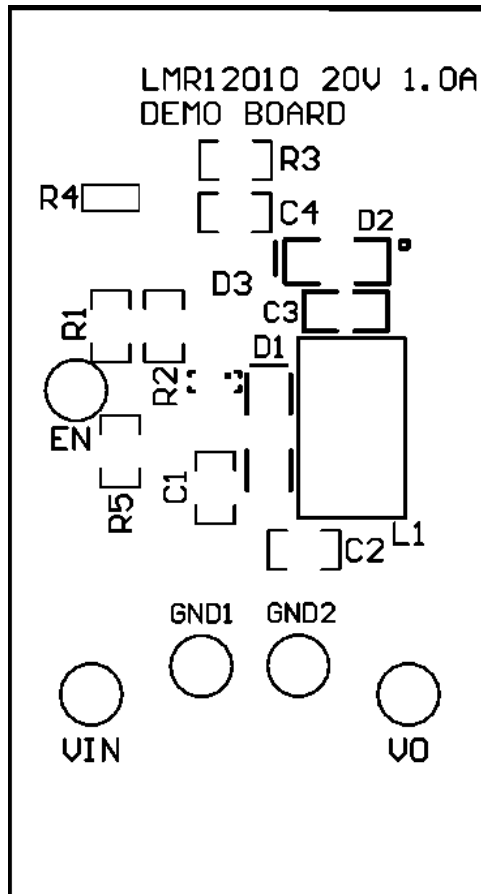


Figure 5. Top Overlay

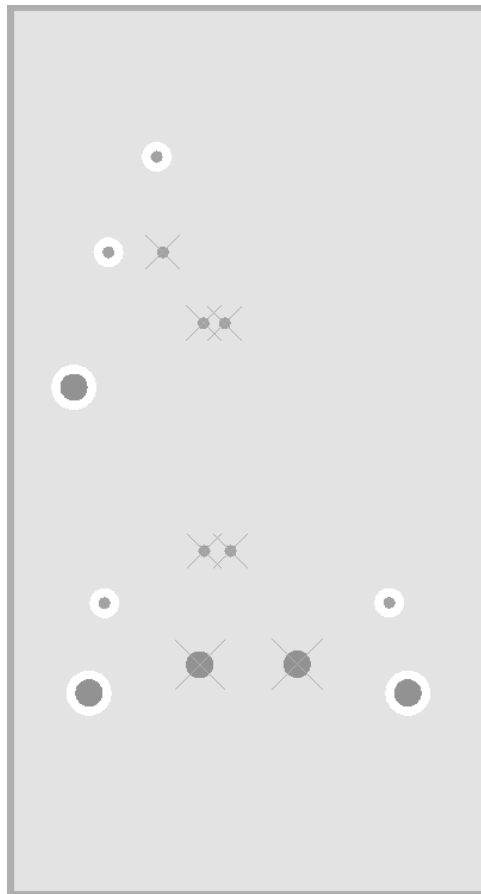


Figure 6. Inner Layer 1

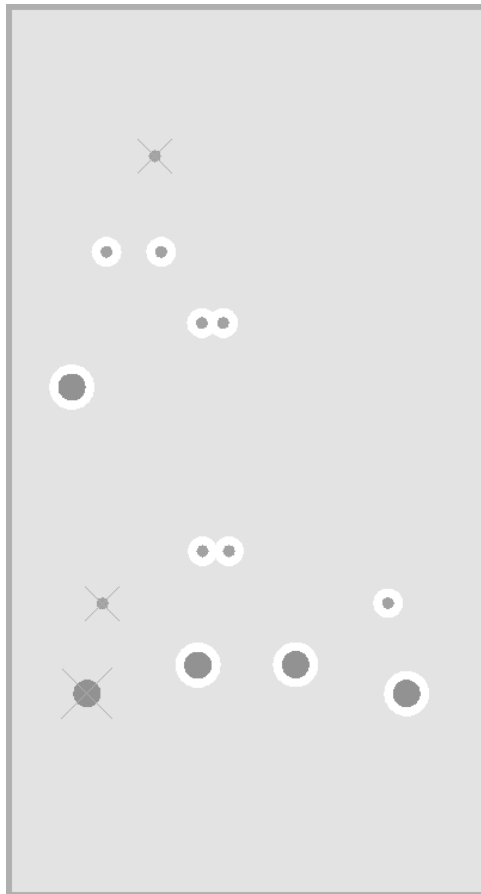


Figure 7. Inner Layer 2

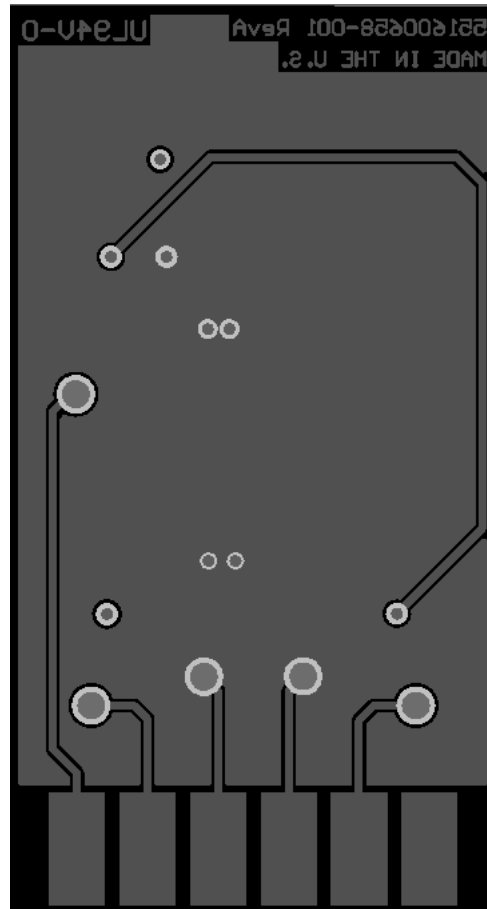


Figure 8. Bottom Layer

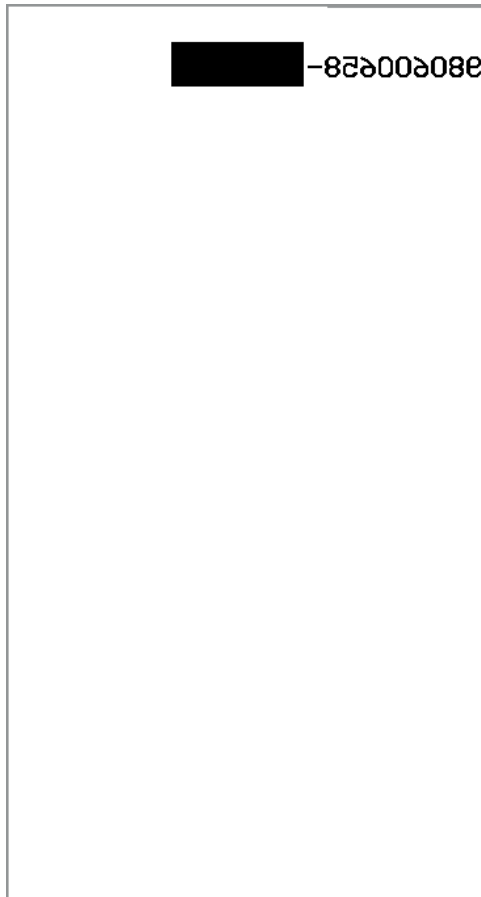


Figure 9. Bottom Overlay

8 Additional Circuit Configuration Schematics

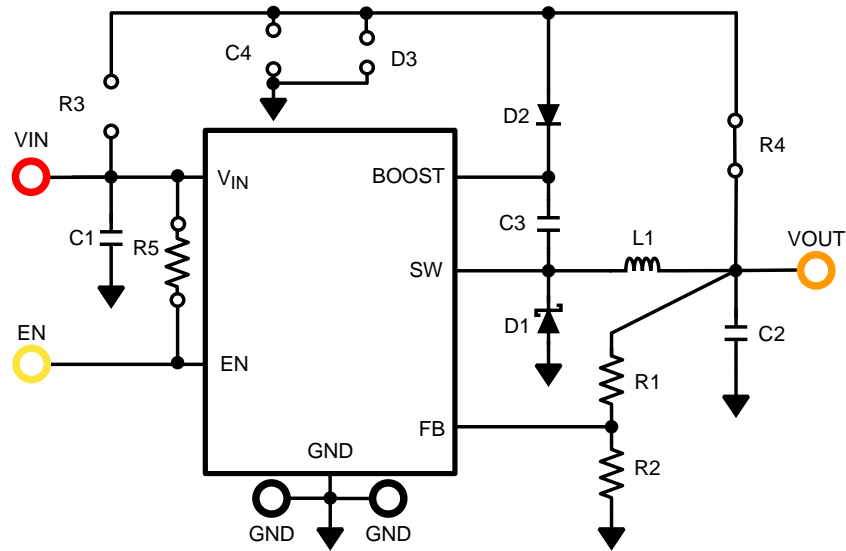


Figure 10. V_{BOOST} Derived from V_{OUT}

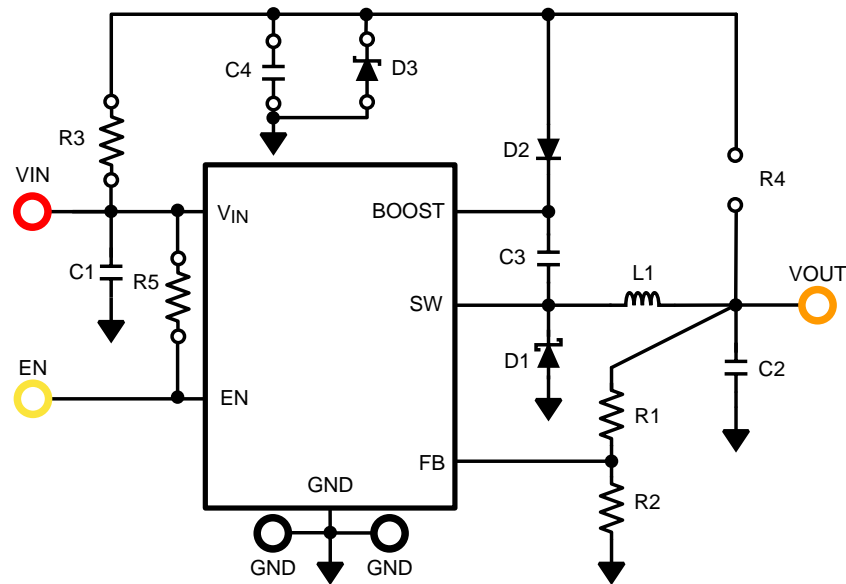


Figure 11. V_{BOOST} Derived from V_{SHUNT}

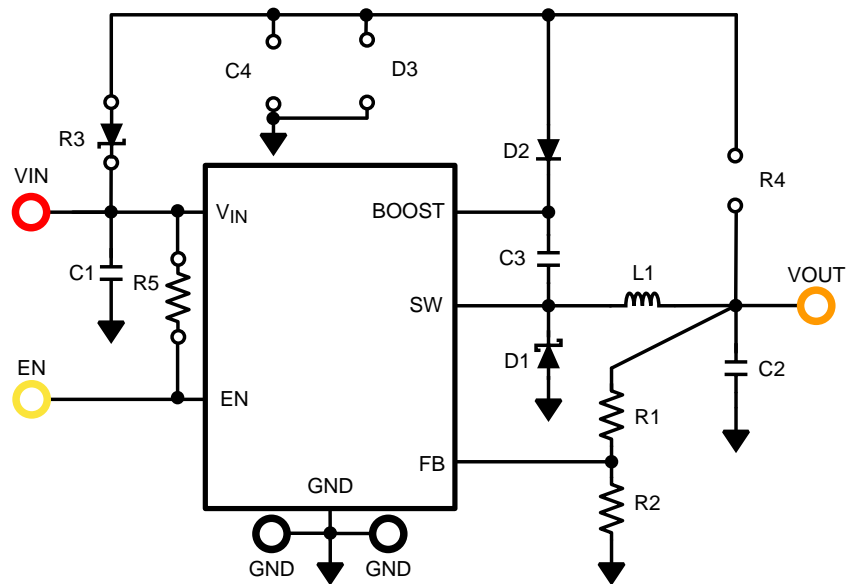


Figure 12. V_{BOOST} Derived from Series Zener Diode (V_{IN})

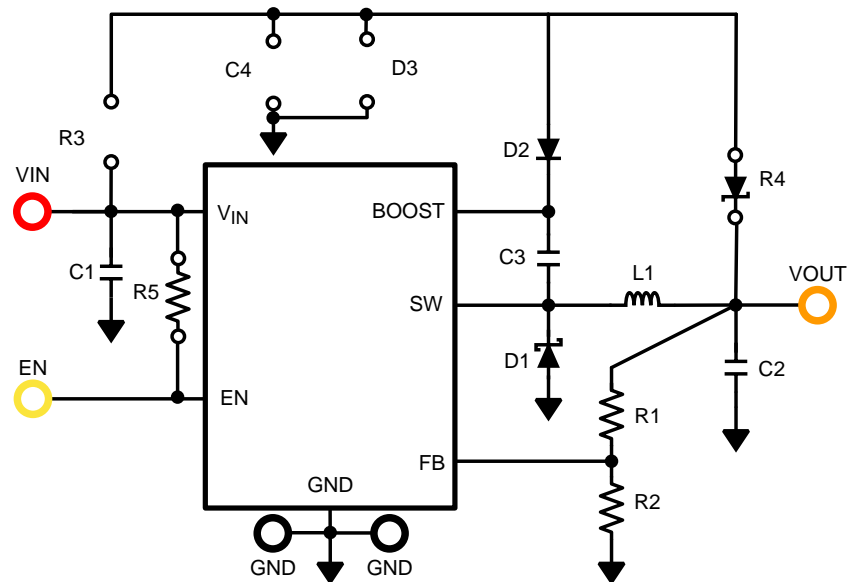


Figure 13. V_{BOOST} Derived from Series Zener Diode (V_{OUT})

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