

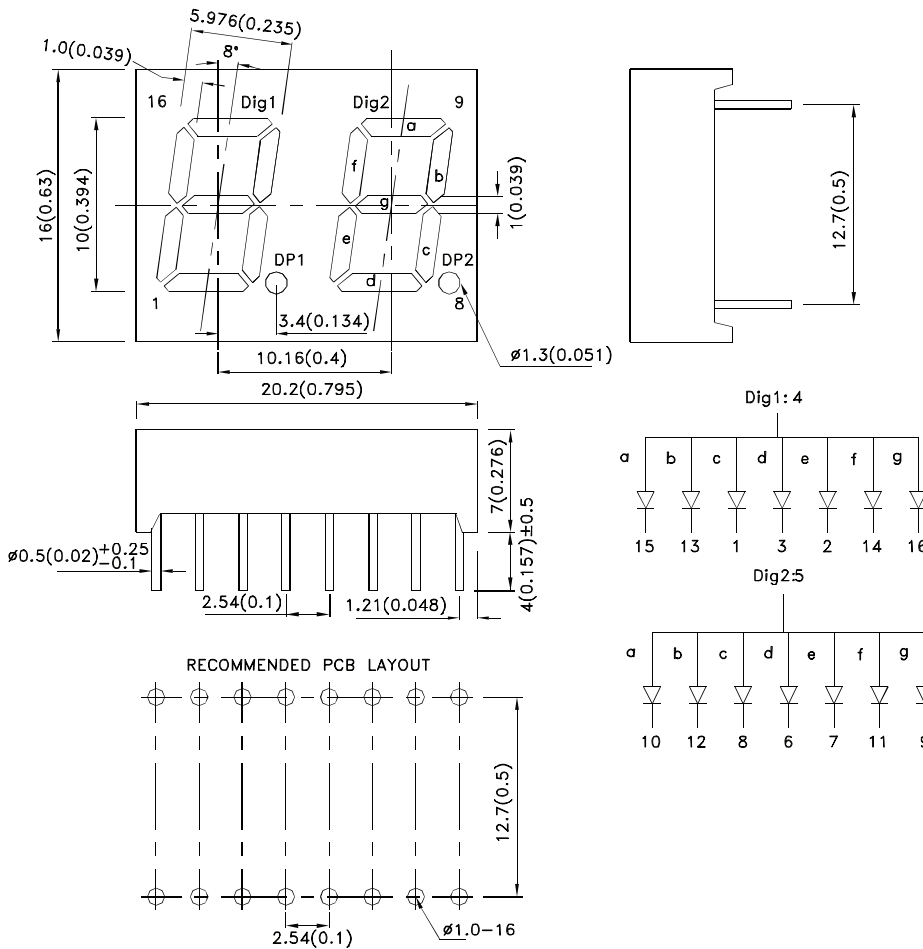
Features

- 0.4 inch digit height.
- Low current operation.
- Excellent character appearance.
- Easy mounting on P.C. boards or sockets.
- Two digit package simplifies alignments & assembly.
- Mechanically rugged.
- Standard : gray face, white segment.
- RoHS compliant.

Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

Package Dimensions & Internal Circuit Diagram



Selection Guide

Part No.	Dice	Lens Type	Iv (ucd) [1] @ 10mA		Description
			Min.	Typ.	
DA04-11EWA	High Efficiency Red (GaAsP/GaP)	White Diffused	3600	6400	Common Anode
			*900	*2000	

Note:

1. Luminous intensity/ luminous Flux: +/-15%.

* Luminous Intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Typ.	Max.	Units	Test Conditions
λ_{peak}	Peak Wavelength	High Efficiency Red	627		nm	I _F =20mA
λ_D [1]	Dominant Wavelength	High Efficiency Red	617		nm	I _F =20mA
$\Delta\lambda_{1/2}$	Spectral Line Half-width	High Efficiency Red	45		nm	I _F =20mA
C	Capacitance	High Efficiency Red	15		pF	V _F =0V;f=1MHz
V _F [2]	Forward Voltage	High Efficiency Red	2.0	2.5	V	I _F =20mA
I _R	Reverse Current	High Efficiency Red		10	uA	V _R =5V

Notes:

1. Wavelength: +/-1nm.

2. Forward Voltage: +/-0.1V.

3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

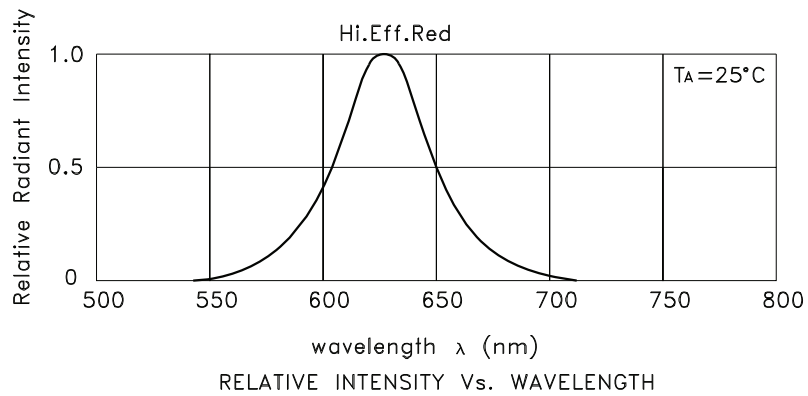
Absolute Maximum Ratings at TA=25°C

Parameter	High Efficiency Red	Units
Power dissipation	75	mW
DC Forward Current	30	mA
Peak Forward Current [1]	160	mA
Reverse Voltage	5	V
Operating / Storage Temperature	-40°C To +85°C	
Lead Solder Temperature[2]	260°C For 3-5 Seconds	

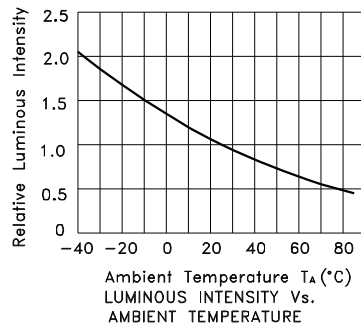
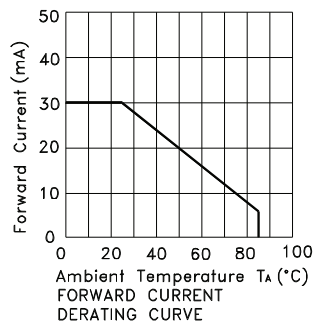
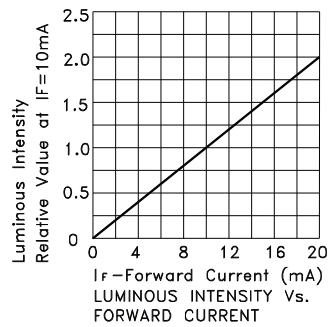
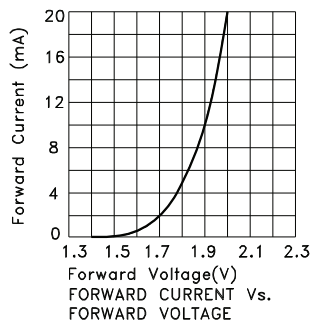
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. 2mm below package base.

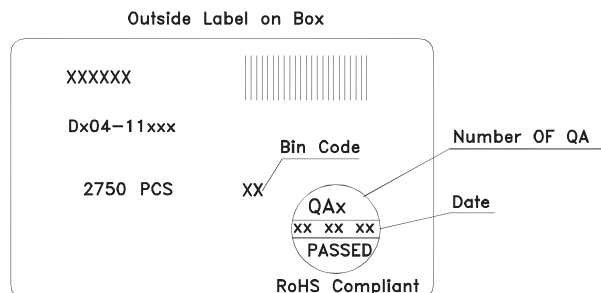
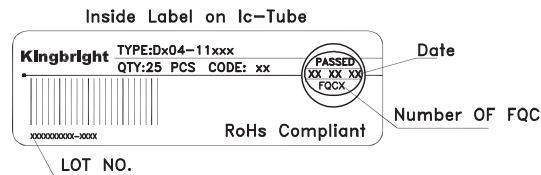
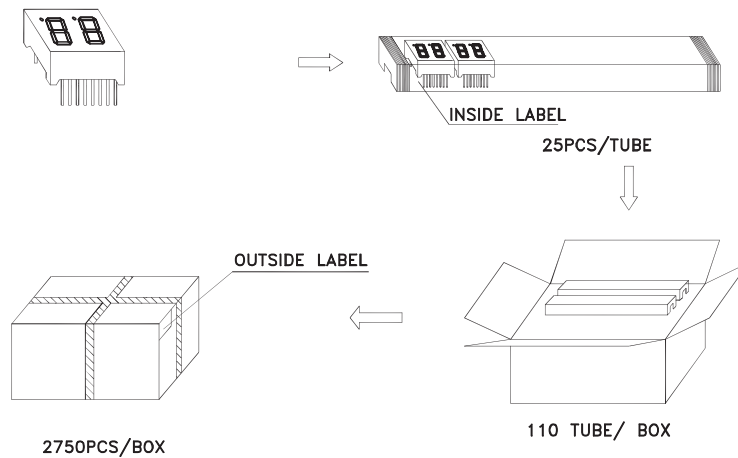


High Efficiency Red DA04-11EWA



PACKING & LABEL SPECIFICATIONS

DA04-11EWA



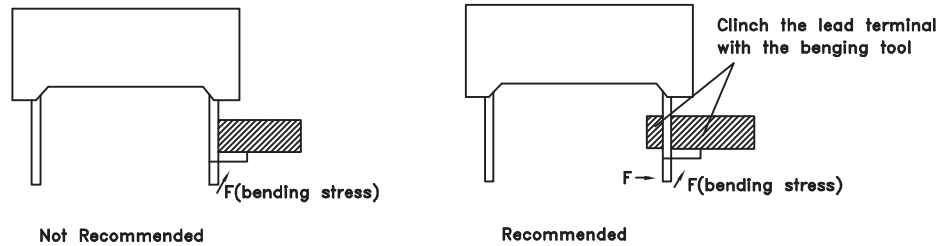
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THROUGH HOLE DISPLAY MOUNTING METHOD

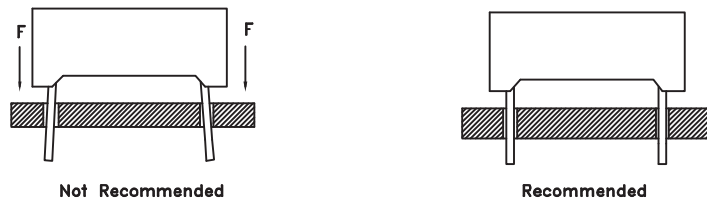
Lead Forming

Do not bend the component leads by hand without proper tools.
The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

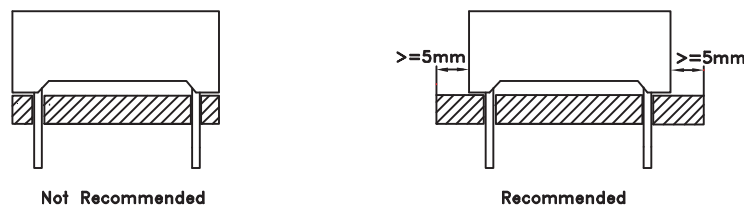


Installation

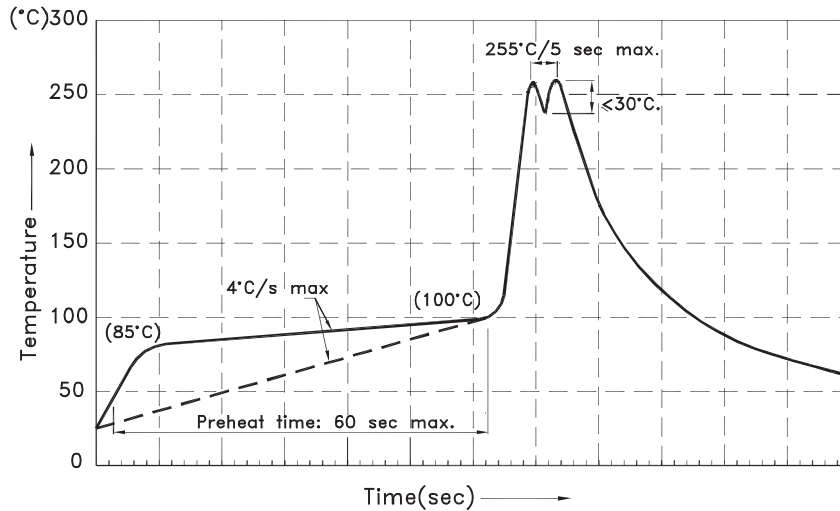
1. The installation process should not apply stress to the lead terminals.
2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



Recommended Wave Soldering Profiles:



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.
7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Soldering General Notes:

1. Through-hole displays are incompatible with reflow soldering.
2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

1. Mild "no-clean" fluxes are recommended for use in soldering.
2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts. And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the Displays.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.

