74AXP1G06

Low-power inverter with open-drain output Rev. 1 — 15 January 2014

Product data sheet

General description 1.

The 74AXP1G06 is a single inverter with open-drain output.

Schmitt-trigger action at the input makes the circuit tolerant of slower input rise and fall times.

This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.7 V to 2.75 V. It is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

Features and benefits 2.

- Wide supply voltage range from 0.7 V to 2.75 V
- Low input capacitance; C_I = 0.5 pF (typical)
- Low output capacitance; C_O = 0.7 pF (typical)
- Low dynamic power consumption; $C_{PD} = 1.0 \text{ pF}$ at $V_{CC} = 1.2 \text{ V}$ (typical)
- Low static power consumption; $I_{CC} = 0.6 \mu A (85 \, ^{\circ}C \text{ maximum})$
- High noise immunity
- Complies with JEDEC standard:
 - ◆ JESD8-12A.01 (1.1 V to 1.3 V)
 - ◆ JESD8-11A.01 (1.4 V to 1.6 V)
 - ◆ JESD8-7A (1.65 V to 1.95 V)
 - ◆ JESD8-5A.01 (2.3 V to 2.7 V)
- ESD protection:
 - ♦ HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 2 kV
 - CDM JESD22-C101E exceeds 1000 V
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Input accepts voltages up to 2.75 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C



Low-power inverter with open-drain output

3. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range Name		Description	Version			
74AXP1G06GM	–40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886			
74AXP1G06GN	–40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 \times 1.0 \times 0.35 mm	SOT1115			
74AXP1G06GS	–40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 \times 1.0 \times 0.35 mm	SOT1202			
74AXP1G06GX	–40 °C to +85 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226			

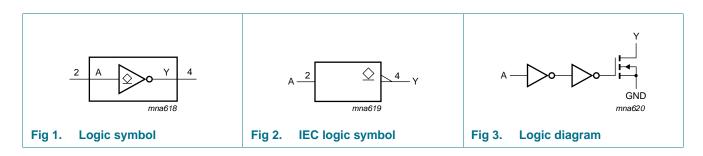
4. Marking

Table 2. Marking

Type number	Marking code ^[1]
74AXP1G06GM	rR
74AXP1G06GN	rR
74AXP1G06GS	rR
74AXP1G06GX	rR

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

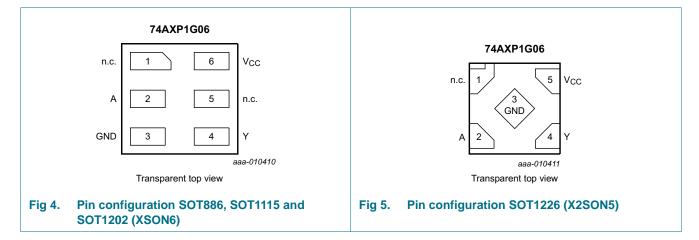
5. Functional diagram



Low-power inverter with open-drain output

6. Pinning information

6.1 Pinning



6.2 Pin description

Table 3. Pin description

Symbol	Pin		Description	
	X2SON5	XSON6		
n.c.	1	1	not connected	
A	2	2	data input	
GND	3	3	ground (0 V)	
Υ	4	4	data output	
n.c.	-	5	not connected	
V _{CC}	5	6	supply voltage	

7. Functional description

Table 4. Function table[1]

Input	Output
A	Υ
L	Z
Н	L

^[1] H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

Low-power inverter with open-drain output

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+3.3	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage		<u>[1]</u> –0.5	+3.3	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage		<u>[1]</u> –0.5	+3.3	V
I _O	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.7	2.75	V
V_{I}	input voltage		0	2.75	V
Vo	output voltage	Active mode	0	V_{CC}	V
		Power-down mode; V _{CC} = 0 V	0	2.75	V
T _{amb}	ambient temperature		-40	+85	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.7 \text{ V to } 2.75 \text{ V}$	0	200	ns/V

Low-power inverter with open-drain output

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions, unless otherwise specified; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions			$T_{amb} = -40$	°C to +85 °C		Unit
				Min	Typ 25 °C	Max 25 °C	Max 85 °C	
	HIGH-level input	$V_{CC} = 0.75 \text{ V to } 0.85 \text{ V}$		$0.75V_{CC}$	-	-	-	V
	voltage	V _{CC} = 1.1 V to 1.95 V		0.65V _{CC}	-	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.6	-	-	-	V
V_{IL}	LOW-level input	$V_{CC} = 0.75 \text{ V to } 0.85 \text{ V}$		-	-	$0.25V_{CC}$	0.25V _{CC}	V
	voltage	V _{CC} = 1.1 V to 1.95 V		-	-	$0.35V_{CC}$	0.35V _{CC}	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		-	-	0.7	0.7	V
V _{OL}	LOW-level output	$I_O = 20 \mu A; V_{CC} = 0.7 V$		-	0.01	-	-	V
	voltage	$I_O = 100 \mu A; V_{CC} = 0.75 V$		-	-	0.1	0.1	V
		$I_O = 2 \text{ mA}; V_{CC} = 1.1 \text{ V}$		-	-	0.275	0.275	V
		$I_O = 3 \text{ mA}; V_{CC} = 1.4 \text{ V}$		-	-	0.35	0.35	V
		$I_O = 4.5 \text{ mA}; V_{CC} = 1.65 \text{ V}$		-	-	0.45	0.45	V
		$I_O = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$		-	-	0.7	0.7	V
l _l	input leakage current	$V_I = 0 \text{ V to } 2.75 \text{ V};$ $V_{CC} = 0 \text{ V to } 2.75 \text{ V}$	[1]	-	0.001	±0.1	±0.5	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IL}; V_O = 0 V \text{ to } 2.75 V$	[1]	-	0.02	±0.1	±0.5	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0$ V to 2.75 V; $V_{CC} = 0$ V	<u>[1]</u>	-	0.01	±0.1	±0.5	μΑ
ΔI_{OFF}	additional power-off leakage current	V_{I} or $V_{O} = 0$ V or 2.75 V; $V_{CC} = 0$ V to 0.1 V	[1]	-	0.02	±0.1	±0.5	μΑ
I _{CC}	supply current	$V_I = 0 \text{ V or } V_{CC}; I_O = 0 \text{ A}$	[1]	-	0.01	0.3	0.6	μΑ
ΔI_{CC}	additional supply current	$\begin{aligned} &V_{I} = V_{CC} - 0.5 \text{ V}; \text{ I}_{O} = 0 \text{ A}; \\ &V_{CC} = 2.5 \text{ V} \end{aligned}$		-	2	100	150	μΑ

^[1] All typical values are measured at V_{CC} = 1.2 V.

Low-power inverter with open-drain output

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit, see Figure 12.

Symbol	Parameter	Conditions		T _{amb} = 25 °C			$T_{amb} = -40$ °C to +85 °C		Unit
				Min	Typ[1]	Max	Min	Max	
t _{pd}	propagation	A to Y; see Figure 6	[2][3]				•		
	delay	$V_{CC} = 0.75 \text{ V to } 0.85 \text{ V}$		3	12	33	3	104	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.2	5.1	7.9	2.0	8.3	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.7	3.7	5.2	1.5	5.6	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.4	3.5	5.3	1.2	5.6	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.2	2.6	3.8	1.0	4.0	ns
t _t	transition time	V _{CC} = 2.7 V; see <u>Figure 6</u>	<u>[4]</u>	-	-	-	0.9	-	ns
Cı	input capacitance	$V_I = 0 \text{ V or } V_{CC};$ $V_{CC} = 0 \text{ V to } 2.75 \text{ V}$		-	0.5	-	-	-	pF
Co	output capacitance	$V_{O} = 0 \text{ V}; V_{CC} = 0 \text{ V}$		-	0.7	-	-	-	pF
C_{PD}		$f_i = 1 \text{ MHz}; V_I = 0 \text{ V to } V_{CC}$	<u>[5]</u>						
	capacitance	$V_{CC} = 0.75 \text{ V to } 0.85 \text{ V}$		-	0.9	-	-	-	pF
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		-	1.0	-	-	-	pF
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		-	1.0	-	-	-	pF
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		-	1.1	-	-	-	pF
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		-	1.3	-	-	-	pF

^[1] All typical values are measured at nominal V_{CC} .

$$P_D = C_{PD} \times V_{CC}{}^2 \times f_i + C_L \times V_{CC}{}^2 \times f_o \text{ where:}$$

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V.

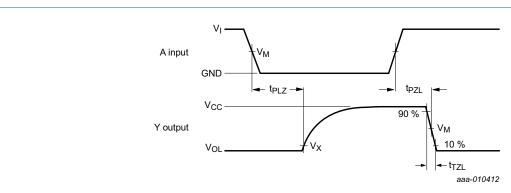
^[2] t_{pd} is the same as t_{PZL} and t_{PLZ} .

^[3] For additional propagation delay (t_{PZL}) values at different load capacitances see Figure 7 to Figure 11.

^[4] t_t is the same as t_{TZL} and t_{TLZ} .

^[5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

12. Waveforms



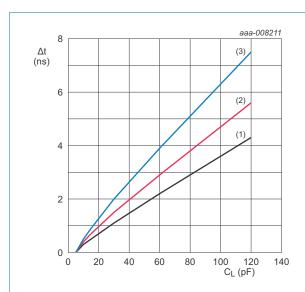
Measurement points are given in Table 9.

 V_{OL} is the typical output voltage level that occurs at the output load.

Fig 6. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	y voltage Input Output		Input		
V _{CC}	V _M	VI	$t_r = t_f$	V _M	V _X
0.75 V to 1.6 V	0.5V _{CC}	V _{CC}	≤ 3.0 ns	0.5V _{CC}	$V_{OL} + 0.1 V$
1.65 V to 2.7 V	0.5V _{CC}	V_{CC}	≤ 3.0 ns	0.5V _{CC}	$V_{OL} + 0.15 V$



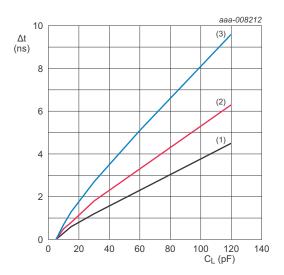
 $T_{amb} = -40 \, ^{\circ}\text{C}$ to +85 $^{\circ}\text{C}$ unless otherwise specified.

(1) Minimum: $V_{CC} = 2.7 \text{ V}$

(2) Typical: T_{amb} = 25 °C; V_{CC} = 2.5 V

(3) Maximum: $V_{CC} = 2.3 \text{ V}$

Fig 7. Additional t_{PZL} versus load capacitance



 T_{amb} = -40 °C to +85 °C unless otherwise specified.

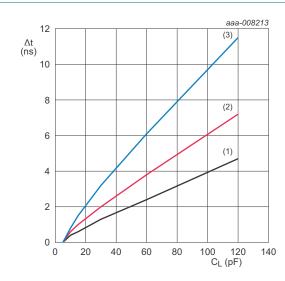
(1) Minimum: $V_{CC} = 1.95 \text{ V}$

(2) Typical: T_{amb} = 25 °C; V_{CC} = 1.8 V

(3) Maximum: V_{CC} = 1.65 V

Fig 8. Additional t_{PZL} versus load capacitance

Low-power inverter with open-drain output



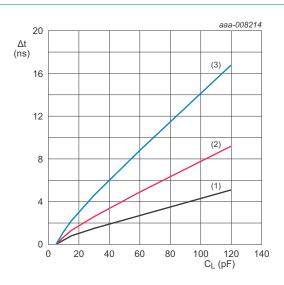
 T_{amb} = -40 °C to +85 °C unless otherwise specified.

(1) Minimum: $V_{CC} = 1.6 \text{ V}$

(2) Typical: T_{amb} = 25 °C; V_{CC} = 1.5 V

(3) Maximum: $V_{CC} = 1.4 \text{ V}$

Fig 9. Additional t_{PZL} versus load capacitance



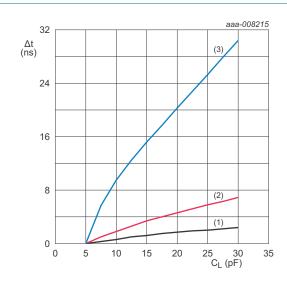
 $T_{amb} = -40$ °C to +85 °C unless otherwise specified.

(1) Minimum: $V_{CC} = 1.3 \text{ V}$

(2) Typical: $T_{amb} = 25 \, ^{\circ}C$; $V_{CC} = 1.2 \, V$

(3) Maximum: $V_{CC} = 1.1 \text{ V}$

Fig 10. Additional t_{PZL} versus load capacitance



 $T_{amb} = -40 \, ^{\circ}\text{C}$ to +85 $^{\circ}\text{C}$ unless otherwise specified.

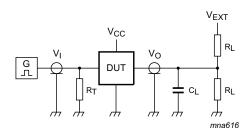
(1) Minimum: $V_{CC} = 0.85 \text{ V}$

(2) Typical: $T_{amb} = 25 \,^{\circ}C$; $V_{CC} = 0.8 \,^{\circ}V$

(3) Maximum: $V_{CC} = 0.75 \text{ V}$

Fig 11. Additional t_{PZL} versus load capacitance

Low-power inverter with open-drain output



Test data is given in <u>Table 10</u>.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig 12. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}	
V _{CC}	C _L	R _L	t _{PLH} , t _{PHL}	t _{PZL} , t _{PLZ}
0.75 V to 2.7 V	5 pF	10 kΩ	0 V	$2 \times V_{CC}$

13. Package outline

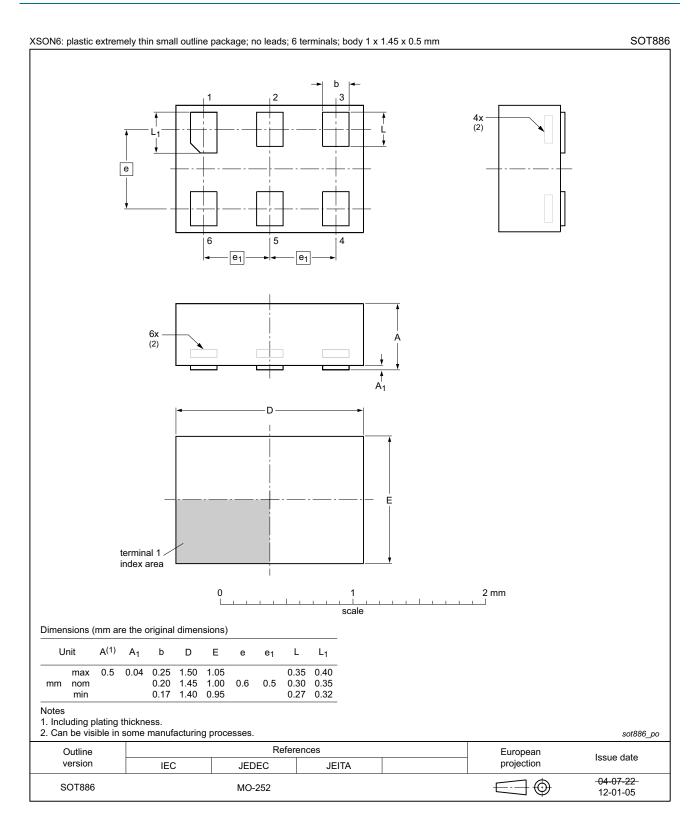


Fig 13. Package outline SOT886 (XSON6)

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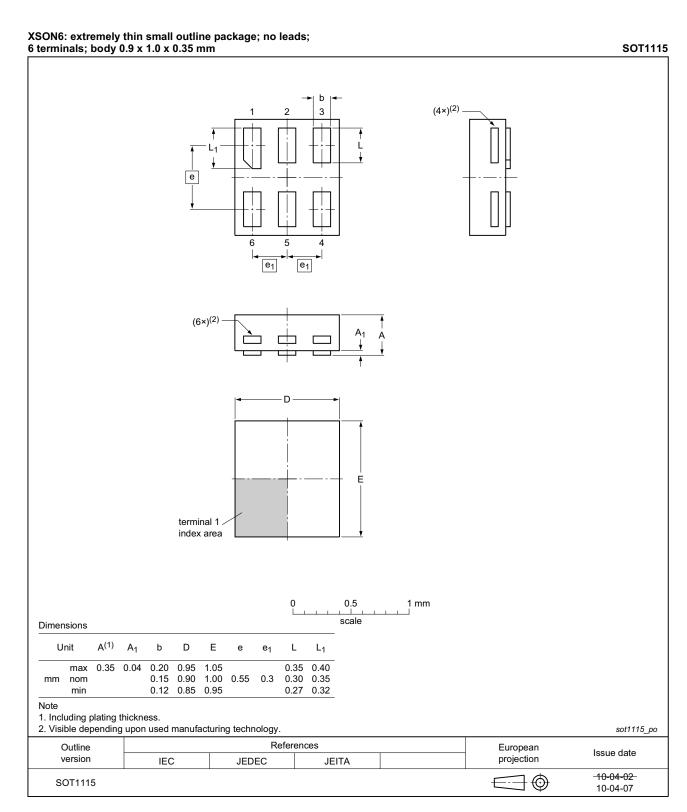


Fig 14. Package outline SOT1115 (XSON6)

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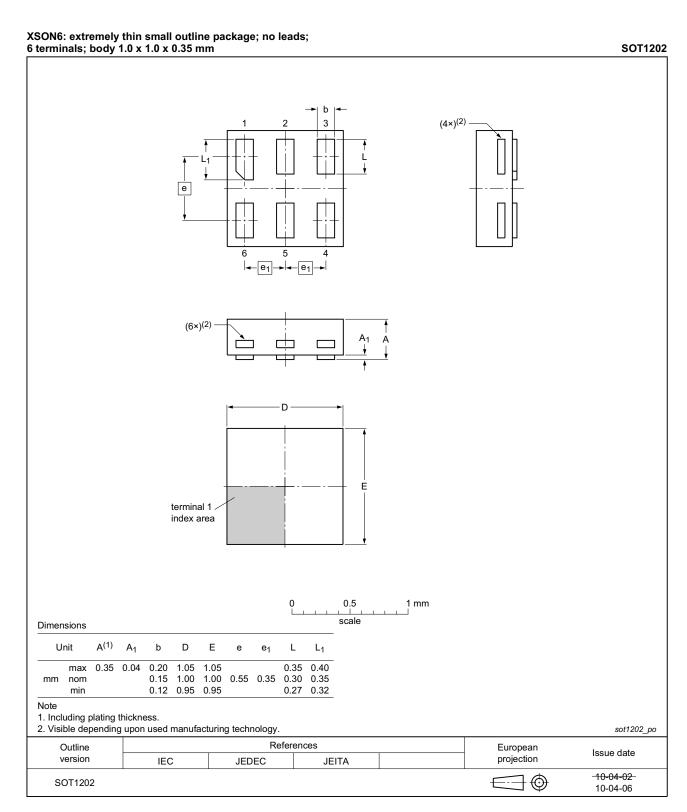


Fig 15. Package outline SOT1202 (XSON6)

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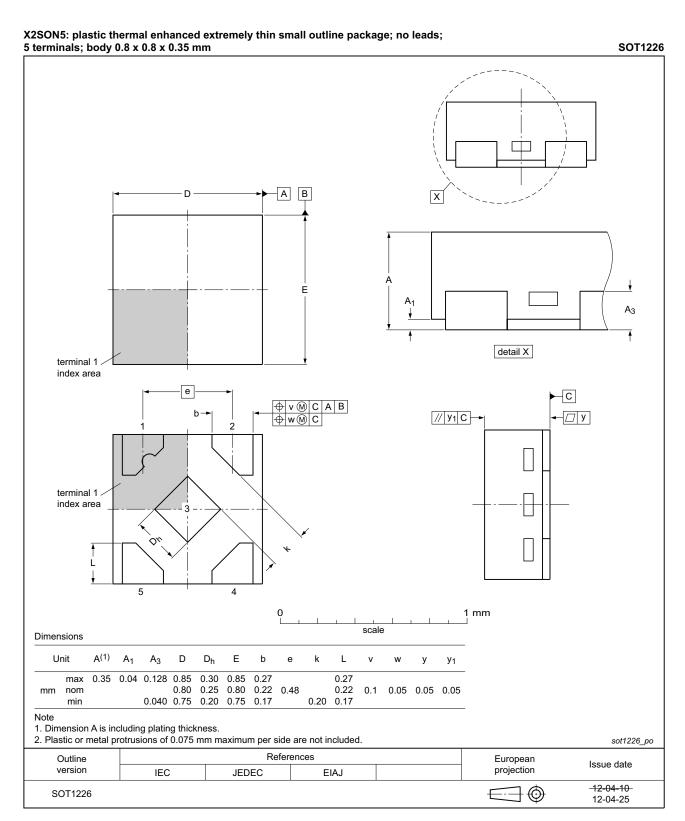


Fig 16. Package outline SOT1226 (X2SON5)

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Low-power inverter with open-drain output

14. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model

15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AXP1G06 v.1	20140115	Product data sheet	-	-

Low-power inverter with open-drain output

16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Low-power inverter with open-drain output

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Low-power inverter with open-drain output

18. Contents

1	General description	. 1
2	Features and benefits	. 1
3	Ordering information	. 2
4	Marking	. 2
5	Functional diagram	. 2
6	Pinning information	. 3
6.1	Pinning	. 3
6.2	Pin description	. 3
7	Functional description	. 3
8	Limiting values	. 4
9	Recommended operating conditions	. 4
10	Static characteristics	. 5
11	Dynamic characteristics	. 6
12	Waveforms	. 7
13	Package outline	10
14	Abbreviations	14
15	Revision history	14
16	Legal information	15
16.1	Data sheet status	15
16.2	Definitions	15
16.3	Disclaimers	15
16.4	Trademarks	16
17	Contact information	16
18	Contents	17

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74AXP1G06 Packaging Information





Type Number	Orderable Part Number	Package Name
74AXP1G06GM	74AXP1G06GMH	XSON6
74AXP1G06GN	74AXP1G06GNH	XSON6
74AXP1G06GS	74AXP1G06GSH	XSON6
74AXP1G06GX	74AXP1G06GXH	X2SON5