

PESD1IVN-U

In-vehicle network ESD protection diode

15 July 2015

Product data sheet

1. General description

ElectroStatic Discharge (ESD) protection diode in a small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package designed to protect one automotive in-vehicle network line from the damage caused by ESD and other transients.

2. Features and benefits

- One small SOT323 package to protect one in-vehicle network line
- Low clamping voltage: V_{CL} = 38 V at I_{PP} = 1 A
- ESD protection up to 18 kV; IEC 61000-4-2, level 4
- IEC 61000-4-5 (surge); I_{PP} = 3 A at t_p = 8/20 μ s
- AEC-Q101 qualified

3. Applications

- In-vehicle network ESD protection for CAN, LIN, FlexRay and Single Edge Nibble Transmission (SENT) interfaces
- Generic automotive applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	26.5	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	-	8.5	11	pF





In-vehicle network ESD protection diode

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode 1]3	1 4 8
2	n.c.	not connected		
3	K2	cathode 2		2 — aaa-018783
			1	

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PESD1IVN-U	SC-70	plastic surface-mounted package; 3 leads	SOT323			

7. Marking

Table 4. Marking codes

3	
Type number	Marking code
	[1]
PESD1IVN-U	3X%

[1] % = placeholder for manufacturing site code

In-vehicle network ESD protection diode

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1][2]	-	150	W
I _{PPM}	rated peak pulse current		[1][2]	-	3	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximur	n ratings					,
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2][3]	-	18	kV
		MIL-STD-883 (human body model)	[2][3]	-	10	kV

- Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.
- [2] Measured from pin 1 to 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

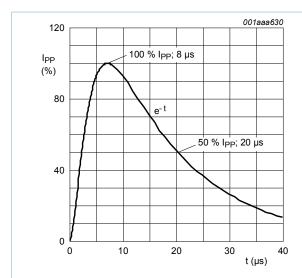


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5 and IEC 61643-321

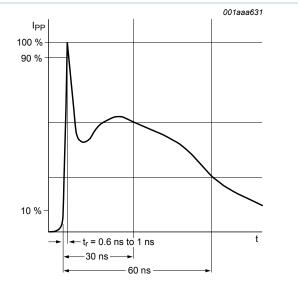
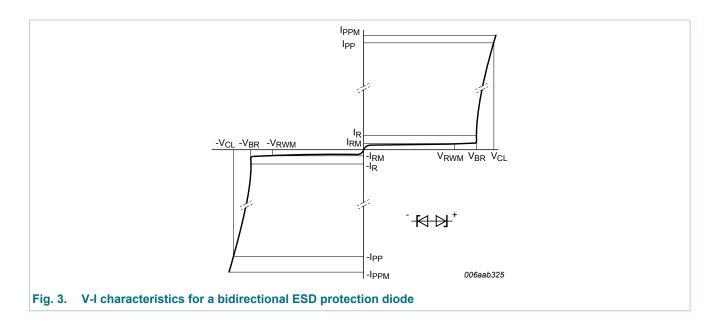


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

In-vehicle network ESD protection diode



9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	26.5	V
I _{RM}	reverse leakage current	V _{RWM} = 26.5 V; T _{amb} = 25 °C		-	1	50	nA
V_{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		28	30	32	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	8.5	11	pF
		$f = 1 \text{ MHz}; V_R = 2.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	6.6	-	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _{amb} = 25 °C	[1][2]	-	-	38	V
		I _{PPM} = 3 A; T _{amb} = 25 °C	[1][2]	-	-	53	V
R _{dyn}	dynamic resistance	I _R = 20 A; T _{amb} = 25 °C	[3]	-	2	-	Ω

Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

^[2] Measured from pin 1 to 3.

³ Non-repetitive current pulse, Transmission line Pulse (TLP), square pulse, ANSI/ESD STM5.5.1-2008.

In-vehicle network ESD protection diode

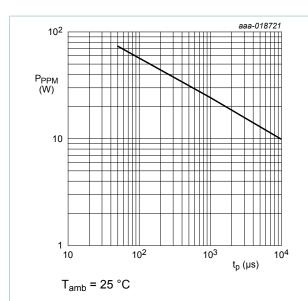


Fig. 4. Rated peak pulse power as a function of square pulse duration; typical values

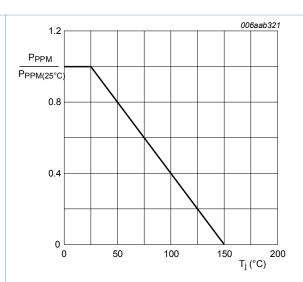
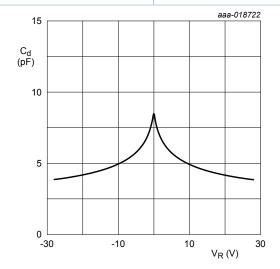


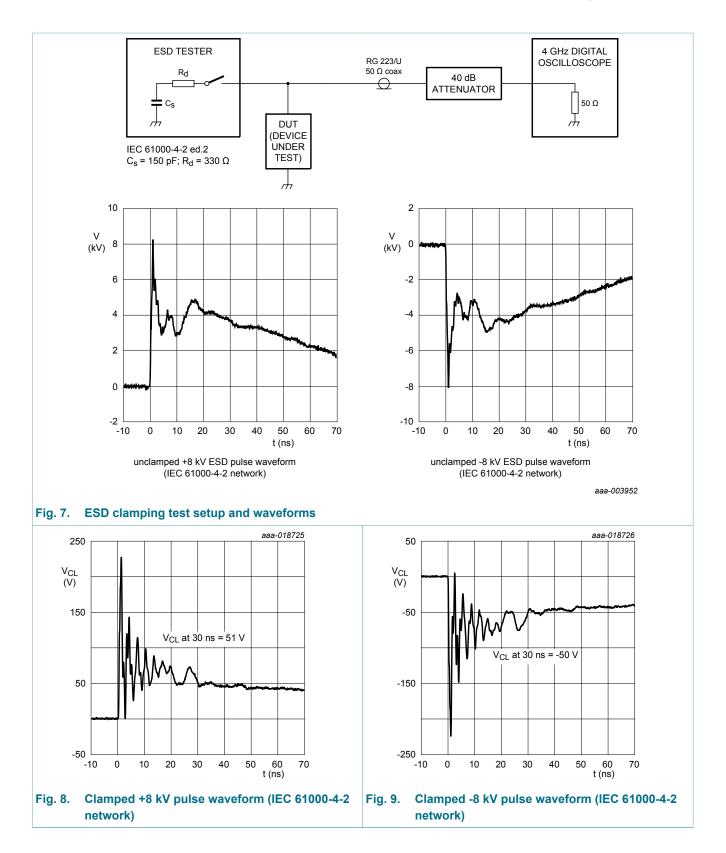
Fig. 5. Relative variation of rated peak pulse power as a function of junction temperature; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$

Fig. 6. Diode capacitance as a function of reverse voltage; typical values

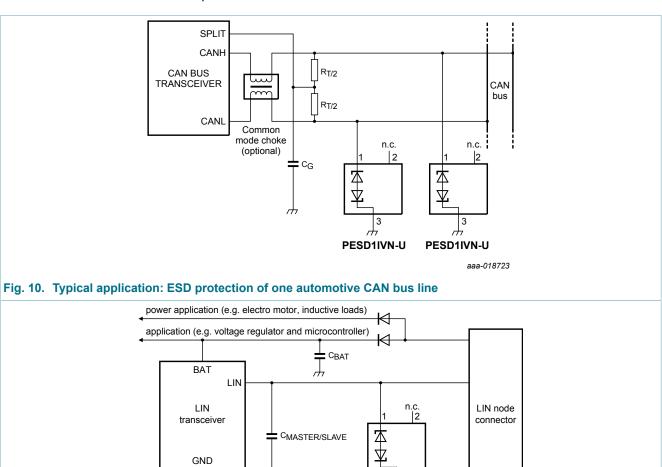
In-vehicle network ESD protection diode



In-vehicle network ESD protection diode

10. Application information

The device is designed for the protection of one automotive in-vehicle network bus line from surge pulses and ESD damage. The device provides a surge capability of up to 3 A for an $8/20~\mu s$ waveform.



Circuit board layout and protection device placement

Fig. 11. Typical application: ESD protection of one automotive LIN bus line

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

PESD1IVN-U

aaa-018724

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.

PESD1IVN-U

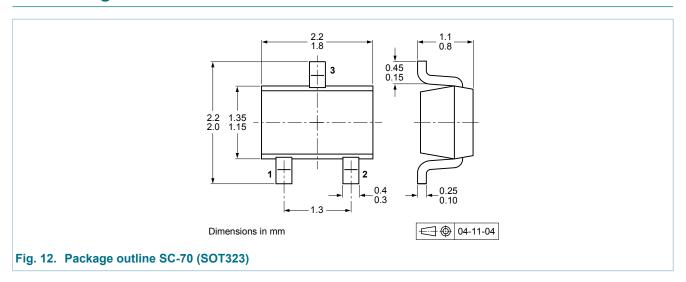
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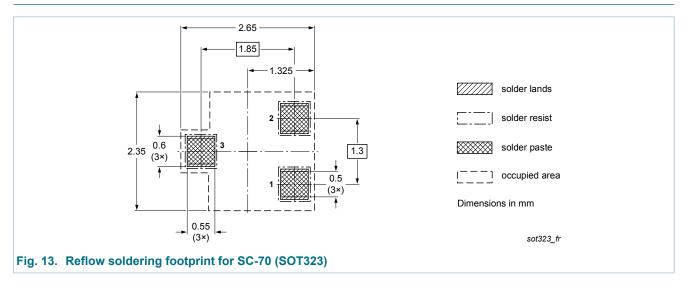
In-vehicle network ESD protection diode

- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

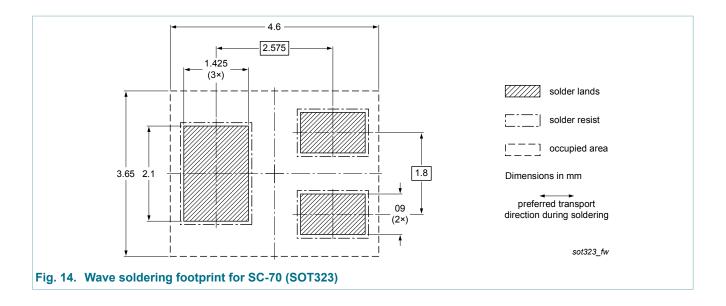
11. Package outline



12. Soldering



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In-vehicle network ESD protection diode

13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD1IVN-U v.1	20150715	Product data sheet	-	-

In-vehicle network ESD protection diode

14. Legal information

14.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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In-vehicle network ESD protection diode

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PESD1IVN-U

In-vehicle network ESD protection diode

15. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Characteristics	4
10	Application information	7
11	Package outline	8
12	Soldering	8
13	Revision history	10
14	Legal information	11
14.1	Data sheet status	
14.2	Definitions	11
14.3	Disclaimers	11
14.4	Trademarks	12

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