

Part Number: L-7679C1SYC-H



Technical Data

Features:

- *High luminance output.
- *Design for high current operation.
- *Uniform color.
- *Low power consumption.
- *Low thermal resistance.
- *Low profile.
- *Packaged in tubes for use with automatic insertion equipment.
- *Soldering methods: wave soldering.
- *RoHS Compliant.

Benefits:

- *Outstanding Material Efficiency.
- *Electricity savings.
- *Maintenance savings.
- *Reliable and Rugged.

Typical Applications:

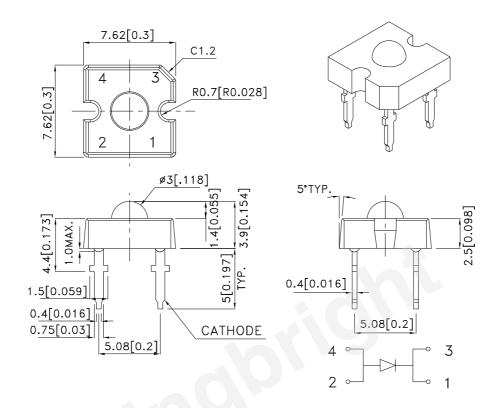
- *Automotive Exterior Lighting.
- *Electronic Signs and Signals.
- *Specialty Lighting.





SPEC NO: DSAE6462 APPROVED: WYNEC REV NO: V.14B CHECKED: Allen Liu DATE: MAY/09/2013 DRAWN: Q.M.CHEN PAGE: 1 OF 7 ERP: 1101012192

Outline Drawings



Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.25(0.01") unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.
- 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

Absolute Maximum Ratings at TA=25°C

PARAMETER	SY-H	UNITS	
DC Forward Current	70	mA	
Power dissipation	245	mW	
Reverse Voltage	5	V	
Operating Temperature	-40 To +85	°C	
Storage Temperature	-55 To +85	°C	
Lead Solder Temperature[1]	260°C For 5 Seconds		

1.1.5mm[0.06inch]below seating plane. NO Reflow soldering .

SPEC NO: DSAE6462 REV NO: V.14B DATE: MAY/09/2013 PAGE: 2 OF 7

APPROVED: WYNEC CHECKED: Allen Liu DRAWN: Q.M.CHEN ERP: 1101012192

Selection Guide

Part No.	LED COLOR	lv(cd)[1] @70mA Min. Typ.		Фv(lm)[1] @70mA Тур.	Viewing Angle[2] 201/2 Typ.
L-7679C1SYC-H	Super Bright Yellow (AlGaInP)	2.3	4.2	5.1	70°

Notes:

Optical Characteristics at TA=25°C I_F=70mA Rθj-a=200°C/W

DEVICE TYPE	PEAK WAVELENGTH λPEAK (nm) TYP.	DOMINANT[1] WAVELENGTH λDOM (nm) TYP.	SPECTRAL LINE WAVELENGTH Δλ1/2(nm) TYP.
SY-H	590	589	20

Electrical Characteristics at TA=25°C

DEVICE TYPE	FORWARD VOLTAGE [1] VF (VOLTS) @ IF=70mA		REVERSE CURRENT IR (uA) @ VR=5V	CAPACITANCE C (pF) @ V _F =0V F=1MHZ	THERMAL RESISTANCE Rθj -pin °C/W	
	MIN.	TYP.	MAX.	MAX.	TYP.	TYP.
SY-H	2.6	2.9	3.5	10	45	125

Note:

SPEC NO: DSAE6462 REV NO: V.14B **DATE: MAY/09/2013** PAGE: 3 OF 7 **CHECKED: Allen Liu** ERP: 1101012192 **APPROVED: WYNEC** DRAWN: Q.M.CHEN

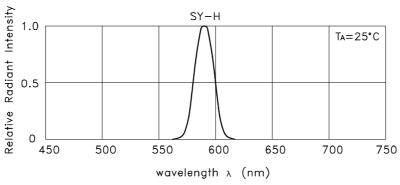
^{1.}Luminous intensity is measured with an integrating sphere after the device has stabilized; Luminous Intensity / luminous flux: +/-15%. 2.01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value. 3.Luminous Intensity / luminous flux value is traceable to the CIE127-2007 compliant national standards

^{1.}The dominant wavelength is derived from the CIE Chromaticity Diagram and represents the perceived color of the device; Wavelength: +/-1nm.

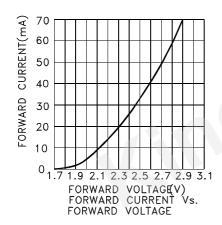
^{2.} Wavelength value is traceable to the CIE127-2007 compliant national standards.

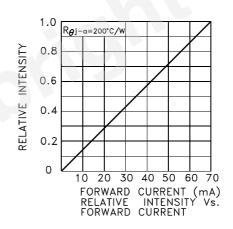
^{1.} Forward Voltage: +/-0.1V.

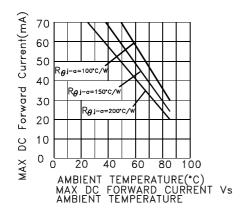
Figures

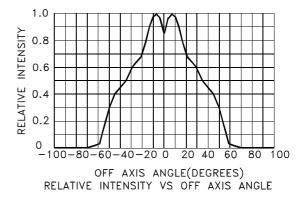


RELATIVE INTENSITY Vs. WAVELENGTH



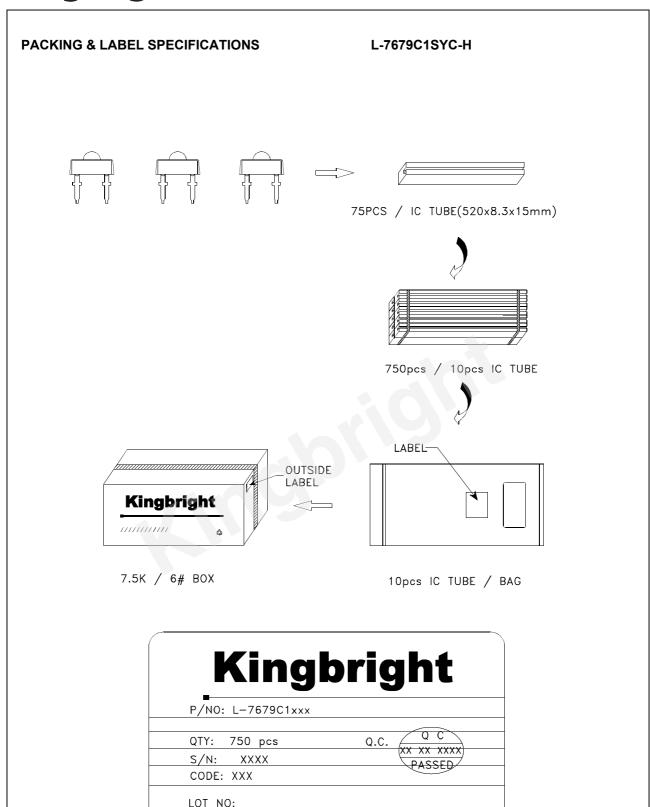






SPEC NO: DSAE6462 REV NO: V.14B DATE: MAY/09/2013 PAGE: 4 OF 7

APPROVED: WYNEC CHECKED: Allen Liu DRAWN: Q.M.CHEN ERP: 1101012192



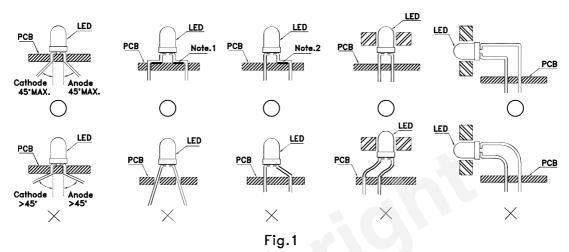
SPEC NO: DSAE6462 APPROVED: WYNEC REV NO: V.14B CHECKED: Allen Liu DATE: MAY/09/2013 DRAWN: Q.M.CHEN

RoHS Compliant

PAGE: 5 OF 7 ERP: 1101012192

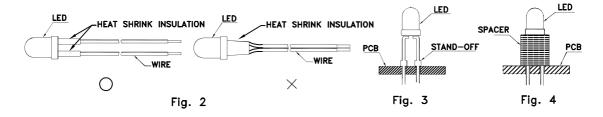
PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



" \bigcirc " Correct mounting method "imes" Incorrect mounting method

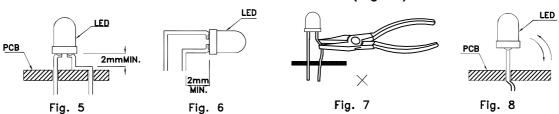
- 2. When soldering wire to the LED, use individual heat—shrink tubing to insulate the exposed leads to prevent accidental contact short—circuit. (Fig.2)
- 3.Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



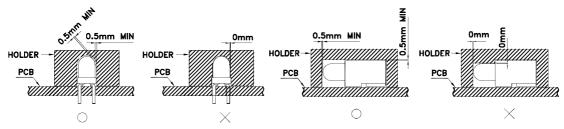
- 4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

SPEC NO: DSAE6462 APPROVED: WYNEC REV NO: V.14B CHECKED: Allen Liu DATE: MAY/09/2013 DRAWN: Q.M.CHEN PAGE: 6 OF 7 ERP: 1101012192

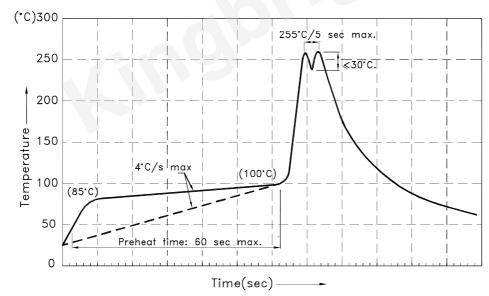
6. Do not bend the leads more than twice. (Fig. 8)



7. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.



- 8. The tip of the soldering iron should never touch the lens epoxy.
- 9. Through-hole LEDs are incompatible with reflow soldering.
- 10. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.
- 11. Recommended Wave Soldering Profiles:



Notes:

- 1.Recommend pre—heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2.Peak wave soldering temperature between 245°C \sim 255°C for 3 sec (5 sec max).
- 3.Do not apply stress to the epoxy resin while the temperature is above 85°C.
- 4.Fixtures should not incur stress on the component when mounting and during soldering process.
- 5.SAC 305 solder alloy is recommended.
- 6.No more than one wave soldering pass.

Detailed application notes are listed on our website.

http://www.kingbright.com/application notes

SPEC NO: DSAE6462 REV NO: V.14B DATE: MAY/09/2013 PAGE: 7 OF 7

APPROVED: WYNEC CHECKED: Allen Liu DRAWN: Q.M.CHEN ERP: 1101012192