

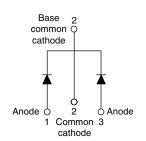
VS-15CTQ...PbF Series, VS-15CTQ...-N3 Series

Vishay Semiconductors

Schottky Rectifier, 2 x 7.5 A



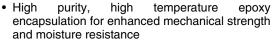
TO-220AB

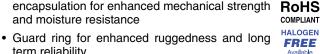


PRODUCT SUMMARY				
Package	TO-220AB			
I _{F(AV)}	2 x 7.5 A			
V_{R}	35 V, 40 V, 45 V			
V _F at I _F	0.51 V			
I _{RM} max.	32 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	10 mJ			

FEATURES

- 150 °C T_J operation
- · Low forward voltage drop
- · High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



term reliability

The VS-15CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	15	А		
V_{RRM}	Range	35 to 45	V		
I _{FSM}	t _p = 5 μs sine	810	Α		
V _F	7.5 A _{pk} , T _J = 125 °C (per leg)	0.51	V		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 15CTQ035PbF	VS- 15CTQ035-N3	VS- 15CTQ040PbF	VS- 15CTQ040-N3	VS- 15CTQ045PbF	VS- 15CTQ045-N3	UNITS
Maximum DC reverse voltage	V _R							
Maximum working peak reverse voltage	V _{RWM}	35	35	40	40	45	45	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 123 °C, rectangular waveform		15	А
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	810	Α
non-repetitive surge current per leg I _{FSM} See fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	145	, ,
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		10	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.5	Α



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
	V _{FM} ⁽¹⁾	7.5 A	T _J = 25 °C	0.55	V	
Maximum forward voltage drop per leg See fig. 1		15 A		0.70		
		7.5 A	T _J = 125 °C	0.51		
		15 A		0.65		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	0.8	mA	
See fig. 2		T _J = 125 °C	v _R = nateu v _R	32	IIIA	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		400	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg	D	DC operation See fig. 4	3.50		
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	1.75	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight			2	g	
Approximate weight			0.07	OZ.	
	ninimum		6 (5)	kgf · cm	
Mounting torque m	aximum		12 (10)	(lbf · in)	
		Case style TO-220AB	15CT	Q035	
Marking device			15CT	Q040	
			15CT	Q045	



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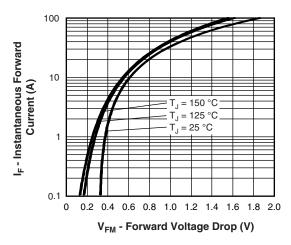


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

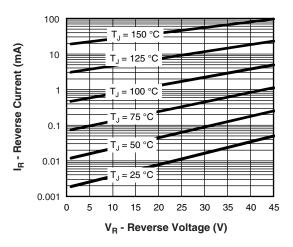


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

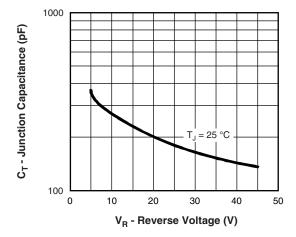


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

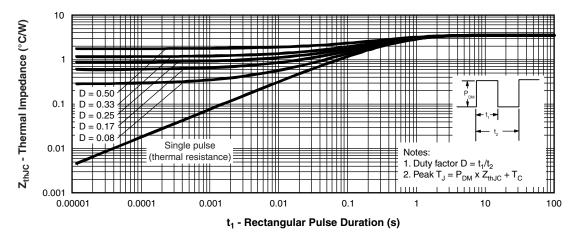


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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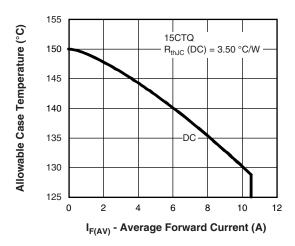


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

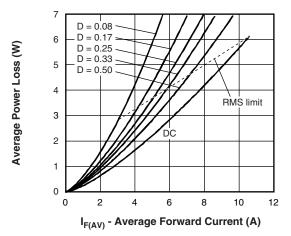


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

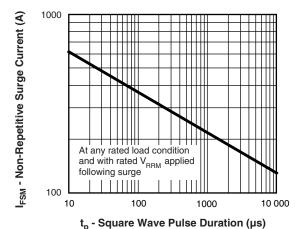


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

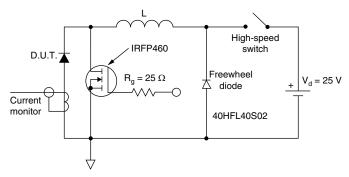


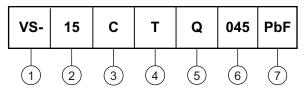
Fig. 8 - Unclamped Inductive Test Circuit

VS-15CTQ...PbF Series, VS-15CTQ...-N3 Series

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (10 = 10 A)

3 - Circuit configuration

C = Common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (150 = 150 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-15CTQ035PbF	50	1000	Antistatic plastic tube	
VS-15CTQ035-N3	50	1000	Antistatic plastic tube	
VS-15CTQ040PbF	50	1000	Antistatic plastic tube	
VS-15CTQ040-N3	50	1000	Antistatic plastic tube	
VS-15CTQ045PbF	50	1000	Antistatic plastic tube	
VS-15CTQ045-N3	50	1000	Antistatic plastic tube	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95222</u>				
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		



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