

# Hyperfast Rectifier, 8 A FRED Pt®



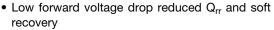


TO-252AA (D-PAK)

PRODUCT SUMMARY							
Package	TO-252AA (D-PAK)						
I <sub>F(AV)</sub>	8 A						
$V_{R}$	200 V						
V <sub>F</sub> at I <sub>F</sub>	0.75 V						
t <sub>rr</sub> (typ.)	23 ns						
T <sub>J</sub> max.	175 °C						
Diode variation	Single die						

#### **FEATURES**

- · Hyperfast recovery time
- 175 °C max. operating junction temperature
- · Output rectification freewheeling





COMPLIANT HALOGEN

FREE

- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	$V_{RRM}$		200	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 156 °C	8						
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	140	Α					
Peak repetitive forward current	I <sub>FM</sub>	$T_C = 156  ^{\circ}\text{C},  f = 20  \text{kHz},  d = 50  \%$	16						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	200	-	-				
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A	-	0.91	0.97	V			
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	0.75	0.85				
Reverse leakage current	I <sub>R</sub>	$V_R = V_R$ rated	-	-	5				
neverse leakage current		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	6	60	μA			
Junction capacitance	Junction capacitance C <sub>T</sub> V <sub>R</sub> = 600 V		-	22	-	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	_	nH			



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt =	100 A/μs, V <sub>R</sub> = 30 V	-	23	27			
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	27	-			
		T <sub>J</sub> = 25 °C		-	24	-	ns		
		T <sub>J</sub> = 125 °C		-	33	-			
Dools recovery average	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A	=	2.3	-	۸		
Peak recovery current		T <sub>J</sub> = 125 °C	$dI_F/dt = 200 A/\mu s$ $V_B = 160 V$	=	4.3	-	A		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	27	-	200		
		T <sub>J</sub> = 125 °C		=	70	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C			
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.7	2.5	°C/W			
Approximate weight				0.3		g			
Approximate weight				0.01		OZ.			
Marking device		Case style D-PAK (TO-252AA)		8EWH	102FN				

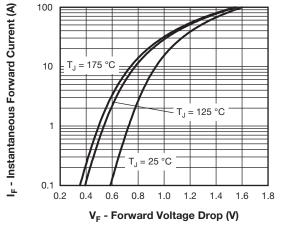


Fig. 1 - Typical Forward Voltage Drop Characteristics

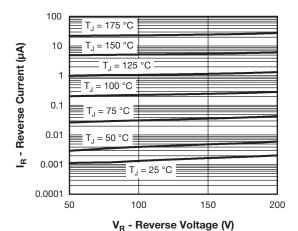


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

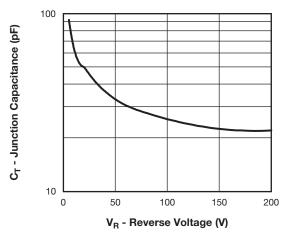


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

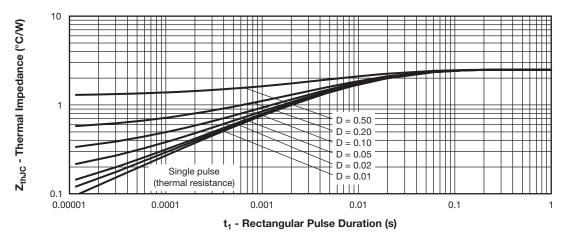


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

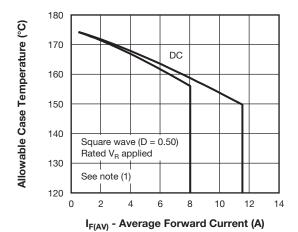


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

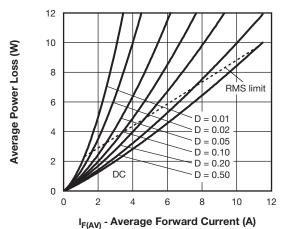


Fig. 6 - Forward Power Loss Characteristics

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## Vishay Semiconductors

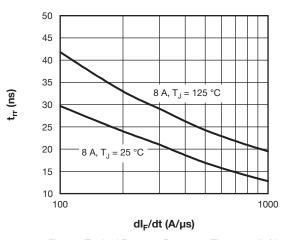


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

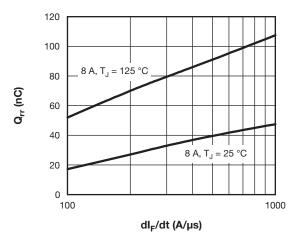


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

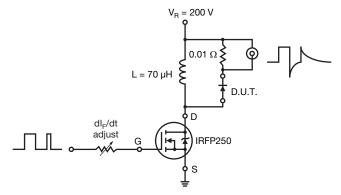
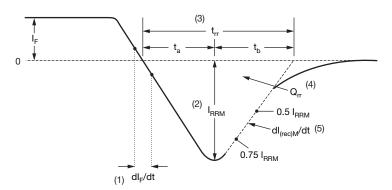


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

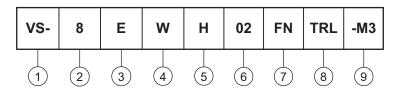
(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions



#### **ORDERING INFORMATION TABLE**





1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

3 - Circuit configuration:

E = single diode

4 - Package identifier:

W = D-PAK

5 - H = hyperfast recovery

6 - Voltage rating (02 = 200 V)

7 - FN = TO-252AA

8 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-8EWH02FN-M3	75	3000	Antistatic plastic tube						
VS-8EWH02FNTR-M3	2000	2000	13" diameter reel						
VS-8EWH02FNTRL-M3	3000	3000	13" diameter reel						
VS-8EWH02FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95384					



**NOTES** 

3

2

MAX.

0.410

0.070

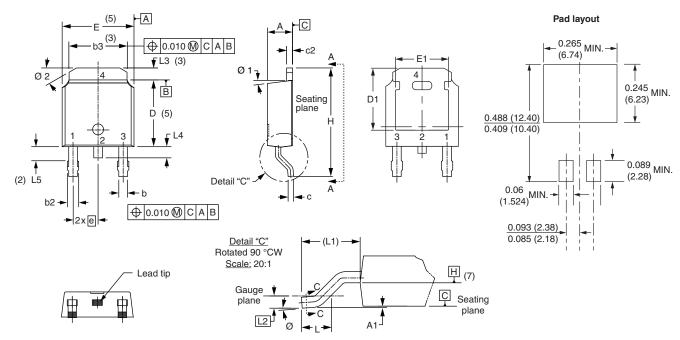
0.050

0.040

0.060

# **D-PAK (TO-252AA)**

#### **DIMENSIONS** in millimeters and inches



Ī	SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		
	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX
ſ	Α	2.18	2.39	0.086	0.094		e 2.29 BSC		2.29 BSC		BSC	
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020 BSC	
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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