Power MOSFET for 1-2 Cells Lithium-ion Battery Protection 24 V, 11.6 m Ω , 10 A, Dual N-Channel

This Power MOSFET features a low on-state resistance. This device is suitable for applications such as power switches of portable machines. Best suited for 1-2 cells Lithium-ion Battery applications.



- Low On-Resistance
- 2.5 V drive
- Common-Drain Type
- ESD Diode-Protected Gate
- Built-in Gate Protection Resistor
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

• 1-2 cells Lithium-ion Battery Charging and Discharging Switch

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	24	V
Gate to Source Voltage	VGSS	±12.5	٧
Drain Current (DC)	ID	10	Α
Drain Current (Pulse) PW ≤ 10 μs, duty cycle ≤ 1%	IDP	60	Α
Power Dissipation Surface mounted on ceramic substrate (1000 mm² × 0.8 mm) 1 unit	PD	1.5	W
Total Dissipation Surface mounted on ceramic substrate (1000 mm² × 0.8 mm)	PŢ	1.6	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

THERMAE REGISTANCE RATINGS						
Parameter	Symbol	Value	Unit			
Junction to Ambient						
Surface mounted on ceramic substrate	$R_{\theta JA}$	83.3	°C/W			
(1000 mm ² × 0.8 mm) 1 unit						

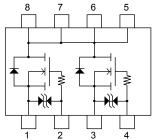


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VDSS	RDS(on) Max	ID Max
24 V	11.6 mΩ @ 4.5 V	
	12.6 mΩ @ 4.0 V	10.4
	15 mΩ @ 3.1 V	10 A
	17.5 mΩ @ 2.5 V	

ELECTRICAL CONNECTION N-Channel



- 1 : Source1
- 2 : Gate1 3 : Source2
- 4 : Gate2
- 5 : Drain
- 6 : Drain
- 7 : Drain 8 : Drain

MARKING







ORDERING INFORMATION

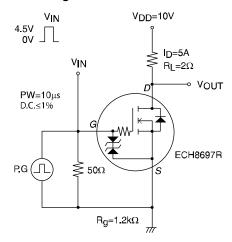
See detailed ordering and shipping information on page 5 of this data sheet.

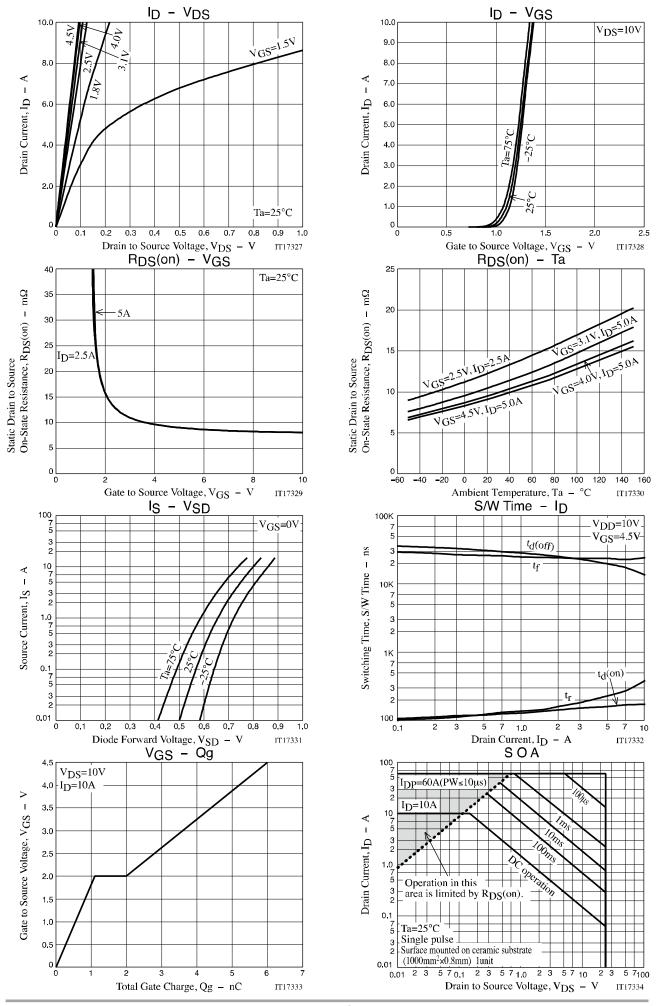
ELECTRICAL CHARACTERISTICS at $Ta = 25^{\circ}C$ (Note 2)

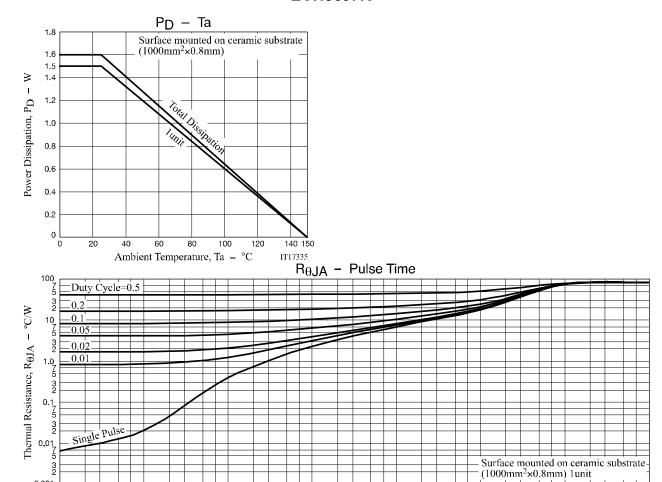
Parameter	Symbol	Symbol Conditions	Value			Unit
Parameter	Symbol	Conditions	min	typ	max	Offic
Drain to Source Breakdown Voltage	V(BR)DSS	I _D = 1 mA, V _{GS} = 0 V	24			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} = 20 V, V _{GS} = 0 V			1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} = ±8 V, V _{DS} = 0 V			±1	μΑ
Gate Threshold Voltage	VGS(th)	V _{DS} = 10 V, I _D = 1 mA	0.5		1.3	٧
Forward Transconductance	gFS	V _{DS} = 10 V, I _D = 5 A		5.0		S
Static Drain to Source On-State Resistance	R _{DS} (on)	I _D = 5 A, V _{GS} = 4.5 V	7.4	9.3	11.6	mΩ
		I _D = 5 A, V _{GS} = 4.0 V	7.7	9.7	12.6	mΩ
		I _D = 5 A, V _{GS} = 3.1 V	8.5	10.7	15	mΩ
		I _D = 2.5 A, V _{GS} = 2.5 V	10	12.5	17.5	mΩ
Turn-ON Delay Time	t _d (on)			160		ns
Rise Time	t _r	Con amonified Took Circuit		230		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		19.7		μS
Fall Time	tf			23.6		μS
Total Gate Charge	Qg			6		nC
Gate to Source Charge	Qgs	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 10 A		1.1		nC
Gate to Drain "Miller" Charge	Qgd			0.9		nC
Forward Diode Voltage	V _{SD}	I _S = 10 A, V _{GS} = 0 V		0.8	1.2	V

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit







5 7_{0.0001} 2 3 5 7_{0.001} 2 3 5 7_{0.001} Pulse Time, PT – s

2 3 5 7 0.1

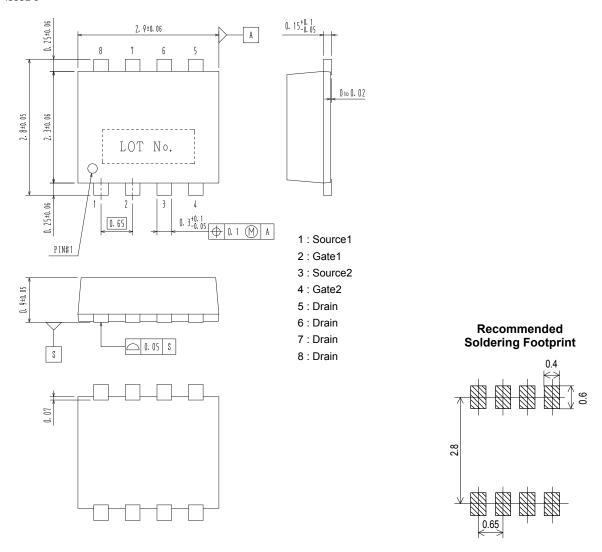
5 70.00001 2 3

5 7 10

IT17937

PACKAGE DIMENSIONS

unit: mm SOT-28FL/ECH8 CASE 318BF ISSUE O



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
ECH8697R-TL-W	UU	SOT-28FL / ECH8 (Pb-Free / Halogen Free)	3,000 / Tape & Reel

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage: Since the ECH8697R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects. Please contact sales for use except the designated application.

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