

### Continental Device India Limited

An IS/ISO 9002 and IECQ Certified Manufacturer



## NPN SILICON PLANAR EPITAXIAL AMPLIFIER TRANSISTORS



BC182L, BC182LA, BC182LB

TO-92 Plastic Package

# **General Purpose Amplifier Transistors**

### ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C unless specified otherwise)

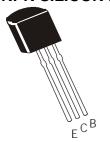
DESCRIPTION	SYMBOL	VALUE	UNITS
Collector-Emitter Voltage	$V_{CEO}$	50	V
Collector-Base Voltage	$V_{CBO}$	60	V
Emitter-Base Voltage	$V_{EBO}$	6.0	V
Collector Current Continuous	I <sub>C</sub>	100	mA
Power Dissipation @ T <sub>a</sub> =25°C	$P_{D}$	350	mW
Derate Above 25°C		2.8	mW/ºC
Total Device Dissipation @ T <sub>c</sub> =25°C	$P_{D}$	1.0	W
Derate Above 25°C		8	mW/ºC
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C
THERMAL RESISTANCE			
Junction to ambient	$R_{th(j-a)}$	357	°C/W
Junction to case	R <sub>th(j-c)</sub>	125	°C/W

### ELECTRICAL CHARACTERISTICS (T<sub>3</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	VALUE			UNITS
			MIN	TYP	MAX	
Collector-Emitter Voltage	$V_{CEO}$	$I_C=2mA$ , $I_B=0$	50			V
Collector-Base Voltage	$V_{CBO}$	$I_{C}=10\mu A, I_{E}=0$	60			V
Emitter-Base Voltage	$V_{EBO}$	I <sub>E</sub> =100μA, I <sub>C</sub> =0	6			V
Collector Cut-off Current	I <sub>CBO</sub>	$V_{CB} = 50V, I_{E} = 0$			15	nA
Emitter-Base Leakage Current	I <sub>EBO</sub>	$V_{EB}=4.0V, I_{C}=0$			15	nA
DC Current Gain	h <sub>FE</sub>	$V_{CE}$ =5 $V$ , $I_{C}$ =10 $\mu$ A ALL	40			
		$V_{CE}$ =5V, $I_{C}$ =2mA <b>BC182Lonly</b>	120		500	
		$V_{CE}$ =5V, $I_{C}$ =100mA <b>ALL</b>	80			

BC182L\_LA\_BRev240203D

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# ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION		VALUE		
			MIN	TYP	MAX	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA			0.25	V
	, ,	*I <sub>C</sub> =100mA, I <sub>B</sub> =5mA			0.6	V
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	*I <sub>C</sub> =100mA, I <sub>B</sub> =5mA			1.2	V
Base-Emitter On Voltage	$V_{BE(on)}$	I <sub>C</sub> =100μA, V <sub>CE</sub> =5V		0.5		V
	, ,	$I_C=2mA, V_{CE}=5V$	0.55		0.7	V
		$^*I_C=100$ mA, $V_{CE}=5$ V		0.83		V

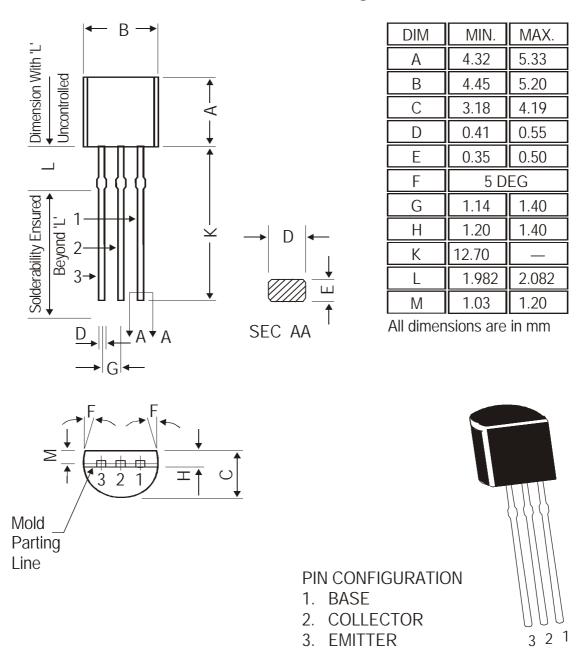
## **DYNAMIC CHARACTERISTICS**

Current-Gain Bandwidth Product	f <sub>T</sub>	I <sub>C</sub> =0.5mA, V <sub>CE</sub> =3V, f=100MHZ		100		MHz
		I <sub>C</sub> =10mA, V <sub>CE</sub> =5V, f=100MHZ	150			MHz
Common Base Output Capacitance	$C_ob$	$V_{CB}$ =10V, $I_{C}$ =0, f=1MHz			5.0	pF
Common Base Input Capacitance	$C_{ib}$	$V_{BE}$ =0.5V, Ic=0, f=1MHz		8.0		pF
Small-Signal Current Gain	h <sub>fe</sub>	$I_C=2mA$ , $V_{CE}=5V$ , $f=1KHz$				
		BC182L	125		500	
		BC182LA	125		260	
		BC182LB	240		500	
Noise Figure	NF	$V_{CE}$ =5.0V, $I_{C}$ =0.2mA, $R_{s}$ =2.0K $\Omega$ ,			10	dB
		f=1KHz, F=200Hz				

<sup>\*</sup>Pulse Condition: Width  $\leq$  300ms, Duty Cycle  $\leq$  2%.

TO-92 Plastic Package

## **TO-92 Plastic Package**



The TO-92 Package, Tape and Ammo Pack Drawings are correct as on the date of issue/revision of this Data Sheet.

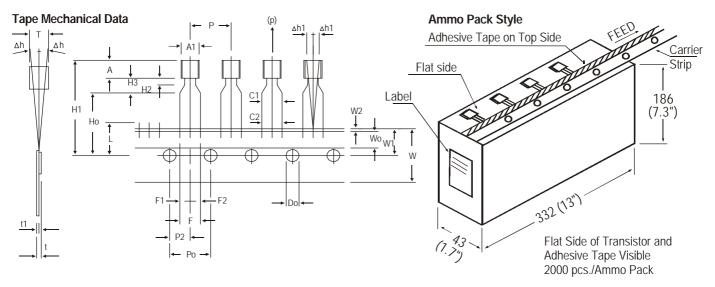
The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

## **Packing Details**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Oty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

# TO-92 Plastic Package

## **TO-92 Tape and Ammo Pack**



#### All dimensions are in mm

		SPECIFICATION				
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	А	4.8		5.2		
BODY THICKNESS	Т	3.9		4.2		
PITCH OF COMPONENT	Р		12.7		± 1.0	
*1FEED HOLE PITCH	Po		12.7		± 0.3	
*2 FEED HOLE CENTRE TO						
COMPONENT CENTRE	P2		6.35		± 0.4	
DISTANCE BETWEEN OUTER LEADS	F		5.08		+ 0.6 - 0.2	
*3 COMPONENT ALIGNMENT SIDE VIEW	△h		0	1.0		
*4 COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		
TAPE WIDTH	W		18		± 0.5	
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	
HOLE POSITION	W1		9		+ 0.7	
					- 0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	
LEAD WIRE CLINCH HEIGHT	Но		16		± 0.5	
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	
*5 TOTAL TAPE THICKNESS	t			1.2		
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4	
CTAND OFF	110	0.45		1 45	- 0.1	
STAND OFF CLINCH HEIGHT	H2	0.45		1.45		
LEAD PARALLELISM	H3			3.0		
PULL - OUT FORCE	C1 - C2   (p)	6N		0.22		
I OLL - OUT FONCE	ı (P)	UIN	I	1		

### **NOTES**

- 1. Maximum alignment deviation between leads will not to be greater than 0.2mm.
- 2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.
- 3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.
- 4. There will be no more than three (3) consecutive missing components in a tape.
- 5. A tape trailer, having at least three feed holes are provided after the last component in a tape.
- 6. Splices should not interfere with the sprocket feed holes.

### **REMARKS**

- \*1 Cumulative pitch error 1.0 mm/20 pitch
- \*2 To be measured at bottom of clinch
- \*3 At top of body
- \*4 At top of body
- \*5 t1 0.3 0.6 mm

**Notes** 

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### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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