1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 2 A
- Reverse voltage: V_R ≤ 60 V
- · Extremely low leakage current
- Low forward voltage
- · High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- High temperature T_i ≤ 175 °C

3. Applications

- · Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- · Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|-------------------------|--|-----|-----|-----|------|
| I _{F(AV)} | average forward current | square wave; δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 160 °C | - | - | 2 | Α |
| V_R | reverse voltage | T _j = 25 °C | - | - | 60 | ٧ |
| V _F | forward voltage | I _F = 2 A; T _j = 25 °C | - | 690 | 760 | mV |
| I _R | reverse current | $V_R = 60 \text{ V; } t_p \le 300 \mu\text{s; } T_j = 25 ^\circ\text{C; } \delta \le 0.02 \text{ ; pulsed}$ | - | 90 | 300 | nA |



5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|----------------|
| 1 | K | cathode[1] | 1 2 | 1 1 2 |
| 2 | Α | anode | SOD123W | sym001 |

^[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | | |
|-------------|---------|--|---------|--|--|--|
| | Name | Description | Version | | | |
| PMEG6020ELR | SOD123W | plastic surface mounted package; 2 leads | SOD123W | | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PMEG6020ELR | K2 |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|--------------------|-------------------------------------|--|-----|-----|------|------|
| V_R | reverse voltage | T _j = 25 °C | | - | 60 | V |
| l _F | forward current | T _{sp} = 155 °C; δ = 1 | | - | 2.83 | Α |
| I _{F(AV)} | average forward current | square wave; δ = 0.5 ; f = 20 kHz; $T_{amb} \le$ 90 °C | [1] | - | 2 | А |
| | | square wave; δ = 0.5 ; f = 20 kHz; $T_{sp} \le$ 160 °C | | - | 2 | Α |
| I _{FSM} | non-repetitive peak forward current | square wave; t _p = 8 ms; T _{j(init)} = 25 °C | | - | 50 | А |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2] | - | 680 | mW |
| | | | [3] | - | 1150 | mW |
| | | | [1] | - | 2140 | mW |
| Tj | junction temperature | | | - | 175 | °C |
| T _{amb} | ambient temperature | | | -55 | 175 | °C |
| T _{stg} | storage temperature | | | -65 | 175 | °C |

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|-----------------------|--|-------------|--------|-----|-----|-----|------|
| ui(j-a) | thermal resistance from junction to | in free air | [1][2] | - | - | 220 | K/W |
| | | | [1][3] | - | - | 130 | K/W |
| | | | [1][4] | - | - | 70 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | [5] | - | - | 18 | K/W |

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

^[5] Soldering point of cathode tab.

60 V, 2 A low leakage current Schottky barrier rectifier

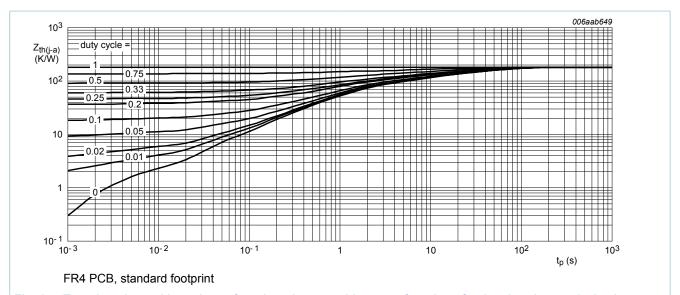


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

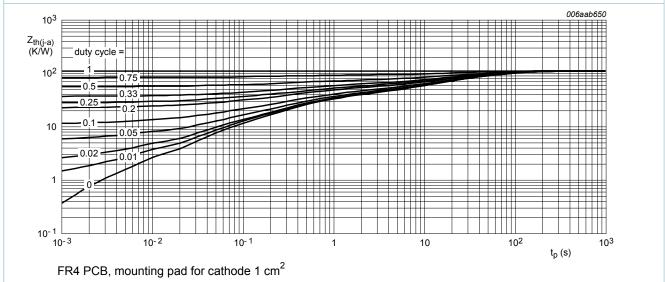
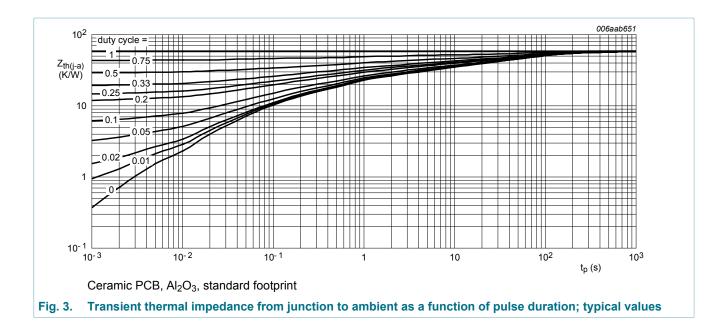


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

60 V, 2 A low leakage current Schottky barrier rectifier



10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|-------------------------------|---|-----|-----|-----|------|
| $V_{(BR)R}$ | reverse breakdown voltage | $I_R = 1 \text{ mA}; T_j = 25 \text{ °C}$ | 60 | - | - | V |
| V _F | forward voltage | I _F = 0.1 A; T _j = 25 °C | - | 475 | 540 | mV |
| | | I _F = 0.5 A; T _j = 25 °C | - | 550 | 605 | mV |
| | | $I_F = 0.7 \text{ A}; T_j = 25 ^{\circ}\text{C}$ | - | 575 | 625 | mV |
| | | I _F = 1 A; T _j = 25 °C | - | 605 | 660 | mV |
| | | I _F = 1.6 A; T _j = 25 °C | - | 660 | 720 | mV |
| | | I _F = 2 A; T _j = 25 °C | - | 690 | 760 | mV |
| I _R | reverse current | V_R = 5 V; $t_p \le 300 \ \mu s$; T_j = 25 °C; $\delta \le 0.02$; pulsed | - | 5 | - | nA |
| | | V_R = 10 V; $t_p \le 300 \ \mu s$; T_j = 25 °C; $\delta \le 0.02$; pulsed | - | 6 | - | nA |
| | | V_R = 40 V; $t_p \le 300 \mu s$; T_j = 25 °C; δ ≤ 0.02 ; pulsed | - | 25 | 50 | nA |
| | | V_R = 60 V; $t_p \le 300 \mu s$; T_j = 25 °C; δ ≤ 0.02 ; pulsed | - | 90 | 300 | nA |
| | | V_R = 10 V; $t_p \le 300 \ \mu s; T_j$ = 125 °C; δ ≤ 0.02 ; pulsed | - | 25 | - | μΑ |
| | | V_R = 60 V; $t_p \le 300 \ \mu s; T_j$ = 125 °C; δ ≤ 0.02 ; pulsed | - | 120 | - | μΑ |
| C_d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | - | 110 | - | pF |
| | | V _R = 4 V; f = 1 MHz; T _j = 25 °C | - | 65 | - | pF |
| | | V _R = 10 V; f = 1 MHz; T _j = 25 °C | - | 45 | - | pF |
| t _{rr} | reverse recovery time | $I_F = 0.5 \text{ A}$; $I_R = 0.5 \text{ A}$; $I_{R(meas)} = 0.1 \text{ A}$; $T_j = 25 \text{ °C}$ | - | 4.5 | - | ns |
| V_{FRM} | peak forward recovery voltage | $I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$ | - | 580 | - | mV |

60 V, 2 A low leakage current Schottky barrier rectifier

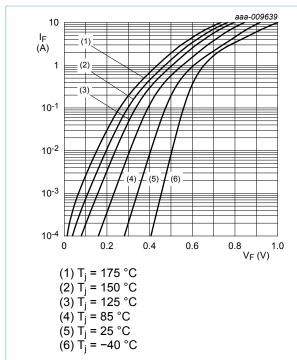


Fig. 4. Forward current as a function of forward voltage; typical values

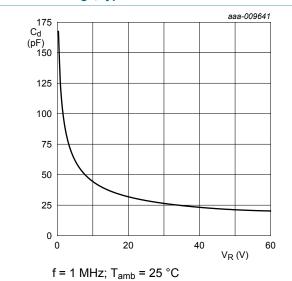


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

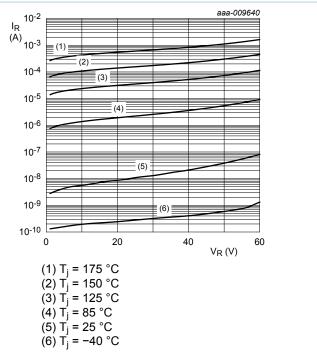


Fig. 5. Reverse current as a function of reverse voltage; typical values

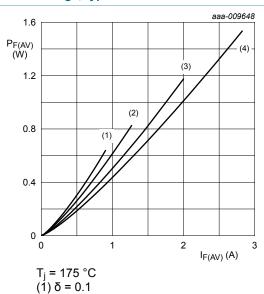


Fig. 7. Average forward power dissipation as a function of average forward current; typical values

 $(2) \delta = 0.2$

 $(3) \delta = 0.5$ $(4) \delta = 1$

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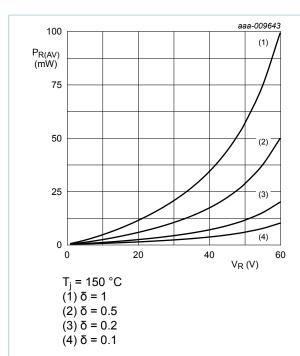


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values

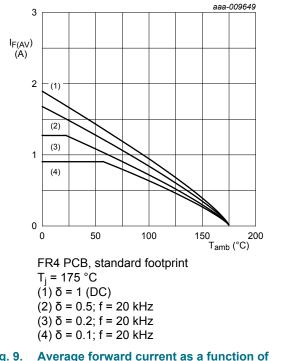
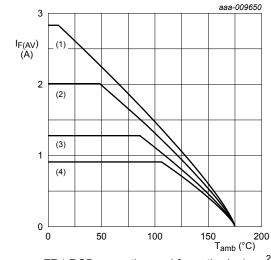


Fig. 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

 $T_j = 175 \,{}^{\circ}\text{C}$

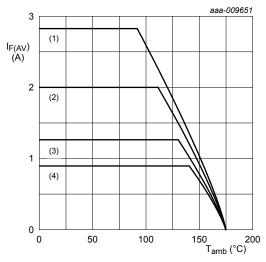
 $(1) \delta = 1 (DC)$

(2) δ = 0.5; f = 20 kHz

(3) δ = 0.2; f = 20 kHz

(4) δ = 0.1; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 175 °C

 $(1) \delta = 1 (DC)$

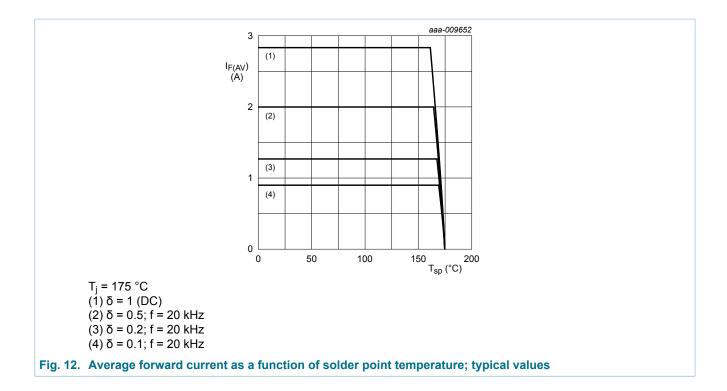
(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

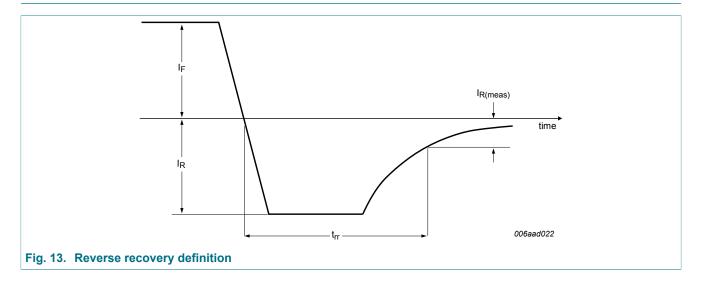
(4) δ = 0.1; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values

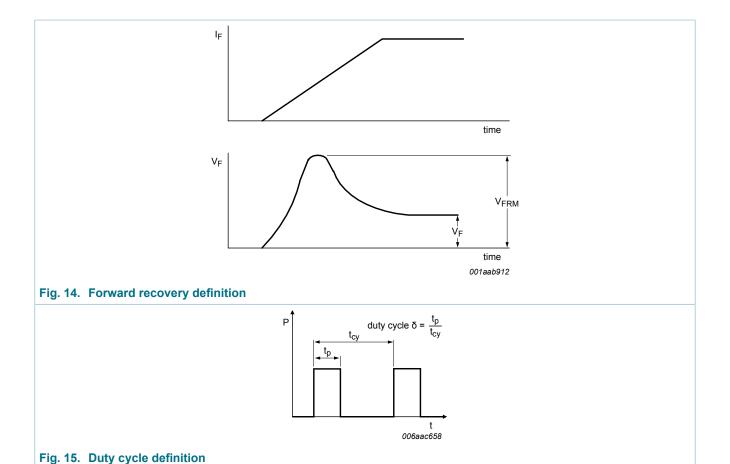
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11. Test information



60 V, 2 A low leakage current Schottky barrier rectifier



The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_{M} \times \delta$ with I_{M} defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_{M} \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

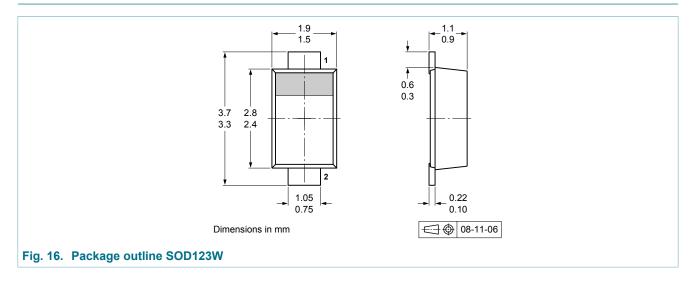
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

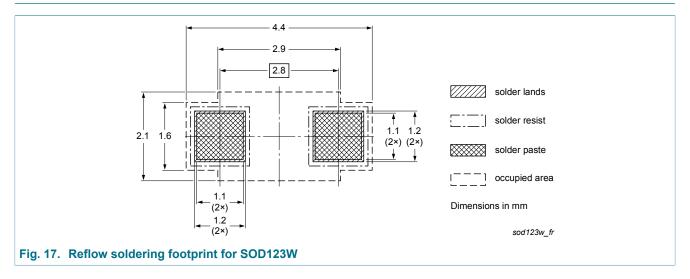
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12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

| Table of Iteriological India | . , | | | | | | |
|------------------------------|-----------------------------|------------------------|---------------|-----------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| PMEG6020ELR v.3 | 20160908 | Product data sheet | - | PMEG6020ELR v.2 | | | |
| Modifications: | Figure 12: editorial change | | | | | | |
| PMEG6020ELR v.2 | 20140603 | Product data sheet | - | PMEG6020ELR v.1 | | | |
| PMEG6020ELR v.1 | 20131108 | Preliminary data sheet | - | - | | | |

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Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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