Nch 40V 39A Middle Power MOSFET

V_{DSS}	40V
R _{DS(on)} (Max.)	7.2mΩ
I _D	±39A
P _D	20W

Features

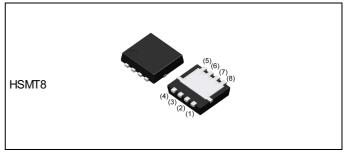
- 1) Low on resistance.
- 2) High Power Package (HSMT8).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

Application

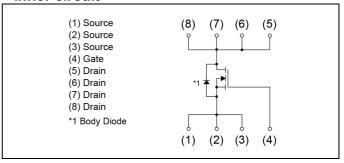
Switching

DC/DC converter

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	3000
	Taping code	ТВ
	Marking	G150GN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	40	V	
Continuous dusis suurent	T _c = 25°C	I _D *1	±39	Α
Continuous drain current	T _a = 25°C	I _D	±15	Α
Pulsed drain current	I _{DP} *2	±60	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche current, single pulse	I _{AS} *3	15	Α	
Avalanche energy, single pulse		E _{AS} *3	17	mJ
Dower dissinction		P _D *1	20	W
Power dissipation		P _D *4	2	W
Junction temperature	T _j	150	°C	
Operating junction and storage to	T _{stg}	-55 to +150	°C	

●Thermal resistance

Doromotor	Symbol	Values			l limit
Parameter		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	6.2	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C)

Daramatar	Cymah ol	Conditions	Values			l lmi4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	26.2	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V	1	1	1	μΑ	
Gate - Source leakage current	I _{GSS}	V_{GS} = ±20V, V_{DS} = 0V	ı	1	±100	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 1mA$	1.2	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-4.9	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 15A	-	5.1	7.2	O	
on - state resistance	R _{DS(on)} *5	V _{GS} = 4.5V, I _D = 15A	-	6.4	8.9	mΩ	
Gate resistance	R _G	f=1MHz, open drain	-	1.4	-	Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 15A	16	-	-	S	

^{*1}Tc=25°C, Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*} Limited only by maximum chamel temperaturer allowed.

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Davanastan	Cy reads a l	Conditions	Values			Lloit
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	1450	-	
Output capacitance	C _{oss}	V _{DS} = 20V	-	260	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	80	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 20V, V_{GS} = 10V$	-	16.8	-	
Rise time	t _r *5	I _D = 7.5A	-	6.4	-	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 2.7\Omega$	-	62.1	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	11.0	-	

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymah al	Conditions		Values			l limit
Parameter	Symbol	Conditi	Conditions		Тур.	Max.	Unit
Total gate charge Q_g^{*5}	O *5	$V_{CS} = 10$ $V_{DD} \simeq 20V$	V _{GS} = 10V	-	24.1	-	
	Q_g			-	11.6	-	»C
Gate - Source charge	Q _{gs} *5	I _D = 15A	V _{GS} = 4.5V	-	4.7	-	nC
Gate - Drain charge	Q _{gd} *5			-	3.0	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Daramatar	Cumbal	Conditions	Values		Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I _S	T = 25°C	-	-	1.67	Α
Pulse forward current	I _{SP} *2	⁻ T _a = 25°C	-	-	60	Α
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 15A, V _{GS} =0V	-	27	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	23	-	nC

Fig.1 Power Dissipation Derating Curve

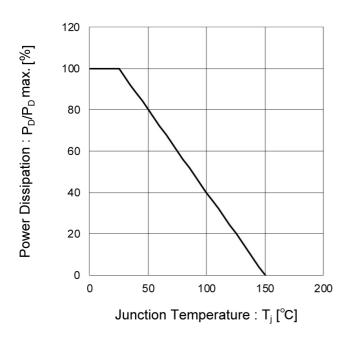
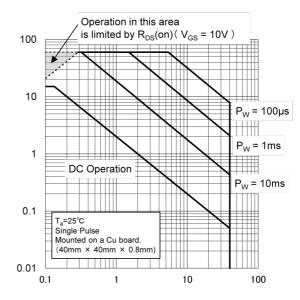


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

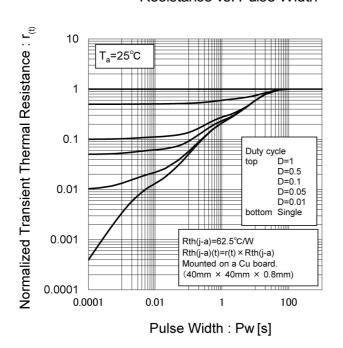
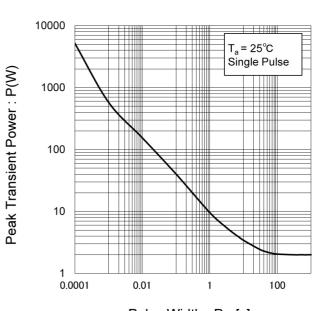


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Drain Current : I_D [A]

• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

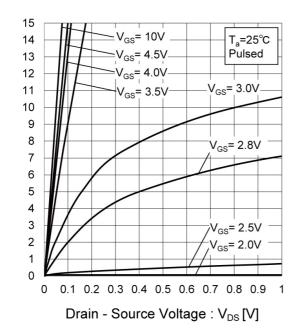
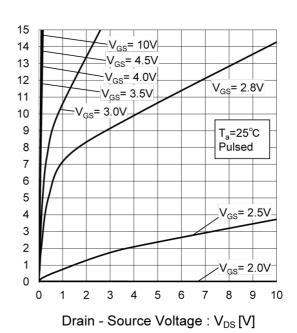


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs. Junction Temperature

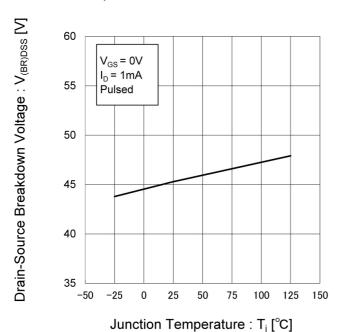


Fig.8 Typical Transfer Characteristics

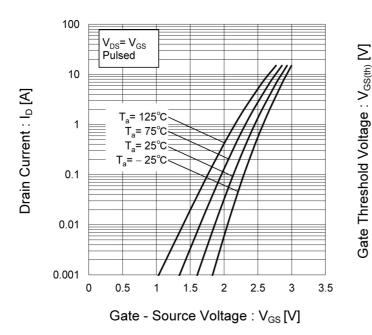


Fig.9 Gate Threshold Voltage vs. Junction Temperature

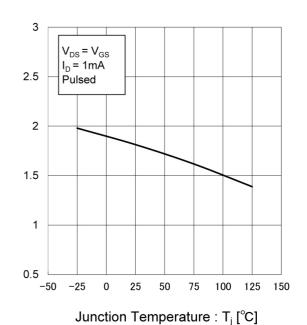


Fig.10 Forward Transfer Admittance vs. Drain Current

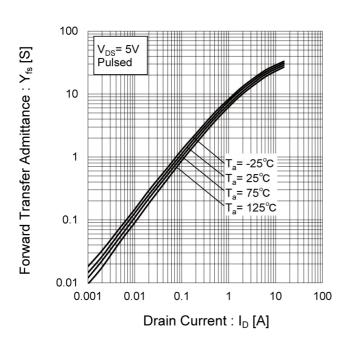


Fig.11 Drain Current Derating Curve

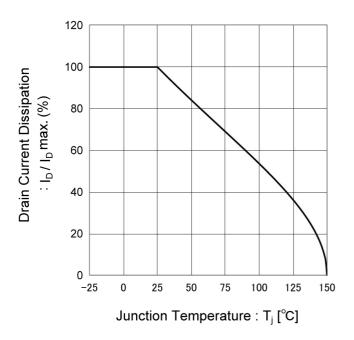


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

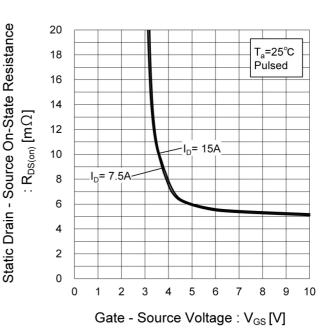


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

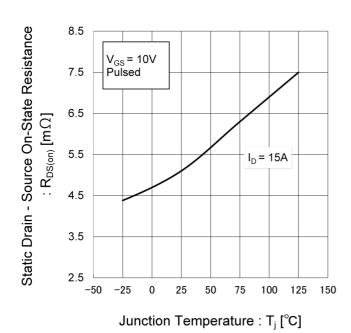


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

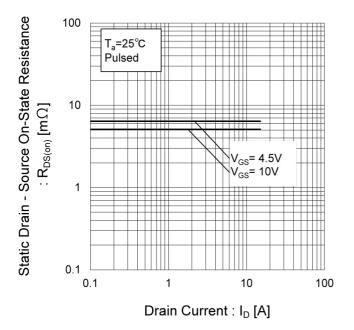


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

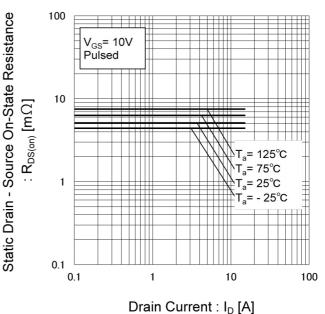


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

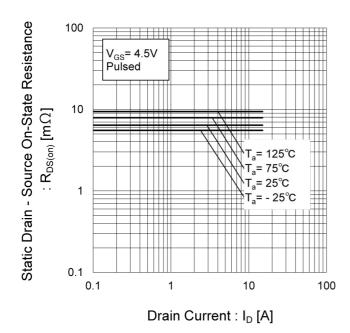


Fig.17 Typical Capacitance vs. Drain - Source Voltage

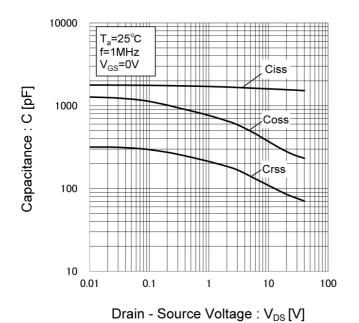


Fig.18 Switching Characteristics

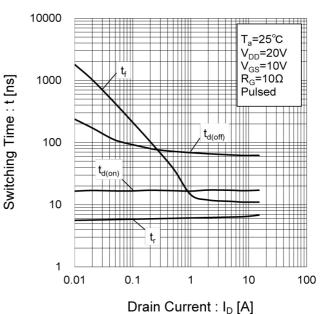


Fig.19 Dynamic Input Characteristics

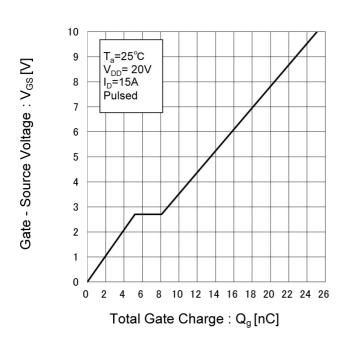
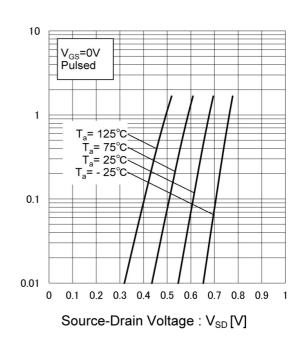


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

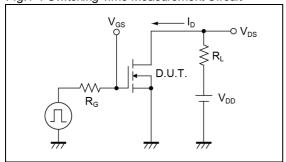


Fig.2-1 Gate Charge Measurement Circuit

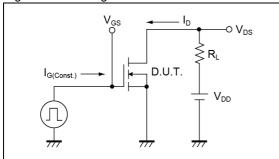


Fig.3-1 Avalanche Measurement Circuit

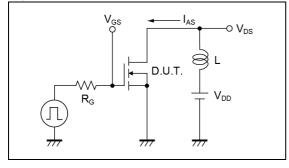


Fig.1-2 Switching Waveforms

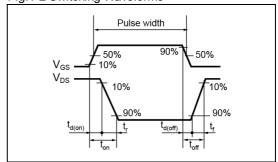


Fig.2-2 Gate Charge Waveform

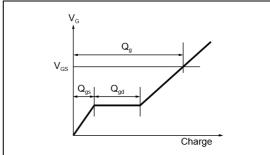
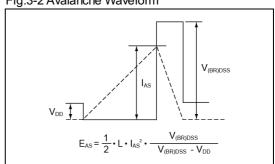


Fig.3-2 Avalanche Waveform



Notice

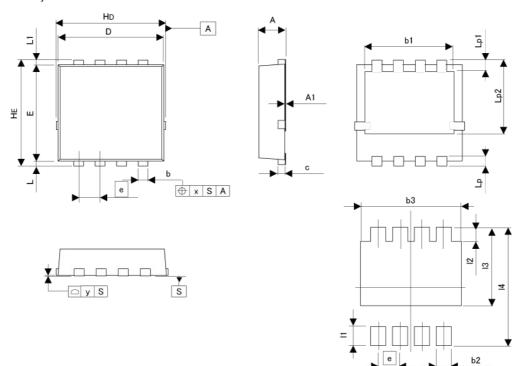
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

HSMT8

(3.3x3.3)



DIM	MILIME	TERS	INCI	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	0.70	0.90	0.028	0.035	
A1	0.00	0.05	0.000	0.002	
b	0.27	0.37	0.011	0.015	
b1	2.50	2.70	0.098	0.106	
С	0.10	0.30	0.004	0.012	
D	3.10	3.30	0.122	0.130	
E	2.90	3.10	0.114	0.122	
е	0.	65	0.026		
HD	3.20	3.40	0.126	0.134	
HE	3.20	3.40	0.126	0.134	
L	0.07	0.25	0.003	0.010	
L1	0.07	0.25	0.003	0.010	
Lp	0.20	0.40	0.008	0.016	
Lp1	0.25	0.45	0.010	0.018	
Lp2	2.20	2.40	0.087	0.094	
Х	-	0.10	-	0.004	
у	-	0.10		0.004	

DIM	MILIME	TERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
b2		0.47		0.019
b3	1.5	2.70	-	0.106
l1	-	0.50	-	0.020
12	-	0.55	-	0.022
13	(1.5.)	2.40	-	0.094
14	121	3.40	-	0.134

Dimension in mm/inches



Pattern of terminal position areas [Not a pattern of soldering pads]

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CI ACCIII	CLASS II b	CI VCCIII
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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Notice-PGA-E Rev.003



RQ3G150GN - Web Page

Part Number	RQ3G150GN
Package	HSMT8
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes