AEC-Q101 Qualified

2.5V Drive Pch MOS FET

RTR030P02FHA

Structure

Silicon P-channel MOS FET

Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

Application

Power switching, DC / DC converter.

TSMT3 2 9 0 4 0 85 0 85 0 95 0 95 0 95 0 19 0 16 (1) Gate Each lead has same dimensions

(2) Source

(3) Drain

Packaging specifications

	Package	Taping
Туре	Code	TL
	Basic ordering unit (pieces)	3000
RTR030P02	0	

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V _{DSS}	-20	V
Gate-source voltage		V _{GSS}	±12	V
Drain current	Continuous	I _D	±3.0	Α
Diam current	Pulsed	I _{DP} *1	±12	Α
Source current	Continuous	Is	-0.8	Α
(Body diode)	Pulsed	I _{SP} *1	-3.2	Α
Total power dissipation		P _D *2	1.0	W
Channel temperature		Tch	150	°C
Range of Storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

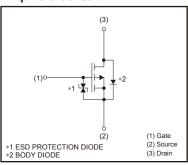
Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth (ch-a)*	125	°C / W	

^{*} Mounted on a ceramic board.

●Equivalent circuit

Abbreviated symbol : TV



ROHM

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	_	_	±10	μΑ	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V(BR) DSS	-20	_	_	V	I _D = -1mA, V _G s=0V
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V _{DS} = -20V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	-0.7	_	-2.0	V	V _{DS} = -10V, I _D = -1mA
01-11-1-1-1-1		_	55	75	mΩ	I _D = -3.0A, V _{GS} = -4.5V
Static drain-source on-state resistance	R _{DS (on)} *	_	60	85	mΩ	I _D = -3.0A, V _{GS} = -4.0V
resistance		-	90	125	mΩ	I _D = -1.5A, V _{GS} = -2.5V
Forward transfer admittance	Y _{fs} *	2.5	-	_	S	V _{DS} = -10V, I _D = -1.5A
Input capacitance	Ciss	_	840	_	pF	V _{DS} = -10V
Output capacitance	Coss	_	140	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	100	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	12	_	ns	I _D = -1.5A
Rise time	tr *	_	20	_	ns	VDD≒ -15V
Turn-off delay time	td (off) *	_	50	_	ns	V _{GS} = -4.5V R _L =10Ω
Fall time	t _f *	_	20	_	ns	R _G =10Ω
Total gate charge	Qg	_	9.3	_	nC	V _{DD} ≒−15V
Gate-source charge	Qgs	_	1.6	_	nC	V _{GS} = -4.5V
Gate-drain charge	Q _{gd}	_	2.6	_	nC	I _D = -3.0A

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V _{SD}	_	_	-1.2	V	I _S = -0.8A, V _{GS} =0V

Electrical characteristic curves

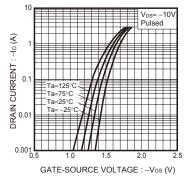


Fig. 1 Typical Transfer Characteristics

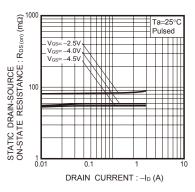


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

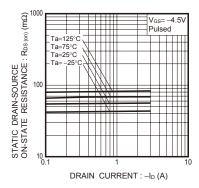


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

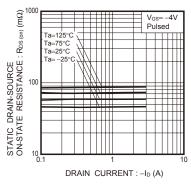


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

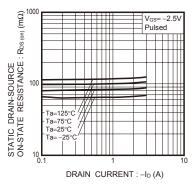


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

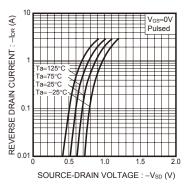


Fig.6 Reverse Drain Current vs.Source-Drain Voltage

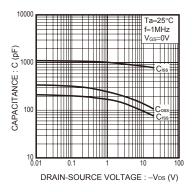


Fig.7 Typical Capacitance vs. Drain-Source Voltage

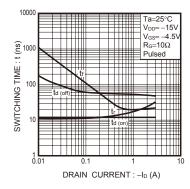


Fig.8 Switching Characteristics

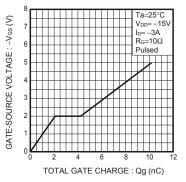


Fig.9 Dynamic Input Characteristics

Measurement circuits

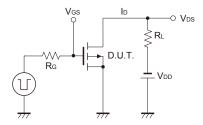


Fig.10 Switching Time Test Circuit

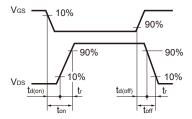


Fig.11 Switching Time Waveforms

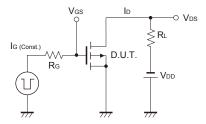


Fig.12 Gate Charge Test Circuit

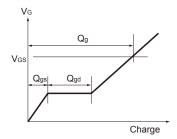


Fig.13 Gate Charge Waveform

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JAPAN	AN USA EU		CHINA
CLASSⅢ	CLACCIII	CLASS II b	СГУССШ
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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RTR030P02FHA - Web Page

Distribution Inventory

Part Number	RTR030P02FHA
Package	TSMT3
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes