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September 2016

FCPF150N65F — N-Channel SuperFET[®] II FRFET[®] MOSFET

FCPF150N65F N-Channel SuperFET[®] II FRFET[®] MOSFET

650 V, 24 A, 150 mΩ

Features

- 700 V @ T_J = 150°C
- Typ. R_{DS(on)} = 133 mΩ
- Ultra Low Gate Charge (Typ. Q_q = 72 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 361 pF)
- 100% Avalanche Tested
- · RoHS Compliant

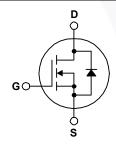
Applications

- Telecom/Server Power Supplies
 Solar Inverters
- · Computing Power Supplies FPD TV Power/Lighting

Description

SuperFET[®] II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. SuperFET II FRFET® MOSFET combines a faster and more rugged intrinsic body diode performance with fast switching, aimed at achieving better reliability and efficiency especially in resonant switching applications. SuperFET II FRFET is very suitable for the switching power applications such as server/telecom power, Solar inverter, FPD TV power, computing power, lighting and industrial power applications.





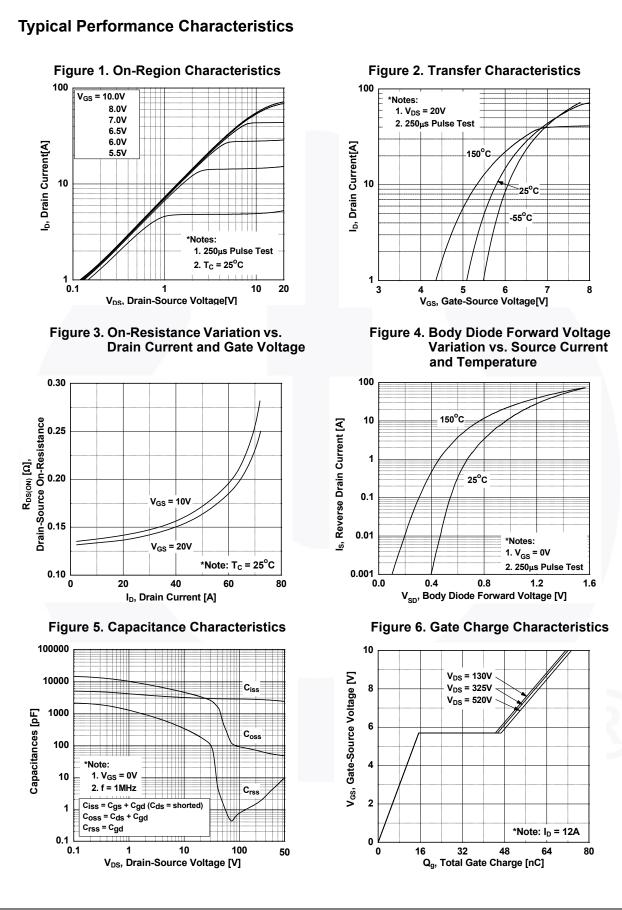
Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter			FCPF150N65F	Unit
V _{DSS}	Drain to Source Voltage			650	V
V _{GSS}		- DC	- DC		- V
	Gate to Source Voltage	- AC	- AC (f > 1 Hz)		
	Desia Ourrent	- Continuous (T _C = 25 ^o C)	- Continuous ($T_C = 25^{\circ}C$)		^
I _D	Drain Current	- Continuous (T _C = 100 ^o C)		14.9*	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	72*	А
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		663	mJ	
I _{AR}	Avalanche Current (Note 1)		4.7	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.98	mJ	
du/dt	MOSFET dv/dt			100	V/ns
dv/dt F	Peak Diode Recovery dv/dt (Note 3)			50	V/IIS
P _D	Power Dissinction	(T _C = 25 ^o C)		39	W
	Power Dissipation	- Derate Above 25°C		0.31	W/ºC
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C	
Drain current lim	ited by maximum junction temperature).	1		

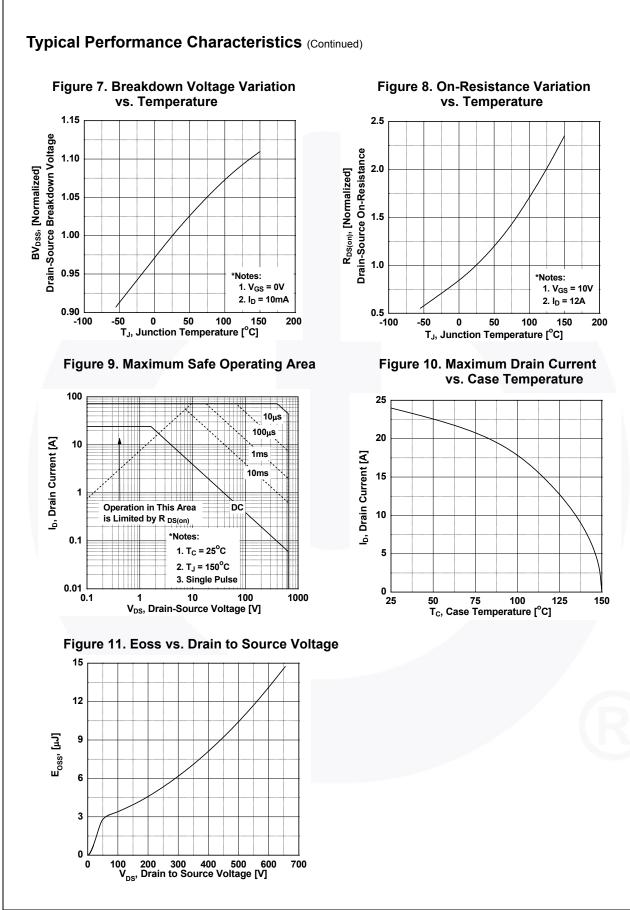
Thermal Characteristics

Symbol	Parameter	FCPF150N65F	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	3.2	°C/W	
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.			

Part Nu	Part Number Top Mark Packa		Package	Packing Method	Reel Size	Тар	e Width	Qua	ntity
FCPF150N65F FCPF150N65F TO-220F		TO-220F	Tube	N/A		N/A	50 units		
Electrica	l Char	racteristics T _c = 2	25°C unless o	otherwise noted.					
Symbol		Parameter		Test Conditions		Min.	Тур.	Max.	Uni
Off Charac	teristic	·e							
		.5		(1 - 0)(1 - 10 - 0)	T - 0500	050			
BV _{DSS} Drain to Source Breakdown Volt		Itage	$V_{GS} = 0 V, I_D = 10 mA, T_J = 25^{\circ}C$		650 700	-	-	V	
ABV _{DSS}	Breakd	own Voltage Temperatu	re	V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C		700	-	-	+
ΔTJ	Coeffic			I_D = 10 mA, Referenced to 25 ^o C		-	0.72	-	- V/º0
J				V _{DS} = 650 V, V _{GS} = 0 V		-	-	10	
DSS	Zero Gate Voltage Drain Current		$V_{\rm DS} = 520 \text{ V}, V_{\rm GS} = 0 \text{ V}, T_{\rm C} = 125^{\circ}\text{C}$		-	86	-	μA	
GSS	Gate to	Body Leakage Current		$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$		-	-	±100	nA
							u	1	1
On Charac	teristic	S							
V _{GS(th)}	Gate Threshold Voltage		V_{GS} = V_{DS} , I_D = 2.4 m	A	3	-	5	V	
R _{DS(on)}	Static E	Static Drain to Source On Resistance		V _{GS} = 10 V, I _D = 12 A		-	133	150	mΩ
9 _{FS}	Forward Transconductance			V _{DS} = 20 V, I _D = 12 A		-	22	-	S
Dynamic C	haract	eristics							
C _{iss}	Input C	out Capacitance Itput Capacitance everse Transfer Capacitance			-	2810	3737	pF	
C _{oss}	Output			─ V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz		-	91	121	pF
C _{rss}	Revers					-	0.77	-	pF
C _{oss}	Output	Output Capacitance		V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz		-	54	-	pF
C _{oss(eff.)}	Effectiv	Effective Output Capacitance		$V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$		-	361	-	pF
Q _{g(tot)}	Total G	ate Charge at 10V		V _{DS} = 380 V, I _D = 12 A,		-	72	94	nC
Q _{gs}	Gate to	Gate to Source Gate Charge		V _{GS} = 10 V		-	15	-	nC
Q _{gd}	Gate to	Gate to Drain "Miller" Charge		(Note 4)		-	31	-	nC
ESR	Equivalent Series Resistance			f = 1 MHz		-	0.69	-	Ω
Switching	Charac	teristics					1		
d(on)	-					-	28	66	ns
r	Turn-On Delay Time Turn-On Rise Time			$V_{DD} = 380 \text{ V}, \text{ I}_{D} = 12 \text{ A},$		-	15	40	ns
		ff Delay Time		$V_{\rm GS}$ = 10 V, R _g = 4.7 Ω		-	73	156	ns
d(off)		Turn-Off Fall Time		(Note 4)		-	6	22	ns
							Ū		110
	i	de Characteristics				-	-	24	A
S						-	-	72	A
SM	Maximum Pulsed Drain to Source Diode Fo					-	-	1.4	V
V _{SD}		Drain to Source Diode Forward Voltage Reverse Recovery Time		$V_{GS} = 0 \text{ V, } I_{SD} = 12 \text{ A}$ $V_{GS} = 0 \text{ V, } I_{SD} = 12 \text{ A,}$ $dI_{F}/dt = 100 \text{ A}/\mu\text{s}$		-	- 123	-	ns
	Reverse Recovery Time Reverse Recovery Charge					-	597	-	nC
Q _{rr} lotes:	1/2/2120	Charge Charge				-	531	-	
	: pulse width	limited by maximum junction te	mperature.						

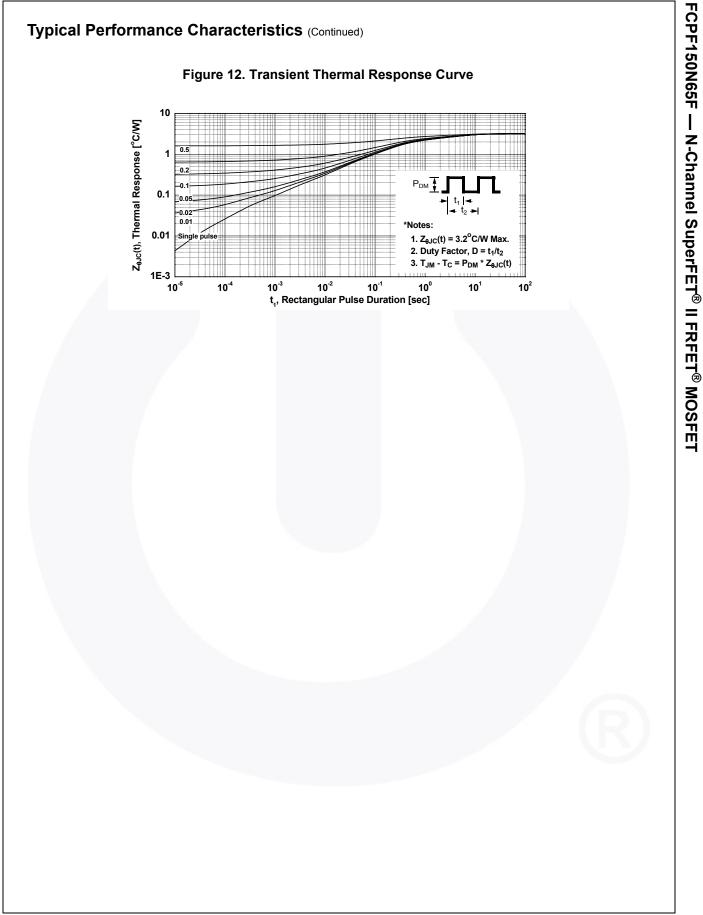


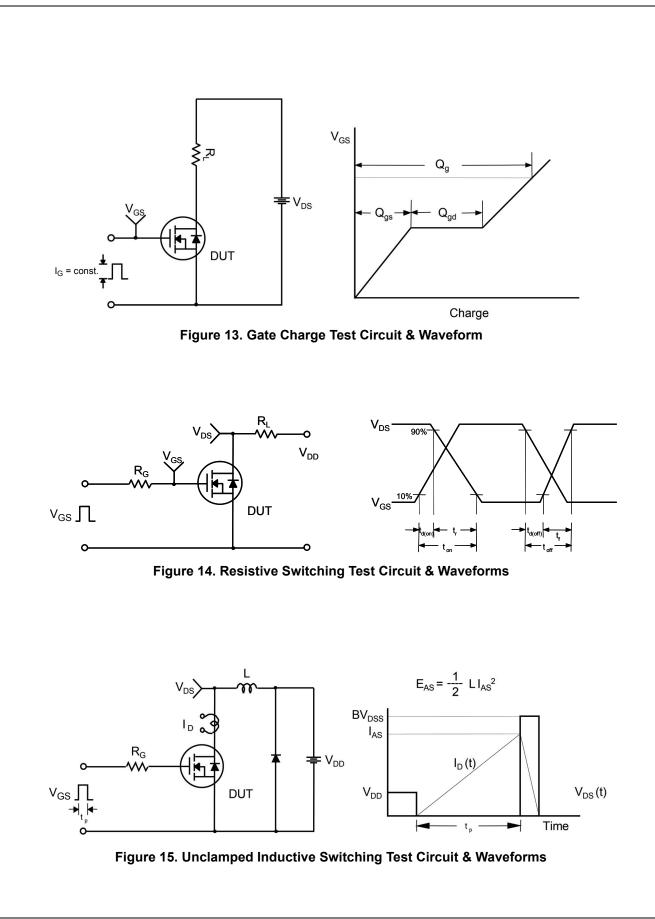
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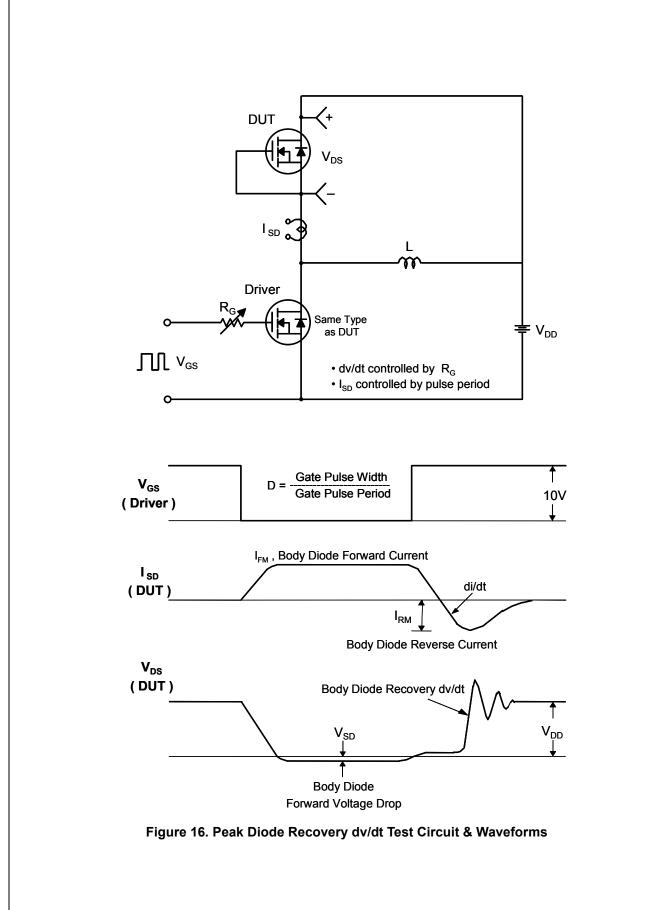
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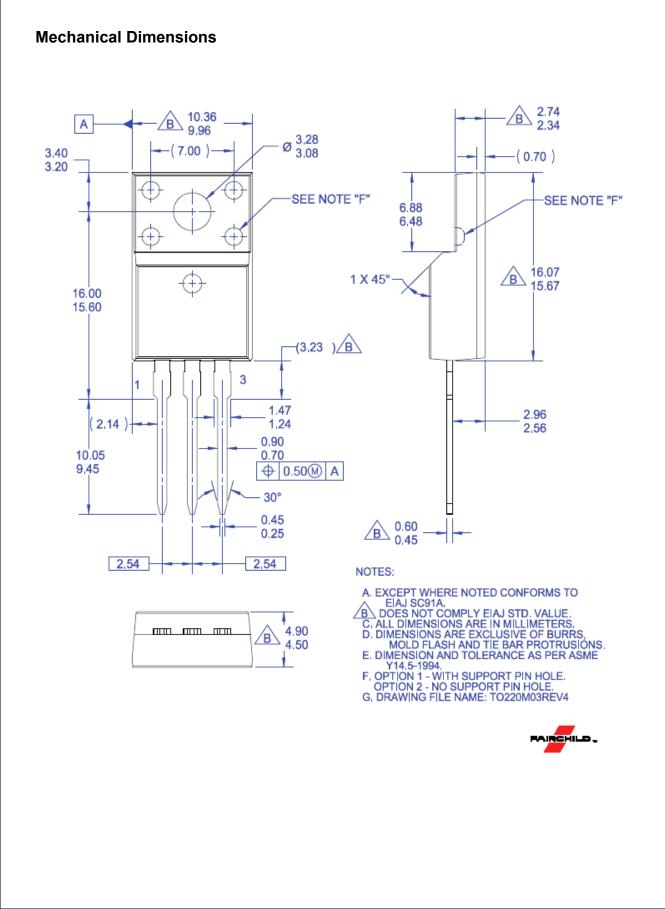




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