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Product Change Notification - SYST-19QQDG412
(Printer Friendly)

Date:

23 Oct 2017

Product Category:

Ethernet Switches

Affected CPNs:

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Notification subject:

Data Sheet - KSZ8863MLL/FLL/RLL - Integrated 3-Port 10/100 Managed Switch with PHYs Data Sheet Document Revision

Notification text:

SYST-19QQDG412

Microchip has released a new DeviceDoc for the KSZ8863MLL/FLL/RLL - Integrated 3-Port 10/100 Managed Switch with PHYs of devices. If you are using one of these devices please read the document located at KSZ8863MLL/FLL/RLL - Integrated 3-Port 10/100 Managed Switch with PHYs.

Notification Status: Final

Description of Change: 1) Table 4-2 - changed PHY1 Register 29 (PHYAD = 0x1, REGAD = 0x1D) from "Not support" to "LinkMD Control/Status". 2) Table 4-7 - changed Register 27 (0x1B) from "Not supported" to "LinkMD Result". 3) Figures 7-13 and 7-14 - updated SPI input and output timing illustrations. 4) Table 7-6 - updated SPI timing parameters. 5) Minor text changes throughout.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 23 Oct 2017

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

KSZ8863MLL/FLL/RLL - Integrated 3-Port 10/100 Managed Switch with PHYs

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