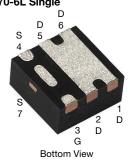
Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PowerPAK® SC-70-6L Single



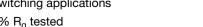


Marking code: KA

PRODUCT SUMMARY				
V _{DS} (V)	-30			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -10 \text{ V}$	0.020			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.033			
Q _g typ. (nC)	8.3			
I _D (A) ^f	-12			
Configuration	Single			

FEATURES

- TrenchFET® Gen IV p-channel power MOSFET
- Thermally enhanced PowerPAK® SC-70 package
- Provides excellent R_{DS}-Q_q Figure-of-Merit (FOM) for switching applications



• 100 % R_a tested

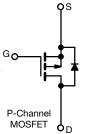
• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · Battery charging and management
- · Load switch
- DC/DC converters
- Power management in battery-operated mobile and wearable devices



RoHS HALOGEN FREE



ORDERING INFORMATION	
Package	PowerPAK SC-70
Lead (Pb)-free and halogen-free	SiA483ADJ-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-30	V	
Gate-source voltage		V_{GS}	-20 / +16		
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-12 ^f		
	T _C = 70 °C	1 ,	-12 ^f		
	T _A =25 °C	l _D	-10.6 ^{a, b}		
	T _A = 70 °C		-8.5 ^{a, b}	Α	
Pulsed drain current (t = 100 μs)		I _{DM}	-60		
Continuous source-drain diode current	T _C = 25 °C	Is	-12 ^f		
	T _A = 25 °C		-2.9 ^{a, b}		
Maximum power dissipation	T _C = 25 °C	P _D	17.9		
	T _C = 70 °C		11.4	w	
	T _A = 25 °C		3.4 ^{a, b}		
	T _A = 70 °C	1	2.2 ^{a, b}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Soldering recommendations (peak temperature) c, d			260		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient a, e	t ≤ 5 s	R _{thJA}	29	37	°C ///	
Maximum junction-to-case (drain)	Steady state	R _{thJC}	5.5	7	°C/W	

Notes

- a. Surface mounted on 1" x 1" FR4 board
- b. t = 5 s
- c. See solder profile (www.vishay.com/ppg?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components
- e. Maximum under steady state conditions is 80 °C/W
- f. Package limited



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$		-	-26	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	1	5	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1	-	-2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = -20 \text{ V} / +16 \text{ V}$	1	-	± 100	nA	
Zero gate voltage drain current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μА	
	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-10		
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = 0 \text{ V}$	-10	-	-	Α	
Drain actives on state registeres 3	_	V _{GS} = -10 V, I _D = -5 A	-	0.016	0.020		
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -3 \text{ A}$	ı	0.025	0.033	Ω	
Forward transconductance ^a	9 _{fs}	V _{DS} = -10 V, I _D = -5 A	-	25	-	S	
Dynamic ^b							
Input capacitance	C _{iss}	V _{DS} = -15 V, V _{GS} = 0 V, f = 1 MHz	-	950	-	pF	
Output capacitance	C _{oss}		-	460	-		
Reverse transfer capacitance	C _{rss}		-	50	-		
Total anto about	0	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -10.6 \text{ A}$	-	17	26	nC	
Total gate charge	Q_g	V _{DS} = -15 V, V _{GS} = -4.5 V, I _D = -10.6 A	-	8.3	12.5		
Gate-source charge	Q _{gs}	V _{DS} = -15 V, V _{GS} = -4.5 V, I _D = -10.6 A	-	3.7	-		
Gate-drain charge	Q _{gd}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -10.6 \text{ A}$	-	3	-		
Gate resistance	R _g	f = 1 MHz	1.5	8.8	15	Ω	
Turn-on delay time	t _{d(on)}		-	26	52		
Rise time	t _r	$V_{DD} = -15 \text{ V}, R_L = 1.8 \Omega, I_D \cong -8.5 \text{ A},$	-	95	190		
Turn-off delay time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	24	80		
Fall time	t _f		-	16	32	ne	
Turn-on delay time	t _{d(on)}		-	12	24	ns	
Rise time	t _r	$V_{DD} = -15 \text{ V}, R_L = 1.8 \Omega, I_D \cong -8.5 \text{ A},$	ı	6	12	- - -	
Turn-off delay time	t _{d(off)}	$V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	ı	30	60		
Fall time	t _f		1	8	16		
Drain-Source Body Diode Characteristic	cs						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-12	А	
Pulse diode forward current	I _{SM}		-	-	-60	^	
Body diode voltage	V_{SD}	I _S = -10 A, V _{GS} = 0 V	-	-0.85	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	19	38	ns	
Body diode reverse recovery charge	Q _{rr}	$I_F = -8.5 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s,}$	-	7	14	nC	
Reverse recovery fall time	t _a	T _J = 25 °C	-	9	-	ns	
Reverse recovery rise time	t _b		-	10	_		

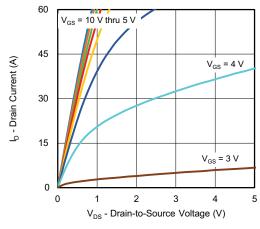
Notes

- a. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing

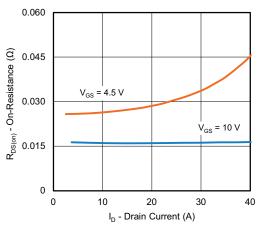
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



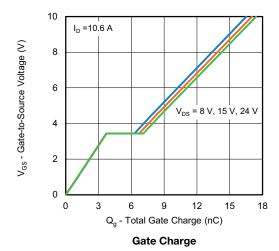
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

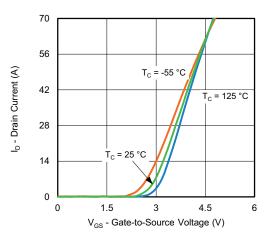


Output Characteristics

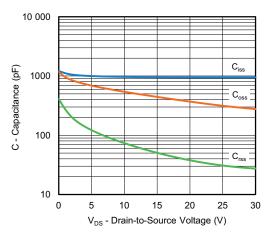


On-Resistance vs. Drain Current and Gate Voltage

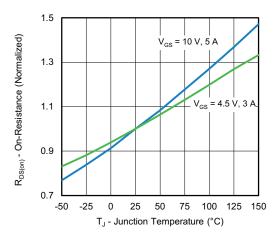




Transfer Characteristics



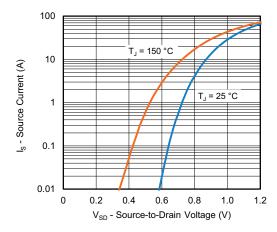
Capacitance



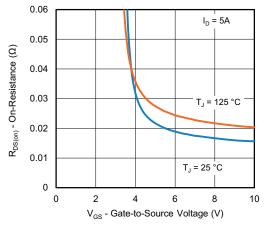
On-Resistance vs. Junction Temperature



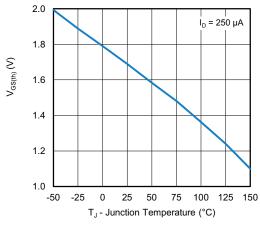
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



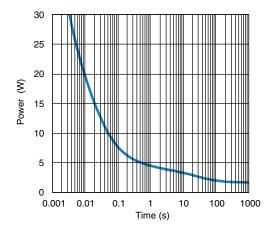
Source-Drain Diode Forward Voltage



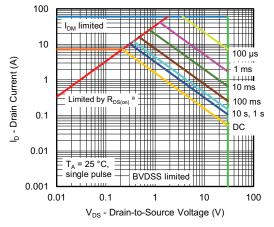
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

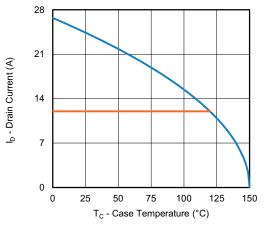
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

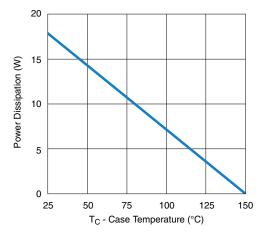
S19-0391-Rev. A, 29-Apr-2019

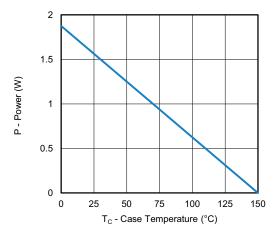
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating a





Power, Junction-to-Case

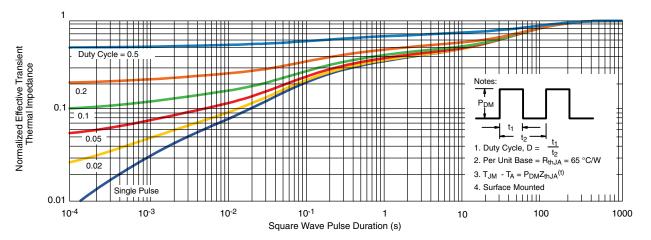
Power, Junction-to-Ambient

Note

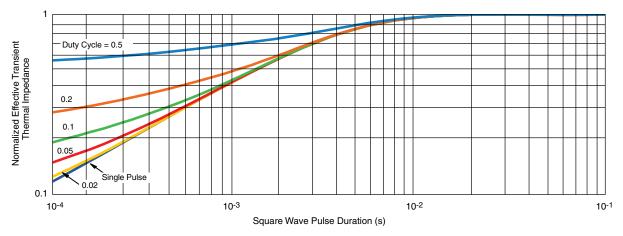
a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-ambient thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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