

The World Leader in High Performance Signal Processing Solutions



Processor Development Tools



CROSSCORE Development Tools

◆ CROSSCORE

- Analog Devices development tools product line
- Provides easier and more robust methods for engineers to develop and optimize systems by shortening product development cycles for faster time-to-market

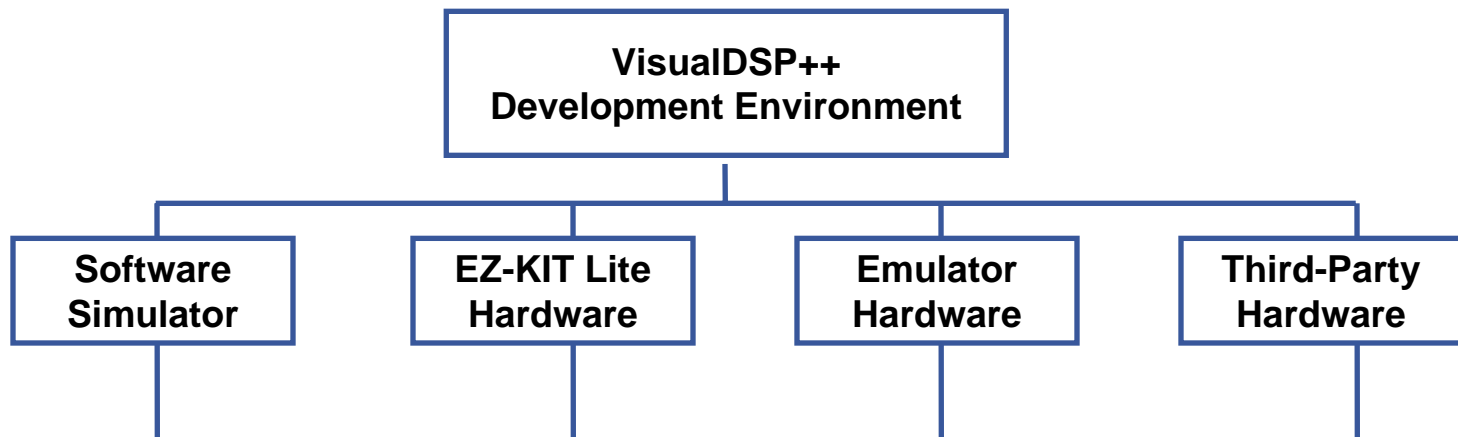
◆ CROSSCORE includes:

- VisualDSP++® Integrated Development and Debug Environment
- Emulators
 - ◆ USB
- Evaluation Boards
 - ◆ EZ-KIT Lite® 's (expandable)
- Single Board Computers
 - ◆ Available for vertical applications:
 - digital media players
 - digital still and video cameras
 - automotive telematics
 - professional audio
 - videophones
 - and more



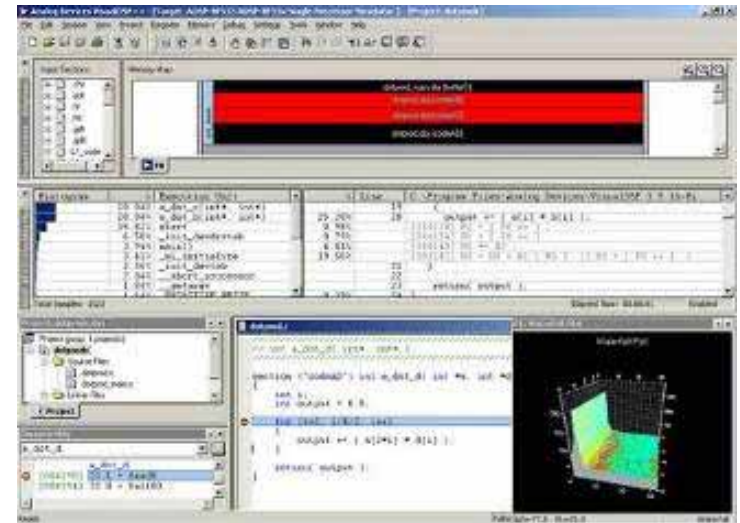
VisualDSP++

- ◆ **VisualDSP++ is a suite of easy-to-use project management tools, comprised of an integrated development and debugging environment that enables programmers to move easily between editing, building and debugging activities with a single interface**
- ◆ **VisualDSP++ offers programmers a powerful programming tool with flexibility that significantly decreases the time required to port software code to a DSP or Embedded Processor, reducing time-to-market**



VisualDSP++ Features (1 of 3)

- ◆ **Code Generation Tools**
 - **C/C++ Compiler, C/C++ Run-Time Library, DSP & Math Libraries, Assembler, Linker, Loader & Splitter**
- ◆ **Connectionless IDDE**
- ◆ **Session Wizard**
- ◆ **Automatic Breakpoints**
- ◆ **Compiled Simulation**
- ◆ **Compiler Annotations**
- ◆ **Profile Guided Optimization**
- ◆ **Expert Linker**
- ◆ **Cache and Pipeline Viewer**
- ◆ **VisualDSP++ RTOS/Kernel/Scheduler (VDK)**
- ◆ **Background Telemetry Channel Support**
- ◆ **Statistical Profiling and Graphical Plotting**





VisualDSP++ Features (2 of 3)

- ◆ **Multiple Processor (MP) Support, Multi-Project support**
- ◆ **Royalty-Free Run Time Libraries**
- ◆ **COM Automation-aware Scripting**
- ◆ **Integrated Source Code Control**
- ◆ **Online Help**
- ◆ **System Builder**
- ◆ **Start up code Wizard**
- ◆ **Custom Board Support**
- ◆ **Core File Support**
- ◆ **MISRA-C:2004**
- ◆ **Ethernet support**
- ◆ **Image Viewer**
- ◆ **Loader Compression**
- ◆ **Stand-alone Flash Programmer (SAFP)**
- ◆ **Energy-Aware Programming**



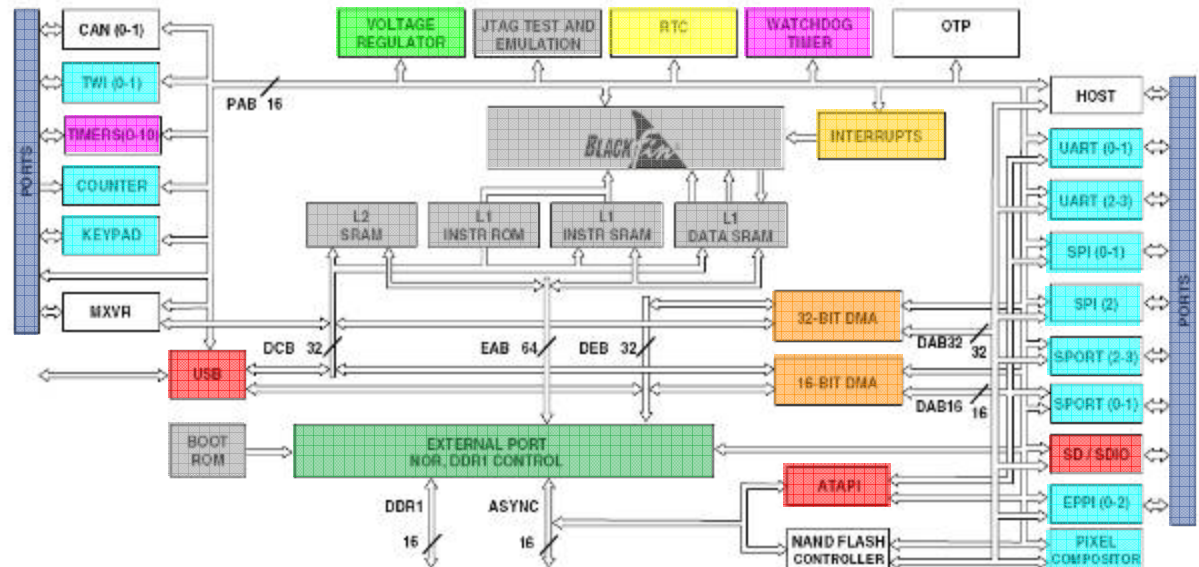
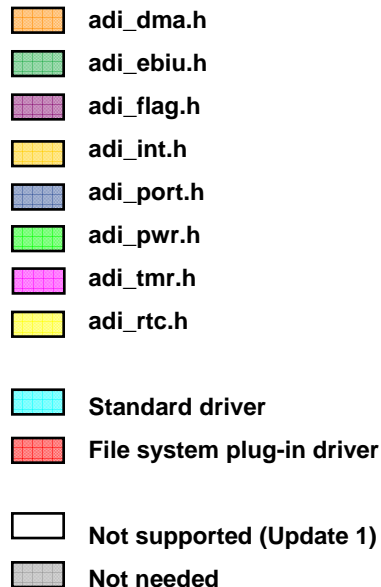
VisualDSP++ Features (3 of 3)

- ◆ **Free technical support**
 - **Integrated into VisualDSP++ environment**
 - **Equal attention to the smallest and largest of customers**
- ◆ **No per-unit royalties or other per-unit costs**
- ◆ **Free VisualDSP++ update and migration to future releases**
- ◆ **The test drive is a 90-day evaluation of full VisualDSP++ package**
 - **Available from Analog Devices' website:**
 - ◆ <http://www.analog.com/processors/testDrive>

Device Drivers & System Services

- ◆ **High-level APIs that configure hardware**
 - Care for external timing configuration
 - Handle port muxing
 - Configure and use peripherals
 - Set-up DMA activity and respond to DMA events
 - Change power modes, CCLK, and SCLK
- ◆ **User does not need to touch low-level registers**

ADSP-BF54x DD&SS in VisualDSP++ 5.0 (Update 1)



Blackfin Processors Development Tools (1 of 2)

Processor	Evaluation Platform	Emulator	Software	Third Party Tools
ADSP-BF522 ADSP-BF522C ADSP-BF524 ADSP-BF524C ADSP-BF526 ADSP-BF526C	---	---	- VisualDSP++ 5.0¹ - Free Upgrade to 5.0	---
ADSP-BF523 ADSP-BF523C ADSP-BF525 ADSP-BF525C ADSP-BF527 ADSP-BF527C	- BF527 EZ-KIT Lite Desktop Evaluation Board	- USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec	- VisualDSP++ 5.0¹ - Free Upgrade to 5.0	
ADSP-BF531 ADSP-BF532 ADSP-BF533	- BF533 EZ-KIT Lite Desktop Evaluation Board - Blackfin EZ-Extender Daughter Board - Blackfin A-V EZ-Extender Daughter Board - Blackfin USB-LAN EZ-Extender Daughter Board - Blackfin FPGA EZ-Extender Daughter Board - Blackfin Audio EZ-Extender Daughter Board - Blackfin Multimedia Starter Kit	- USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec	- VisualDSP++ 5.0¹ - Free Upgrade to 5.0 - VisualAudio - Software Development Kit (SDK) - LabVIEW Embedded for Blackfin	- Mathworks - Green Hills Software - uClinux Kernel + GNU Software - LabVIEW Embedded for Blackfin

Blackfin Processors Development Tools (2 of 2)

Processor	Evaluation Platform	Emulator	Software	Third Party Tools
ADSP-BF534 ADSP-BF536 ADSP-BF537	<ul style="list-style-type: none"> - Audio Starter Kit - BF537 EZ-KIT Lite Desktop Evaluation Board - Blackfin USB-LAN EZ-Extender Daughter Board - Blackfin A-V EZ-Extender Daughter Board - BF537 STAMP Kernel BSP uClinux Kernel Board Support Pkg - Blackfin FPGA EZ-Extender Daughter Board - Blackfin Audio EZ-Extender Daughter Board 	<ul style="list-style-type: none"> - USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec 	<ul style="list-style-type: none"> - VisualDSP++ 5.0¹ - Free Upgrade to 5.0 - VisualAudio - LabVIEW Embedded for Blackfin - Software Development Kit (SDK) 	<ul style="list-style-type: none"> - Mathworks - Green Hills Software - uClinux Kernel + GNU Software - LabVIEW Embedded for Blackfin - Phytex
ADSP-BF535	---	<ul style="list-style-type: none"> - USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec 	<ul style="list-style-type: none"> - VisualDSP++ 5.0¹ - Free Upgrade to 5.0 	<ul style="list-style-type: none"> - Green Hills Software
ADSP-BF538 ADSP-BF538F	<ul style="list-style-type: none"> - BF538F EZ-KIT Lite Desktop Evaluation Board 	<ul style="list-style-type: none"> - USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec 	<ul style="list-style-type: none"> - VisualDSP++ 5.0¹ - Free Upgrade to 5.0 	<ul style="list-style-type: none"> - Green Hills Software
ADSP-BF542 ADSP-BF544 ADSP-BF547 ADSP-BF548 ADSP-BF549	<ul style="list-style-type: none"> - BF548 EZ-KIT Lite Desktop Evaluation Board 	<ul style="list-style-type: none"> - USB-based Emulator USB 1.1, up to 150 KB/sec - High Perf USB-based Emulator USB 2.0, up to 1.5 MB/sec 	<ul style="list-style-type: none"> - VisualDSP++ 5.0¹ - Free Upgrade to 5.0 	<ul style="list-style-type: none"> - LabVIEW Embedded for Blackfin
ADSP-BF561	<ul style="list-style-type: none"> - BF561 EZ-KIT Lite Desktop Evaluation Board 	<ul style="list-style-type: none"> - USB-based Emulator USB 1.1, up to 150 KB/sec 	<ul style="list-style-type: none"> - VisualDSP++ 5.0¹ - Free Upgrade to 5.0 	<ul style="list-style-type: none"> - Green Hills Software - uClinux Kernel + GNU Software

USB Emulators

- ◆ Provide non-intrusive target-based debugging of processor systems
- ◆ Wide range of functions including single-step and full-speed execution with pre-defined breakpoints, viewing and/or altering of register and memory contents
- ◆ **HP-USB**
 - Download speed 1.5MB/sec
- ◆ **USB**
 - Download speed ~150KB/sec
- ◆ **Both Support**
 - All ADI JTAG processors and DSPs
 - Multiple processor and DSP I/O voltage support with automatic detection
 - 1.8V, 2.5V, and 3.3V compliant and tolerant
 - 5V tolerant and 3.3V compliant for 5V processors and DSPs
 - Multiprocessor support
 - 14-pin JTAG connector
 - 3-meter USB cable for difficult-to-reach targets
 - CE-certified
- ◆ **Part Number:**
 - **High Performance USB-Based Emulator**
 - ◆ Part Number: ADZS-HPUSB-ICE
 - ◆ Cost: \$4000.00
 - **USB-Based Emulator**
 - ◆ Part Number: ADZS-USB-ICE
 - ◆ Cost: \$1200.00



ADZS-BF527-EZLITE

◆ Key Features:

- ADSP-BF527 Blackfin processor (600 MHz)
- SDRAM: Micron MT48LC32M16A2TG 64 MB
- Parallel flash memory: ST Micro M29W320EB – 32 Mb
- NAND flash memory: ST Micro NAND04 – 4 Gb
- SPI flash memory: ST Micro M25P16 – 16 Mb
- Low-power audio codec
- 1 stereo LINE OUT jack
- 1 input MIC jack
- 1 input stereo LINE IN jack
- TFT LCD display with touchscreen
 - ◆ Varitronix VLGT-6272-01 – 320 x 240, 3.5" touchscreen LCD
- Maxim MAX1233 – touchscreen and keypad controller
- Ethernet interface (10/100 Mbps/sec): SMSC LAN8700 PHY
- Keypad ACT components– 4 x 4 keypad assembly
- Thumbwheel: CTS Corp rotary encoder
- Universal asynchronous receiver/transmitter (UART)
- Eight LEDs: one power (green), one board reset (red), three general-purpose (amber), and one USB monitor (amber), PHY link (amber), PHY activity (green).
- 3 push buttons: one reset, two programmable flags with debounce logic
- Expansion interface: all ADSP-BF527 processor signals
- Other features
 - ◆ JTAG ICE 14-pin header
 - ◆ Connectors for: USB OTG, HOST interface, PPI, SPORT0 and SPORT1, TWI, SPI, timers, UART0
 - ◆ Blackfin power measurement jumpers
- Cost: \$895.00



ADZS-BF527-EZLITE





ADZS-BF548-EZLITE

- ◆ **Key Features:**
 - **ADSP-BF548 Blackfin processor (600 MHz)**
 - **DDR SDRAM: Micron MT46V32M16: 64 MB**
 - **Burst flash memory: Intel PC28F128K3C115 32 MB**
 - **NAND flash memory: ST Micro NAND02 – 2 Gb**
 - **SPI flash memory: ST Micro M25P16 – 16 Mb**
 - **ATAPI Interface and Toshiba 2.5” MK4032GAX – 40 GB HDD**
 - **Analog Devices AD1980 SoundMAX codec**
 - **6 DAC channels for 5.1 surround**
 - **1 input stereo MIC jack**
 - **1 input stereo LINE IN jack**
 - **1 output stereo LINE OUT/HEAD PHONE OUT jack**
 - **1 output stereo SURROUND jack**
 - **1 output center and LFE jack**
 - **TFT LCD display with touchscreen**
 - ◆ Sharp LQ043T1DG01 – 480 x 272, 4.3” touchscreen LCD
 - ◆ AD7877 – touchscreen controller
 - **Ethernet interface (10/100 Mbits/sec): SMSC LAN9218 device**
 - **Keypad: ACT components– 4 x 4 keypad assembly**
 - **Thumbwheel: CTS Corp rotary encoder**
 - **Universal asynchronous receiver/transmitter (UART)**

- **10 LEDs: 1 power (green), 1 board reset (red), 1 USB (red), 6 general-purpose (amber), and 1 USB monitor (amber)**
- **5 push buttons: 1 reset, 4 programmable flags with debounce logic**
- **Expansion interface: all ADSP-BF548 processor signals**
- **Other features**
 - ◆ JTAG ICE 14-pin header
 - ◆ Connectors for: USB OTG, HOST interface, PPI1, SPORT2 and SPORT3, TWI, SPI, timers, UART3
 - ◆ Blackfin power measurement jumpers
- **Cost: \$995.00**



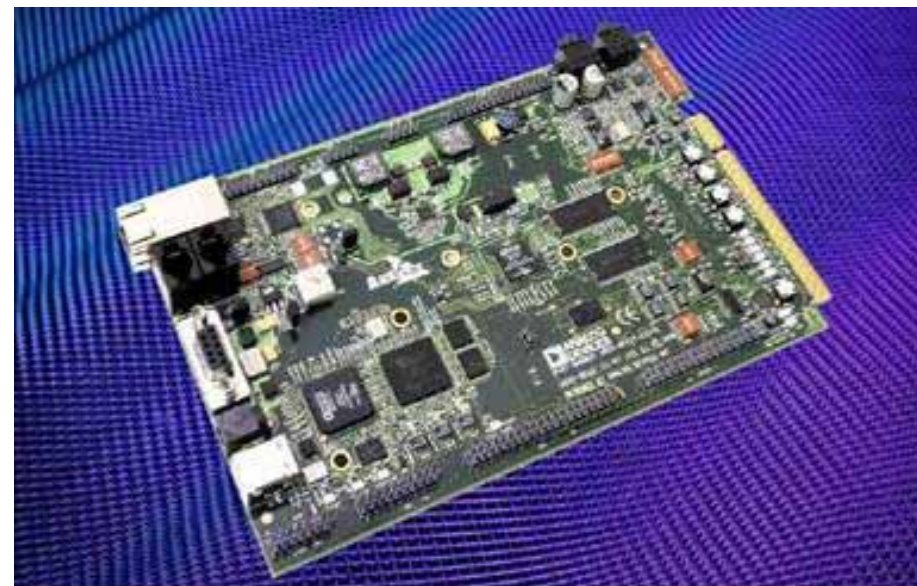
ADZS-BF548-EZLITE



ADZS-BF537-EZLITE

◆ Key Features

- ADSP-BF537 Blackfin processor (600 MHz)
- SDRAM: MT48LC32M8 – 64 MB
- Flash memory: 4 MB
- AD1871 96 kHz ADC
- AD1854 96 kHz DAC
- 1 input stereo jack
- 1 output stereo jack
- Ethernet interface (10/100 Mbits/sec)
- Philips TJA1041 high-speed CAN transceiver
- National Instruments Educational Laboratory Virtual Instrumentation Suite (ELVIS) interface
- Universal asynchronous receiver/transmitter (UART)
- 10 LEDs: 1 power (green), 1 board reset (red), 1 USB (red), 6 general-purpose (amber), and 1 USB monitor (amber)
- 5 push buttons: 1 reset, 4 programmable flags with debounce logic
- Expansion interface
- JTAG ICE 14-pin header
- Cost: \$350.00





ADZS-BF538F-EZLITE

- ◆ **Key Features**
 - ADSP-BF538F Blackfin processor (600 MHz)
 - SDRAM: MT48LC32M8 – 64
 - Flash memory: 4MB
 - AD1871 96 kHz ADC
 - AD1854 96 kHz DAC
 - 1 input stereo jack
 - 1 output stereo jack
 - Philips TJA1041 high-speed CAN transceiver
 - National Instruments Educational Laboratory Virtual Instrumentation Suite (ELVIS) interface
 - Universal asynchronous receiver/transmitter (UART)
 - 10 LEDs: 1 power (green), 1 board reset (red), 1 USB (red), 6 general purpose (amber), and 1 USB monitor (amber)
 - 5 push buttons: 1 reset, 4 programmable flags with debounce logic
 - Expansion interface
 - JTAG ICE 14-pin header
 - Cost: \$350.00





ADZS-BF533-EZLITE

◆ Key Features

- ADSP-BF533 Blackfin processor (600 MHz)
- SDRAM: MT48LC32M16 - 64 MB
- Flash memories: 2 MB
- AD1836 – Analog Devices 96 kHz audio codec
- 4 input RCA phono jacks (2 channels)
- 6 output RCA phono jacks (3 channels)
- ADV7183 video decoder w/ 3 input RCA phono jacks
- ADV7171 video encoder w/ 3 output RCA phono jacks
- Universal asynchronous receiver/transmitter (UART)
- 10 LEDs: 1 power (green), 1 board reset (red), 1 USB (red), 6 general purpose (amber), and 1 USB monitor (amber)
- 5 push buttons with debounce logic: 1 reset, 4 programmable flags
- Expansion interface
- JTAG ICE 14-pin header
- Cost: \$400.00

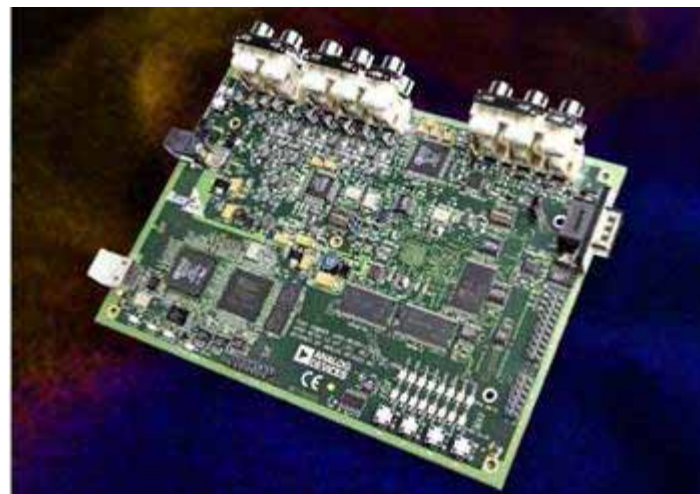




ADZS-BF561-EZLITE

◆ Key Features

- ADSP-BF561 Blackfin processor (600 MHz)
- SDRAM: 64 MB
- Flash memory: 8 MB
- AD1836A – Analog Devices 96 kHz audio codec
- 4 input RCA phono jacks (2 channels)
- 6 output RCA phono jacks (3 channels)
- ADV7183A video decoder w/ 3 input RCA phono jacks
- ADV7179 video encoder w/ 3 output RCA phono jacks
- Universal asynchronous receiver/transmitter (UART)
- 20 LEDs: 1 power (green), 1 board reset (red), 1 USB (red), 16 general-purpose (amber), and 1 USB monitor (amber)
- 5 push buttons with debounce logic: 1 reset, 4 programmable flags
- Expansion interface
- JTAG ICE 14-pin header
- Cost: \$495.00



ADZS-BF-EZEXT-1

◆ Key Features

- OmniVision camera interface for connecting to OmniVision OV6630AA digital camera evaluation board
- 32-pin, 0.1" spacing, right angle, female socket connector
- Analog Devices High Speed Converter (HSC) interface for connecting to HSC evaluation boards (ADC, mixed-signal, and DAC)
- 40-pin, 0.1" spacing, right angle, female connector
- 40-pin, 0.1" spacing, male connector
- External LCD display interface
- 32-pin, right angle FLZ-type connector
- RJ45 connector for providing SPI signals for configuring converter registers
- SMT footprint area
- 1206 and 805 footprints
- SOIC24 and SOIC20 footprints
- Dimensions 5" (H) x 5" (W)
- Cost: \$195.00





ADZS-BFAUDIO-EZEXT

◆ Key Features

- Analog Audio
- AD1938 (x2) - Analog Devices 192 kHz audio codec
- 16 Channels Audio Out (8 stereo channels)
- 8 Channels Audio In (4 stereo channels)
- RCA phono jacks for all 24 channels
- SPDIF (Digital) Audio
- ADAV801 - Audio Codec for Recordable DVD
- SPDIF In
- SPDIF Out
- Individual RCA phono jacks for in and out
- Cost: \$225.00





ADZS-BFAV-EZEXT

◆ Key Features

- Audio Interface with 2 channels of stereo
- input and 3 channels of stereo output
- Analog Devices AD1836 –96 kHz audio codec
- Analog Video Output for CVBS and S-Video output formats
- Analog Devices ADV7179 video encoder
- Analog Video Input for CVBS, S-Video, and YPrPb component video input formats
- Analog Devices ADV7183B video decoder
- Camera Sensor Evaluation Board Interfaces
- Connection to Micron, OmniVision, and Kodak evaluation boards (for details on specific cameras, see the manual)
- Flat panel display Interface
- Cost: \$225.00





ADZS-USBLAN-EZEXT

◆ Key Features

- USB 2.0 Interface
- PLX Technology's Netchip 2272 device
- USB Driver and Application code
- USB Logo Certified
- Ethernet Interface
- SMSC's LAN91C111 device
- SMSC's MII Connector to evaluate different PHYs with the BF537 EZ-KIT Lite
- Ethernet Stack and Application code
- No Dedicated Power Supply
- Derives power from EZ-KIT Lite
- Cost: \$225.00



ADZS-BFFPGA-EZEXT

◆ Key Features

- Xilinx Spartan III FPGA (XC3S1000)
- 2MB of SRAM
- 25 MHz oscillator
- Socket for auxiliary oscillator
- IDC thru-hole connectors
 - ◆ Allows quick access to Blackfin and FPGA pins for probing
 - ◆ Allows access to Blackfin and FPGA pins for off-board connections
- High-speed connector
 - ◆ Allows access to Blackfin and FPGA pins for high-speed application
- Two push buttons
- Eight flag LEDs
- Cost: \$225.00

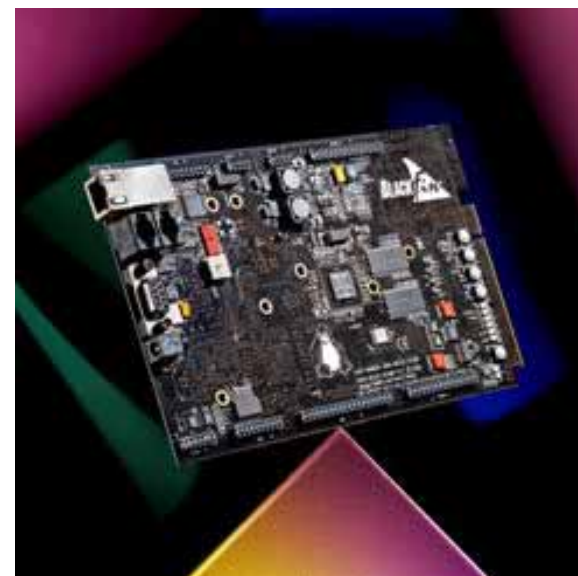




ADZS-BF537-STAMP

◆ Key Features

- Linux OS based on uClinux 2.6.x
- GNU gcc (C/C++) support with gdb/kgdb debug capabilities over Ethernet and JTAG
- Das U-Boot bootloader
- Complete Linux IP stack including standard protocols
- Device drivers for on board peripherals
- ADSP-BF537 500 MHz Blackfin Processor
- IEEE 802.3-Compliant 10/100 Ethernet MAC
- Controller Area Network (CAN) 2.0B Interface
- 64 Mbytes SDRAM
- 4 Mbytes Flash Memory
- RS232 serial interface
- I/O connectors for Blackfin peripherals (SPI, two-wire interface, IrDA™, SPORT0 and SPORT1,
- Timers, PPI (general purpose parallel high speed interface, glueless TFT flat panel)
- JTAG interface for debug and FLASH programming
- Cost: \$200.00



ADI Software Modules

- ◆ **ADI offers Software Modules**
 - Available for Blackfin and SHARC
 - Work within Starter Kits, Core Platforms, and Market-focused Platforms
 - Require no payment to ADI



Released/Available Software Modules



- ◆ **Audio Decode:**
 - DTS 5.1 released
 - DTS Neo:6 / ES released
 - Dolby Digital AC3 released
 - Dolby Headphone v2 released
 - Dolby Virtual Speaker released
 - DPLIIX / EX released
 - MP3 released
 - WMA9 Decoder released
 - MPEG-4 HE-AAC v1 released
 - MPEG-4 HE-AAC v2 released
 - MPEG-4 AAC BSAC released
- ◆ **Audio Encode:**
 - MPEG-4 HE-AAC v2 released
 - DDCE released
 - MP3 released
 - WMA9 Encoder beta
- ◆ **Video Decode:**
 - MPEG-4 SP/ASP released
 - H.264 BP released
 - WMV9 beta
 - MPEG-2 beta
- ◆ **Video Encode:**
 - MPEG-4 SP/ASP released
 - H.264 BP released



- ◆ **Audio Decode:**
 - DTS 5.1 released
 - DTS Neo:6 / ES released
 - Dolby Digital AC3 released
 - Dolby Headphone v2 released
 - Dolby Virtual Speaker released
 - DPLIIX / EX released
- ◆ **NOTES:**
 - Ogg Vorbis and Ogg Speex for Blackfin available as part of the SDK
 - Other modules are available for SHARC in SHARC ROMs
 - Other modules available via 3rd parties



Software Module Details

- ◆ **Evaluation vs. Production Code**
 - Most modules have a timed-out (beeps after 10-minutes) evaluation version available
 - Production code has no time-out – provided with agreement
- ◆ **Source Code vs. Object Code**
 - All modules are available as object code
 - Source code may be available on an exception basis
- ◆ **Special Licensing Restrictions for Certain Modules**
 - Customer must be a licensee for Dolby, DTS, or Microsoft before code can be shipped
 - Code must be certified on a specific processor

2 Clicks away ...

Blackfin

SHARC

Blackfin® Processors embody a new breed of 16/32-bit embedded processor, ideally suited for products where a convergence of capabilities are necessary – multi-format audio, video, voice and image processing...
[Go to Blackfin](#)

SHARC® Processors, dominate the floating-point Digital Signal Processing market, delivering exceptional performance complemented by outstanding I/O throughput. Starting at 319 MFLOPS per dollar...
[Go to SHARC](#)

Software & Ref. Designs

- Download free software from the site. Analog Devices and its partners have a broad range of offerings from device drivers to complete reference designs.

Technical Support

- Knowledge Base
- Contact Technical Support
- Technical Library
- PC Anomalies
- Tools Anomalies

News and Events

- Blackfin in the News
- Press Releases
- Subscribe to eNewsletters
- Trade Shows and Events

Customer Case Studies

Processors in Applications

- Automotive
- Digital Home
- Industrial
- Portable Media Playback
- Security
- Voice over IP (VoIP)

ANALOG DEVICES World leader in high performance signal processing solutions

News

- Video: Students at the Nanyang Polytechnic develop a hexapod robot based on Blackfin and LabVIEW Embedded technology.
- ADI's High Speed Analog-to-Digital Behavior Models Available for Agilent's Advanced Design System Eda Software Platform
- Industry's First Isolated Gate Drivers With Integrated Isolated Power

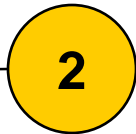
Education

- Embedded Processing Speedway Design Workshop by Silica Breda, Netherlands: April 1 - Brussels, Belgium: April 3
- Blackfin System Development and Programming Workshop San Jose, CA, USA: April 15-18 Norwood, MA, USA: April 29 - May 2

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 - TigerSHARC Processor
 - SHARC Processor
 - ADSP-21xx Processor



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Software and Reference Designs

Analog Devices (ADI) and its partners have a broad range of offerings that let you explore, evaluate and design with our processors. From device drivers to complete reference designs, Analog Devices can provide you with what you need to develop your next product.

Quick Navigation:

Starter Kits

Starter Kits provides you with an EZ-KIT Lite evaluation kit and EZ-Extenders daughter card(s), along with a Software Development Kit (SDK) that makes getting started easy and shortens the learning curve.

Kit Name	Processor	Description
Multimedia Starter Kit	Blackfin	Evaluation board, Multimedia SDK, documentation
Audio Starter Kit	Blackfin	Evaluation board, Multimedia SDK, documentation

Software Development Kits (SDK)

[Software Development Kits \(SDK\)](#): contains example software, source code, device drivers, algorithms, utilities information and application notes that allow you to develop processor applications. The software can be used as a framework, or as examples of how to use certain aspects and peripherals, in conjunction with an ADI processor.

[VisualDSP++ Development Software Test Drive](#)
[Knowledge Base](#)
[Manual: Getting Started with Blackfin](#)
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“ADI's Blackfin processor provides the processing performance we needed to quickly stream music from a computer to a stereo system.”



Software Modules Request Process *NEW*

- ◆ www.analog.com/requestsoftware
- ◆ Optionally you can login on the ADI website for easy form fill
- ◆ The Softwaremodules come up after you selected the Processor on that page
- ◆ Technical support software.module.support@analog.com



Code Delivery

◆ Code delivery includes:

- Library module with a C-callable API (application programming interface) consistent with other ADI software modules
- C source application example, which calls the above library module
- Real-time demonstration executable running on ADI evaluation boards (i.e. EZ-Kits, EZ-Extenders)
- Documentation, including Application Note and detailed Developer's Guide

Starter Kits & SDK

◆ Starter Kits

www.analog.com/processors/platforms/

- Collection of ADI off-the-shelf hardware, SDK and software tools

● Multimedia Starter Kit

◆ ADZS-BF561-MMSKIT

- ADSP-BF561 EZ-KIT Lite
- A-V and USB-LAN EZ-Extender boards
- VisualDSP++ (evaluation version)
- Blackfin SDK software CD
- Cost: \$895.00

◆ ADZS-BF533-MMSKIT

- ADSP-BF533 EZ-KIT Lite
- A-V and USB-LAN EZ-Extender boards
- VisualDSP++ (evaluation version)
- Blackfin SDK software CD
- Cost: \$800.00

● Audio Starter Kit

◆ ADZS-BF537-ASKIT

- ADSP-BF537 EZ-KIT Lite
- Audio and USB-LAN EZ-Extender boards
- VisualDSP++ (evaluation version)
- Blackfin SDK software CD
- Cost: \$750.00

◆ Software Development Kit (SDK)

www.analog.com/processors/platforms/sdk.html

- Free applications software that allow you to develop Blackfin processor based applications

Included in the Blackfin SDK for Multimedia:	ADSP-BF561	ADSP-BF533	ADSP-BF537
Edge Detection with Graphics Overlay	x	x	
JPEG encoder and decoder	x	x	
MJPEG encoder and decoder	x	x	
Ogg Vorbis CODEC	x	x	
Ogg Speex CODEC	x	x	
Simple Raster Graphics Package (SRGP)	x	x	
Video and image processing utilities	x	x	
Application notes and related documents	x	x	
High-speed Blackfin and PC communication drivers and utilities	x	x	
Source Code and supporting device drivers	x	x	
Frame Capture			x
Frame Display			x
Included in the Blackfin SDK for Audio:	ADSP-BF533	ADSP-BF537	
Audio Player	x	x	
Application notes and related documents			x
High-speed Blackfin and PC communication drivers and utilities			x
Source code and supporting device drivers			x



Tools/OS/Stacks Ecosystem

◆ 16/32-bit Initiative Driven

● Tools

- ◆ ADI VisualDSP++
- ◆ Green Hills Software (GHS) MULTI
- ◆ LabVIEW™ Embedded
- ◆ GCC

● RTOS / OS

- ◆ **ADI VisualDSP++ Kernel**
- ◆ Express Logic ThreadX
- ◆ Accelerated Technologies Nucleus+
- ◆ GHS Integrity (w/ VxWorks API)
- ◆ GHS Velocity
- ◆ **uClinux**
- ◆ ETAS Group
- ◆ Unicoi Systems Fusion™
- ◆ Kadak Kwik-Net
- ◆ Quadros RTXCTM
- ◆ **Micrium uC/OSII**

◆ Specific Market Driven

- Industrial CAN packages via Third parties
- Automotive CAN package via Vector CANtech

◆ Networking Stacks

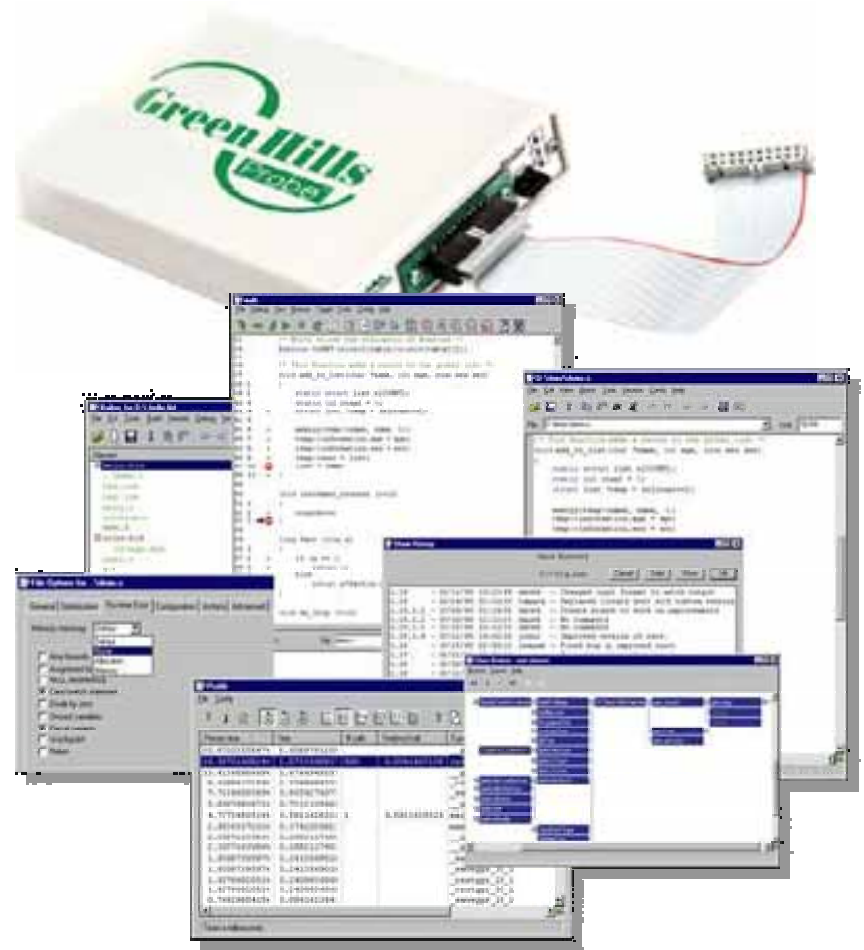
- LwIP (Included, requires VDK)
- uIP (on Blackfin.org, no OS required)
- Kwik-Net (Kadak)
- Fusion Net (Unicoi Systems)
- NetX (Express Logic)
- Quadros QuadNET

◆ More General

- STAMP boards with daughtercards
- Open Source / uClinux

Green Hills Software® Development Tools Partnership

- ◆ **MULTI® IDE** integrated with **VisualDSP++** compiler and simulator
 - Graphical display of compiler options
 - Full debugging support
- ◆ **Green Hills Probe™**
 - Full run control and download support
- ◆ **Complete GHS IDE & toolset**
 - Includes fully validated GHS C and C++ compiler



Linux Open Source Community

www.blackfin.uclinux.org

Linux BLACKfin Log In | New Account

Software/Group Search

HOME MY PAGE PROJECT TREE CODE SNIPPETS PROJECT OPENINGS

Welcome to the uClinux Blackfin processor Projects!

This web site is designed to be the central repository and open source workspace for non-commercial software and hardware projects targetted for use with Analog Devices' family of Blackfin processors. In addition to a wide range of applications, this workspace also focuses on supporting open source hardware and software tools for the Blackfin processor, and supports Open Source hardware and software tools, including the GNU GCC toolchain and the uClinux kernel.

There are 7 main projects run on this site. A [complete list](#) is available.

- **GCC Toolchain**
 - Standard Releases
 - Source Code CVS
 - Get Help on user forums
- **uClinux kernel for the Blackfin Processor**
 - Standard Releases
 - Source Code CVS
 - Get Help on user forums
- **Das U-boot for the Blackfin**
 - Standard Releases
 - Source Code CVS
 - Get Help on user forums
- **Parallel Port JTAG ICE**
 - Program to Write directly to Flash via JTAG
 - Program for doing gdb via JTAG
 - Order a IGLOO
- **CoLinux (Run Linux on Windows)**
 - Standard Releases

Order STAMP board now.

Blackfin Statistics

Hosted Projects: 15
Registered Users: 1,306

Top Project Downloads

- (108,756) **Stamp Development Platforms and Modules**
- (51,091) **uClinux for the Blackfin processor**
- (46,215) **GNU Toolchain for Blackfin gcc 3.x**
- (21,405) **Blackfin/uClinux Documentation Project**
- (13,039) **Uboot for the Blackfin processor**
- (6,411) **JTAG Tools for Blackfin**
- (5,315) **Uboot for the 535 Blackfin processor**
- (4,889) **uClinux for 535 Blackfin processor**
- (1,744) **gdb/jtag debugging**
- (827) **Blackfin XMAME**

[More]

Links:
[Blackfin.org](#)

Internet





Welcome to Ronetix's site

Ronetix's development tools give you a more efficient and economical way to develop embedded systems products.

▶ PEEDI High Speed JTAG/BDM Emulator

- All ARM7/9 and XScale based MCUs
- **ARM11 based MCUs**
- PowerPC MPC55xx, Nexus Class I
- ColdFire MCF52xx, MCF53xx, MCF54xx
- **Blackfin BF5xx, BF532, BF533, BF537**
- Multi Core support - up to 4 cores
- Built in support for GNU gdb/insight
- Built in support for ARM ADP/RDI
- Target Flash Memory programming
- Standalone Flash Programmer
- 10/100Base Ethernet interface
- Linux Kernel debugging with MMU
- uClinux support
- Full access to all CP15 registers

▶ FLASH PROGRAMMER JTAG/BDM Programmer for on-board and on-chip Flash devices

- Works in **Standalone mode**: the file images are stored on a MMC/SD card
- Automatically starts programming
- Supports all ARM7, ARM9, ARM11, XScale based MCUs as well as AT91SAM7, LPC2000, MAC7100, STR7, STR9 and TMS470, MPC55xx, MCF5282, TMS320DM6446, iMX31
- Supports Nexus enabled PowerPC MPC5500, MPC55XX
- Supports ColdFire MCF52xx
- **Supports Blackfin BF5xx, BF533, BF537**
- Atmel AT45DB SPI Data Flash
- **NAND Flash programming**
- NAND Flash JFFS2 support
- Small, robust aluminium case

▶ CPU MODULES Time and Cost effective solutions

- CPU Module with AT91SAM9261
- CPU Module with AT91SAM9263
- **Starter Kit for BLACKFIN**



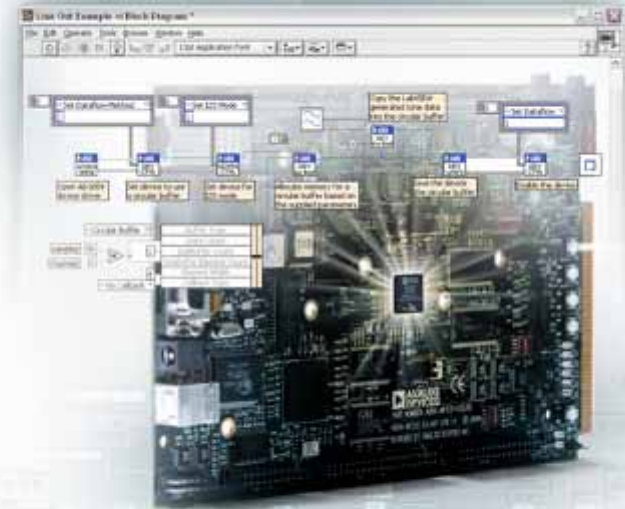
[more](#)

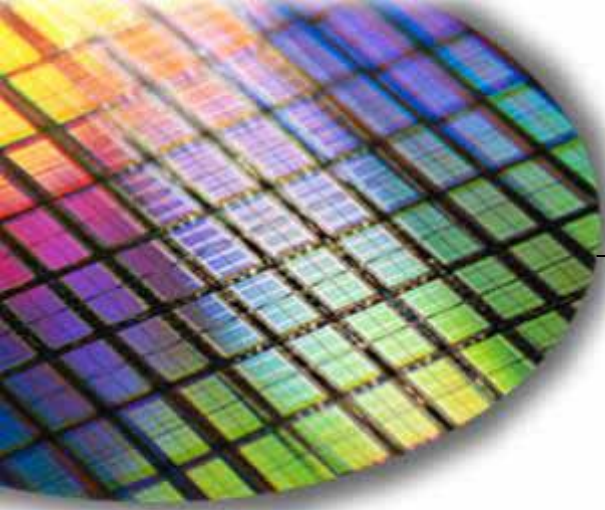
[more](#)

[more](#)

LabVIEW™ Embedded Module for Blackfin

- ◆ A comprehensive Graphical Programming development environment for embedded design jointly developed by Analog Devices and National Instruments
- ◆ Integrates LabVIEW and VisualDSP++ to deliver an easy to use programming toolset
- ◆ Graphical programming with LabVIEW on Blackfin is
 - Optimized C code generation technology
 - Linking of VisualDSP++ compiler and LabVIEW
- ◆ Targets Blackfin ADSP-BF533/BF537





The World Leader in High Performance Signal Processing Solutions



Documentation



1: www.analog.com

- ◆ **Manuals**
- ◆ **EE-Notes**
- ◆ **Knowledgebase**
- ◆ **EZ-KITs (Schematics, Getting started guides)**

- ◆ **Note: to download excessive amount of data (pdf files), you may use: www.freedownloadmanager.org**
 - **Chains downloads**
 - **GPL**
 - **Resume operation (for large VDSP++ 300MB+)**
 - **Speedcontrol**

2 Catalog on DVD

**EE-Notes
For every
Product**

The screenshot displays the Analog Devices Designers' Reference Manual website. The browser window title is "Analog Devices Designers' Reference Manual". The page header includes the Analog Devices logo and navigation tabs: "Products", "Cross Reference", "Sales & Distributors", "Master Indexes", and "Parametric Search". A search bar is visible on the right with the text "Product #".

The left sidebar shows a tree view of products, with "Embedded Processing and DSP" expanded to show "Blackfin Processor" and a list of specific models including ADSP-BF531.

The main content area is titled "Embedded Processing and DSP" and "Blackfin Processor". The specific product "ADSP-BF531" is highlighted. Below the product name are tabs for "Documents", "Description", and "Packaging & Pricing".

The "Product Documentation" section contains a list of links to various technical documents:

- [EE-185](#) Fast Floating-Point Arithmetic Emulation on the Blackfin Processor Platform
- [EE-186](#) Extended-Precision Fixed-Point Arithmetic on the Blackfin Processor Platform
- [EE-192](#) Using C To Create Interrupt-Driven Systems On Blackfin Processors
- [EE-197](#) ADSP-BF531/532/533 Blackfin Processor Multi-cycle Instructions and Latencies
- [EE-210](#) SDRAM Selection and Configuration Guidelines for ADI Processors
- [EE-213](#) Host Communication via the Asynchronous Memory Interface for Blackfin Processors
- [EE-214](#) Ethernet Network Interface for ADSP-BF535 Blackfin Processors
- [EE-229](#) Estimating Power for ADSP-BF531/BF532/BF533 Blackfin Processors (Rev 2, 09/2006)
- [EE-234](#) Interfacing T1/E1 Transceivers/Framers to Blackfin Processors via the Serial Port
- [EE-236](#) Real-Time Solutions Using Mixed-Signal Front-End Devices with the Blackfin Processor
- [EE-237](#) Guide to Blackfin Processor LDF Files
- [EE-257](#) A Boot Compression/Decompression Algorithm for Blackfin Processors
- [EE-258](#) Interfacing Micron MT9V022 Image Sensors to Blackfin Processors (Rev 2, 06/2006)
- [EE-269](#) A Beginner's Guide to Ethernet 802.3 (Rev 1, 06/2004)
- [EE-271](#) Using Cache Memory on Blackfin Processors (Rev 1, 06/2005)
- [EE-276](#) Video Framework Considerations for Image Processing on Blackfin Processors (Rev 1, 09/2005)
- [EE-281](#) Hardware Design Checklist for the Blackfin Processors (Rev 1, 10/2005)
- [EE-288](#) USB OTG Interface for ADSP-BF533 Blackfin Processors (Rev 1, 03/2006)
- [EE-289](#) Implementing FAT32 File Systems on ADSP-BF533 Blackfin Processors (Rev 1, 02/2006)
- [EE-300](#) Interfacing Blackfin EZ-KIT Lite Boards to CMOS Image Sensors (Rev 1, 11/2006)

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3 VDSP++ Installation – built in Help/Manuals

VisualDSP++ 5.0 Help for Blackfin Processors

Hide Locate Back Forward Home First Options

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 - Printing Online Manuals
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Assembler Operators

Table 1-17 lists the assembler's numeric and bitwise operators used in constant expressions and address expressions. These operators are listed in group order from highest precedence to lowest precedence. Operators with the highest precedence are evaluated first. When two operators have the same precedence, the assembler evaluates the left-most operator first. Relational operators are supported only in relational expressions in conditional assembly, as described in "Conditional Assembly Directives".

Table 1-12. Operator Precedence

| Operator | Usage Description | Designation | Processors |
|--------------|---|-------------|----------------------|
| (expression) | expression in parentheses evaluates first | Parentheses | All |
| ~ | Ones complement | Tilde | All |
| - | Unary minus | Minus | All |
| * | Multiply | Asterisk | All |
| / | Divide | Slash | All |
| % | Modulus | Percentage | All |
| + | Addition | Plus | All |
| - | Subtraction | Minus | All |
| << | Shift left | | All |
| >> | Shift right | | All |
| & | Bitwise AND | | All |
| | Bitwise inclusive OR | | All |
| ^ | Bitwise exclusive OR | | TigerSHARC and SHARC |
| && | Logical AND | | TigerSHARC only |
| | Logical OR | | TigerSHARC only |

The assembler also supports special operators. Table 1-13 lists and describes these operators used in constant and address expressions.

Table 1-13. Special Assembler Operators

| Operator | Usage Description |
|--|--|
| ADDRESS(symbol) | Address of <i>symbol</i>
Note: Used with SHARC and TigerSHARC assemblers only. |
| BITPOS(<i>constant</i>) | Bit position (Blackfin processors ONLY) |
| HI(<i>expression</i>)
LO(<i>expression</i>) | Extracts the most significant 16 bits of expression.
Extracts the least significant 16 bits of expression.
Note: Used with the Blackfin assembler ONLY where HI/LO replaces the ADDRESS() operator. The expression in the HI and LO operators can be either symbolic or constant. |
| LENGTH(symbol) | Length of <i>symbol</i> in number of elements (in a buffer/array) |
| symbol | Address pointer to <i>symbol</i> |