

V_{DSS}	650V
$R_{DS(on)}$ (Typ.)	30m Ω
I_D^{*1}	70A
P_D	267W

●Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

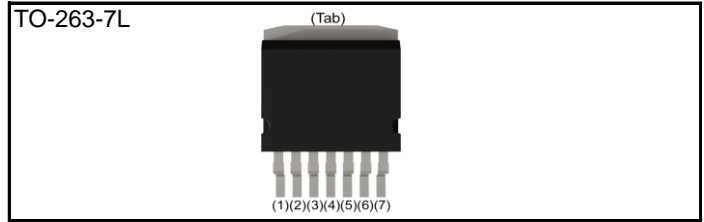
●Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

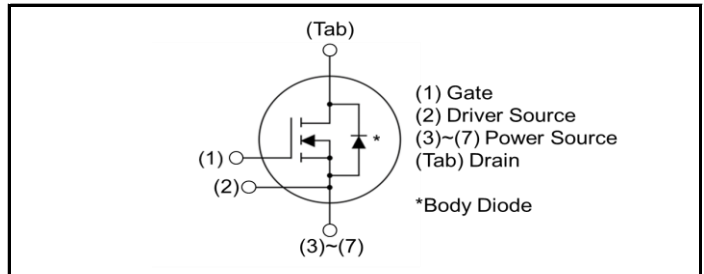
●Absolute maximum ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	V_{DSS}	650	V
Continuous Drain current	$T_c = 25^\circ\text{C}$	I_D^{*1}	70
	$T_c = 100^\circ\text{C}$	I_D^{*1}	50
Pulsed Drain current	$I_{D,pulse}^{*2}$	175	A
Gate - Source voltage (DC)	V_{GSS}	-4 to +22	V
Gate - Source surge voltage ($t_{surge} < 300\text{ns}$)	$V_{GSS,surge}^{*3}$	-4 to +26	V
Recommended drive voltage	$V_{GS,op}^{*4}$	0 / +18	V
Junction temperature	T_j	175	$^\circ\text{C}$
Range of storage temperature	T_{stg}	-55 to +175	$^\circ\text{C}$

●Outline



●Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

●Packaging specifications

Type	Packing	Embossed tape
	Reel size (mm)	330
	Tape width (mm)	24
	Basic ordering unit (pcs)	1000
	Taping code	TL
	Marking	SCT3030AW7

●Electrical characteristics (T_a = 25°C)

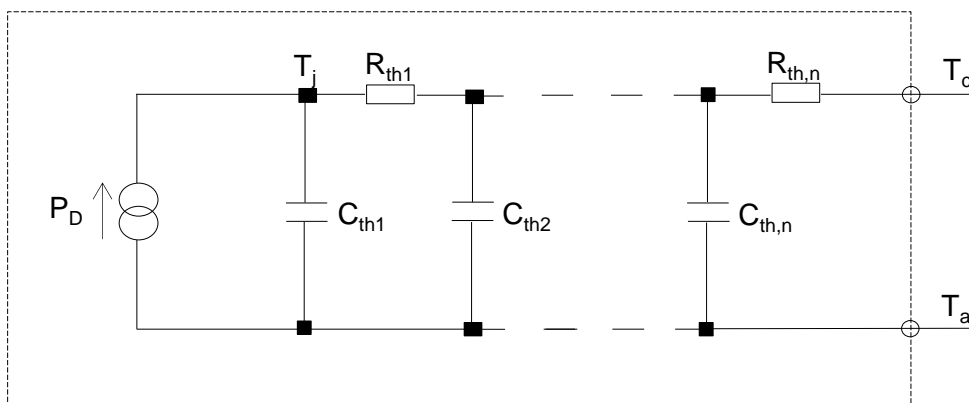
Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA T _j = 25°C T _j = -55°C	650 650	- -	- -	V
Zero Gate voltage Drain current	I _{DSS}	V _{GS} = 0V, V _{DS} = 650V T _j = 25°C T _j = 150°C	- -	1 2	10 -	μA
Gate - Source leakage current	I _{GSS+}	V _{GS} = +22V, V _{DS} = 0V	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	V _{GS} = -4V, V _{DS} = 0V	-	-	-100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 13.3mA	2.7	-	5.6	V
Static Drain - Source on - state resistance	R _{DS(on)} ^{*5}	V _{GS} = 18V, I _D = 27A T _j = 25°C T _j = 150°C	- -	30 43	39 -	mΩ
Gate input resistance	R _G	f = 1MHz, open drain	-	7	-	Ω

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case ^{*6}	R _{thJC}	-	0.44	0.56	°C/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	4.06×10 ⁻²	K/W	C _{th1}	7.06×10 ⁻³	Ws/K
R _{th2}	6.86×10 ⁻²		C _{th2}	2.59×10 ⁻²	
R _{th3}	3.31×10 ⁻¹		C _{th3}	2.77×10 ⁻²	



●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Transconductance	g_{fs}^{*5}	$V_{DS} = 10V, I_D = 27A$	-	9.4	-	S
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	1526	-	pF
Output capacitance	C_{oss}	$V_{DS} = 500V$	-	89	-	
Reverse transfer capacitance	C_{rss}	$f = 1MHz$	-	42	-	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 300V$	-	230	-	pF
Total Gate charge	Q_g^{*5}	$V_{DS} = 300V$ $I_D = 27A$	-	104	-	nC
Gate - Source charge	Q_{gs}^{*5}	$V_{GS} = 18V$	-	19	-	
Gate - Drain charge	Q_{gd}^{*5}	See Fig. 1-1.	-	55	-	
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DS} = 400V$ $I_D = 27A$	-	7	-	ns
Rise time	t_r^{*5}	$V_{GS} = 0V/+18V$	-	22	-	
Turn - off delay time	$t_{d(off)}^{*5}$	$R_G = 0\Omega, L = 750\mu H$ $L_\sigma = 50nH, C_\sigma = 10pF$	-	27	-	
Fall time	t_f^{*5}	See Fig. 2-1, 2-2, 2-3.	-	21	-	
Turn - on switching loss	E_{on}^{*5}	E_{on} includes diode reverse recovery.	-	159	-	μJ
Turn - off switching loss	E_{off}^{*5}		-	87	-	

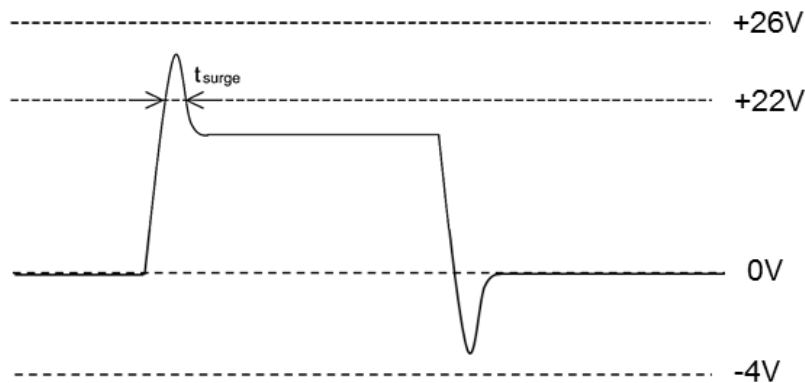
●Body diode electrical characteristics (Source-Drain) ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Body diode continuous, forward current	I_S *1	$T_c = 25^\circ\text{C}$	-	-	70	A
Body diode direct current, pulsed	I_{SM} *2		-	-	175	A
Forward voltage	V_{SD} *5	$V_{GS} = 0\text{V}, I_D = 27\text{A}$	-	3.2	-	V
Reverse recovery time	t_{rr} *5	$I_F = 27\text{A}$ $V_R = 400\text{V}$ $di/dt = 2500\text{A}/\mu\text{s}$	-	28	-	ns
Reverse recovery charge	Q_{rr} *5		-	702	-	nC
Peak reverse recovery current	I_{rrm} *5	$L_\sigma = 50\text{nH}, C_\sigma = 10\text{pF}$ See Fig. 3-1, 3-2.	-	40	-	A

*1 Limited by maximum temperature allowed.

*2 $P_W \leq 10\mu\text{s}$, Duty cycle $\leq 1\%$

*3 Example of acceptable V_{GS} waveform



Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

*4 Please be advised not to use SiC-MOSFETs with V_{GS} below 13V as doing so may cause thermal runaway.

*5 Pulsed

*6 The case mentioned in this parameter is the bottom of leadframe located underneath the chip.

Actual value of the $R_{th(j-c)}$ is influenced by user's application design. The described value is only valid at the specific conditions such as JESD51-14.

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

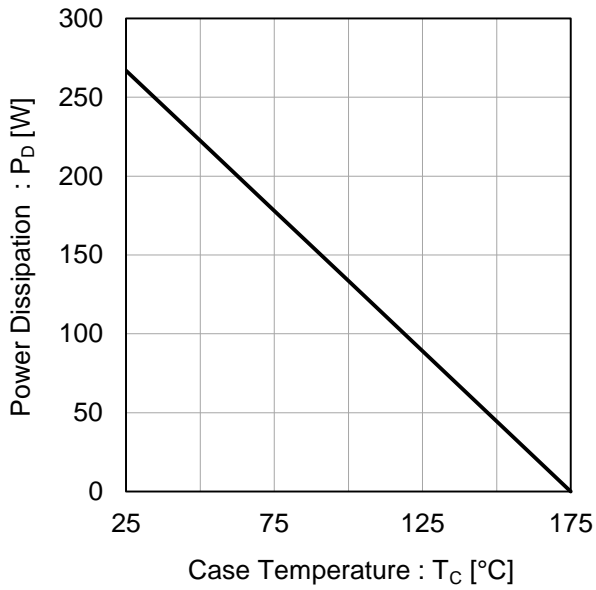


Fig.2 Maximum Safe Operating Area

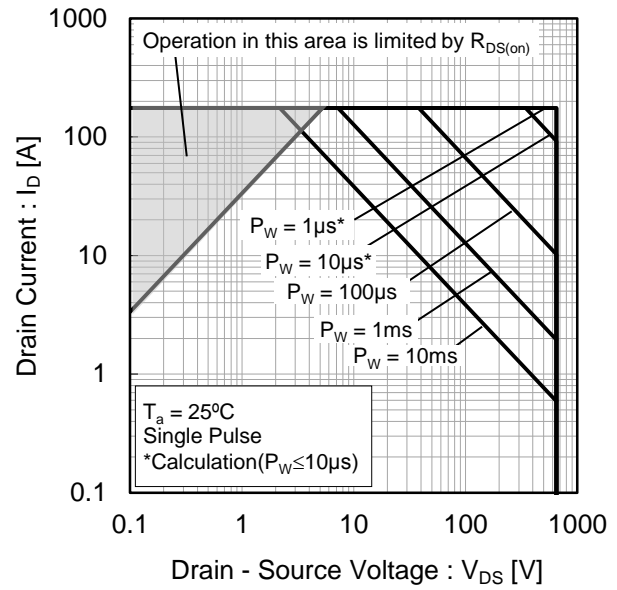
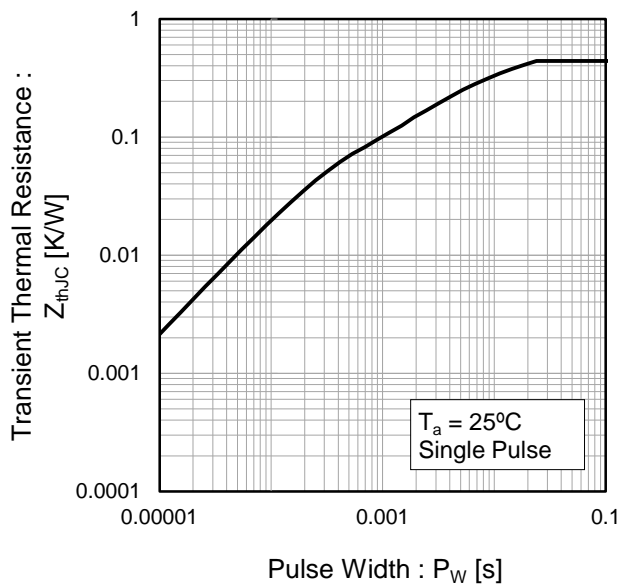


Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



●Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)

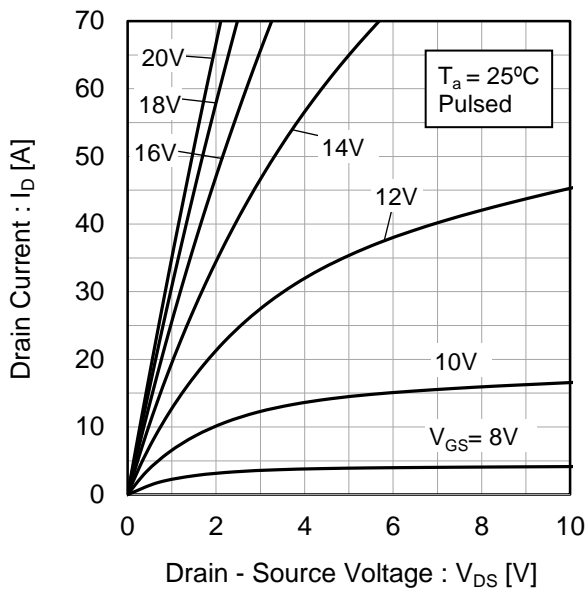


Fig.5 Typical Output Characteristics(II)

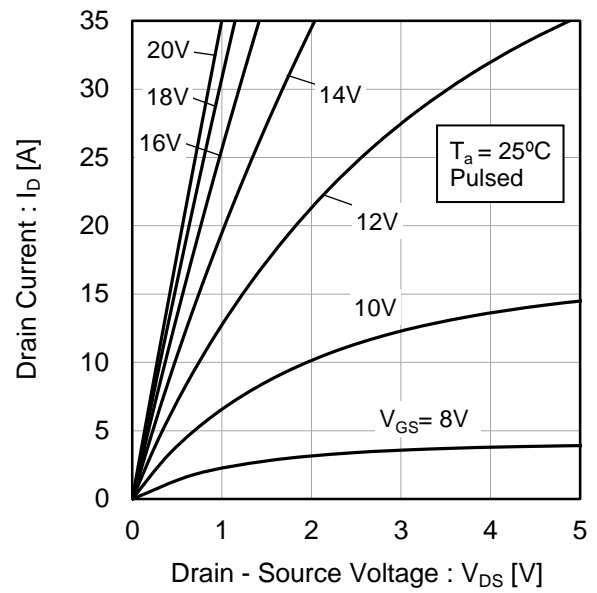
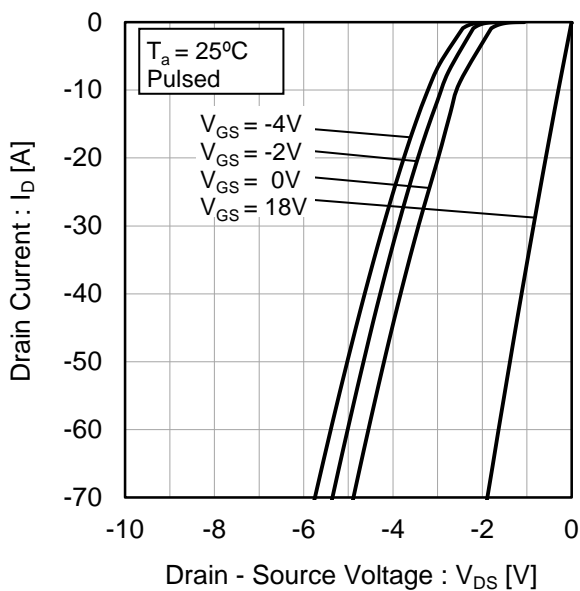


Fig.6 $T_j = 25^\circ\text{C}$ 3rd Quadrant Characteristics



●Electrical characteristic curves

Fig.7 $T_j = 150^\circ\text{C}$ Typical Output Characteristics(I)

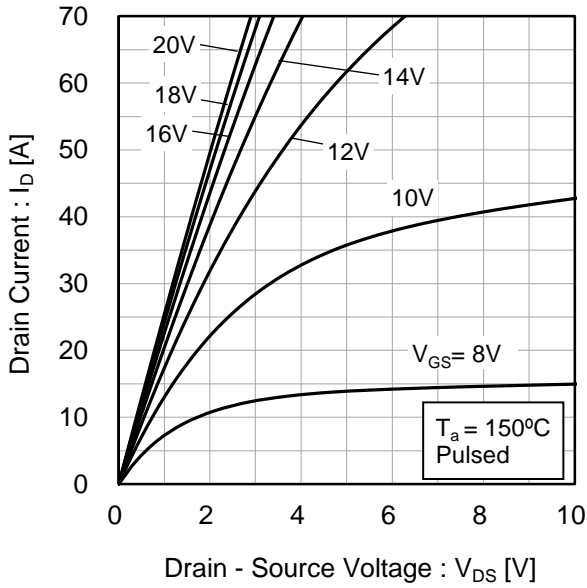


Fig.8 $T_j = 150^\circ\text{C}$ Typical Output Characteristics(II)

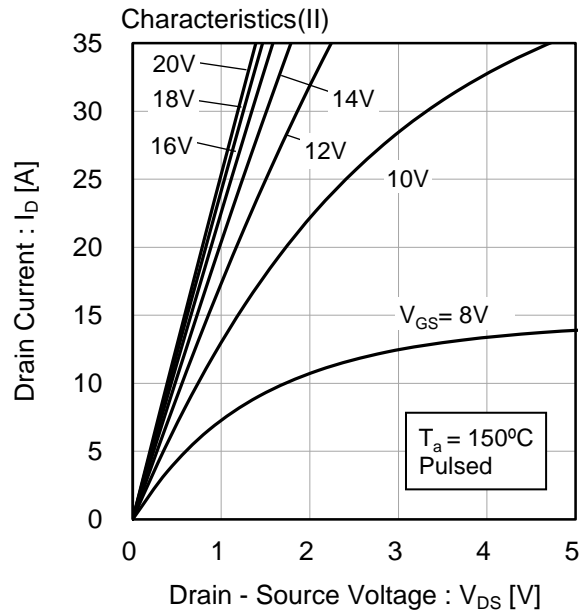


Fig.9 $T_j = 150^\circ\text{C}$ 3rd Quadrant Characteristics

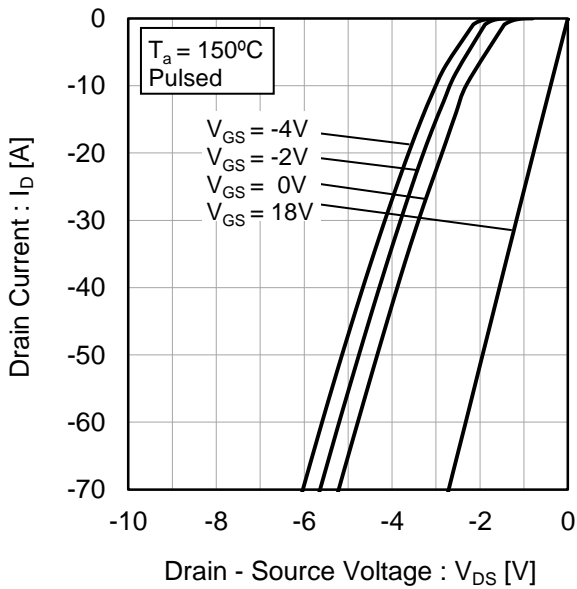
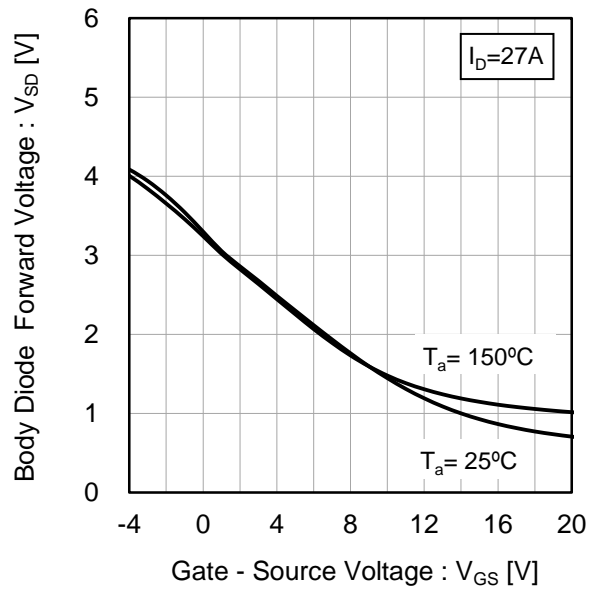


Fig.10 Body Diode Forward Voltage vs. Gate - Source Voltage



●Electrical characteristic curves

Fig.11 Typical Transfer Characteristics (I)

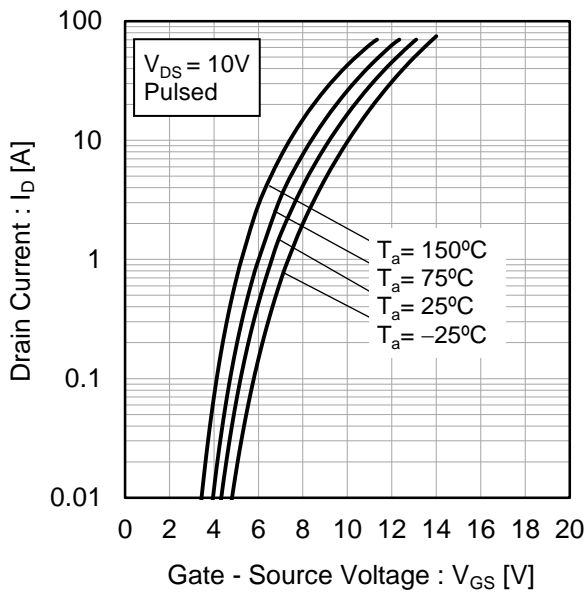


Fig.12 Typical Transfer Characteristics (II)

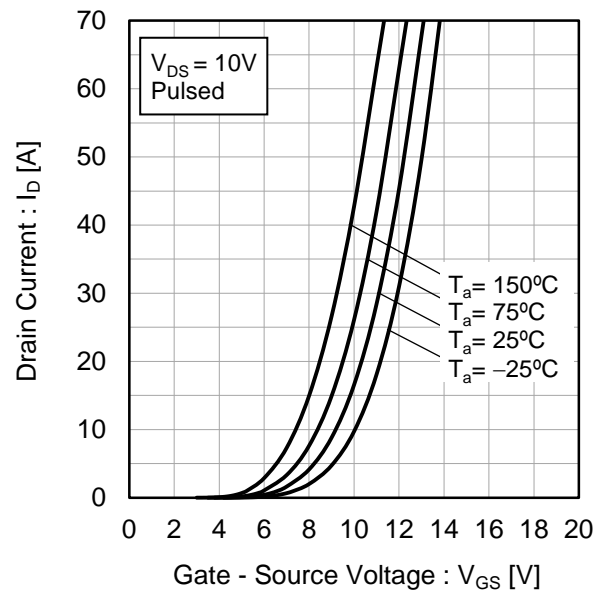


Fig.13 Gate Threshold Voltage vs. Junction Temperature

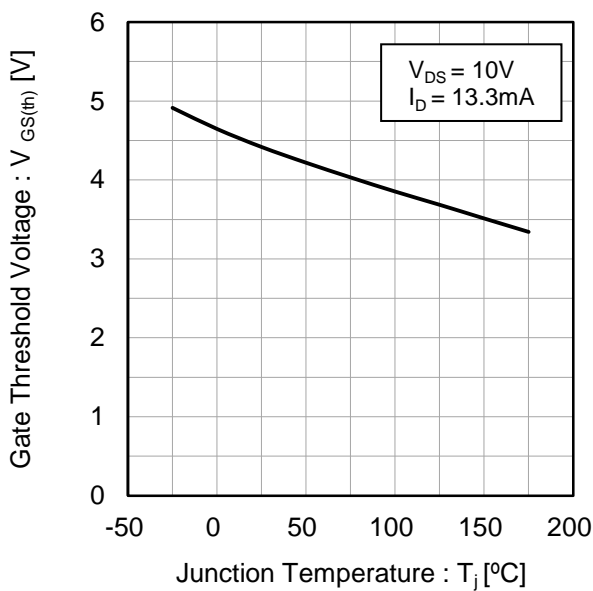
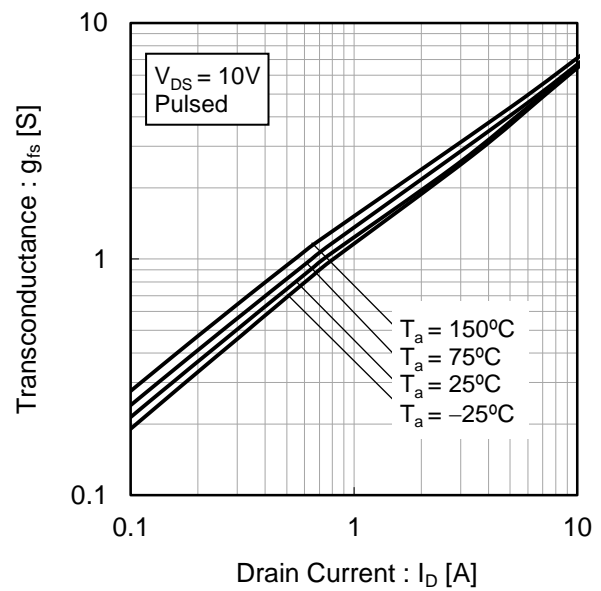


Fig.14 Transconductance vs. Drain Current



●Electrical characteristic curves

Fig.15 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

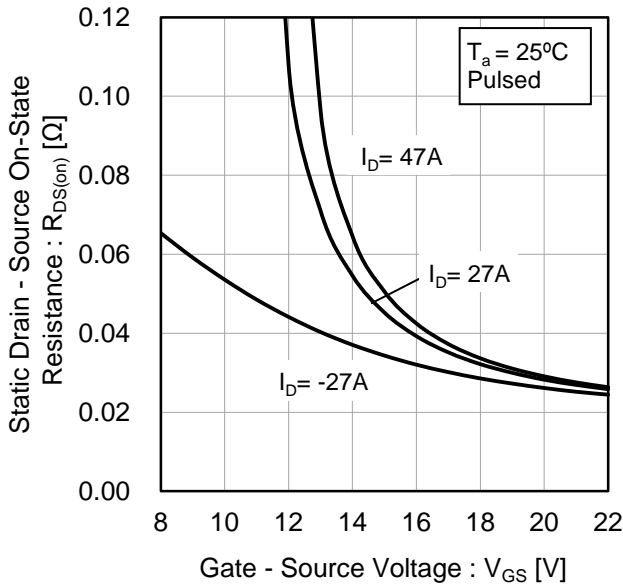


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature

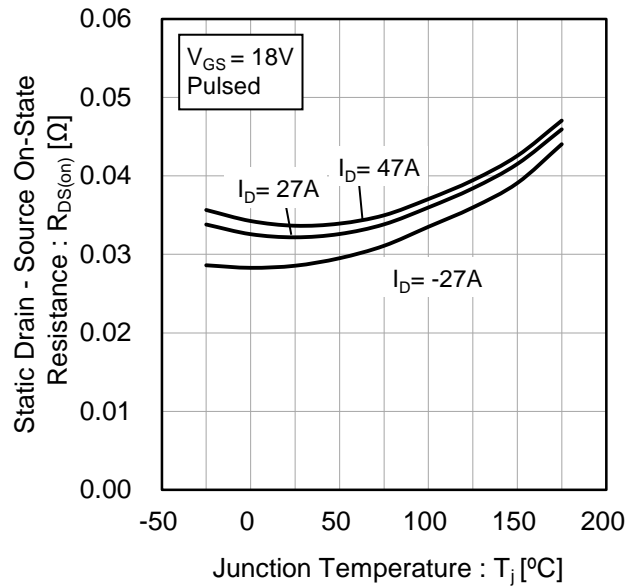


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

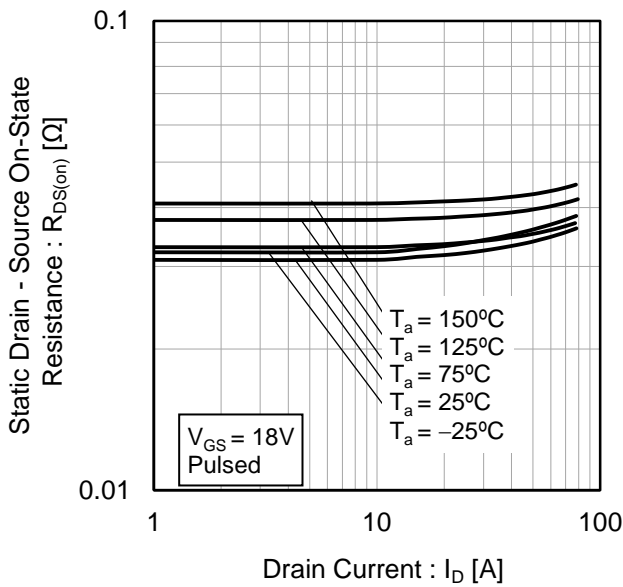
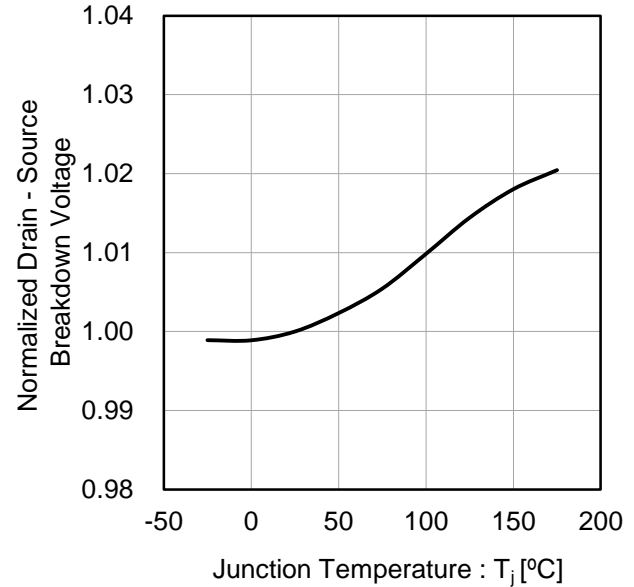


Fig.18 Normalized Drain - Source Breakdown Voltage vs. Junction Temperature



●Electrical characteristic curves

Fig.19 Typical Capacitance vs. Drain - Source Voltage

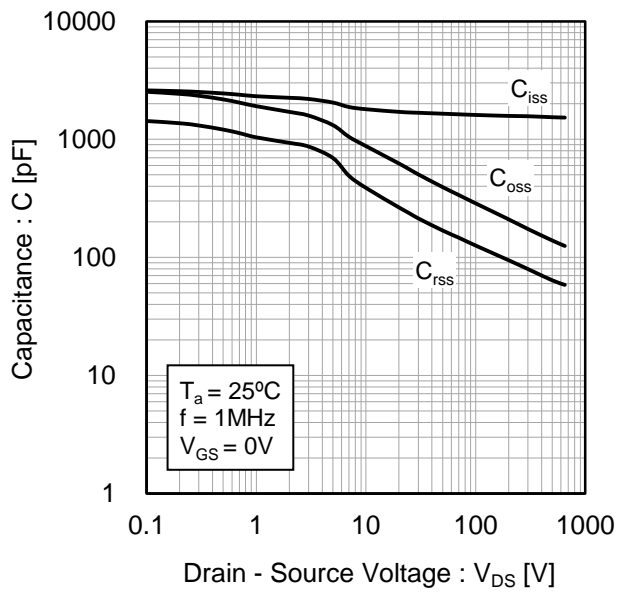


Fig.20 C_{oss} Stored Energy

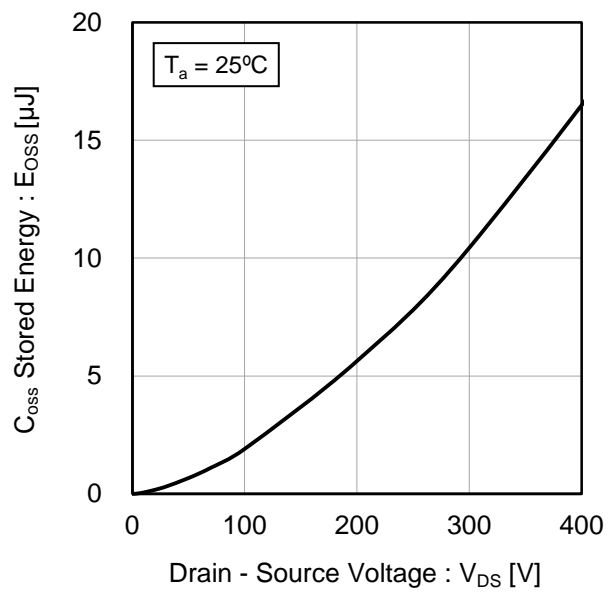
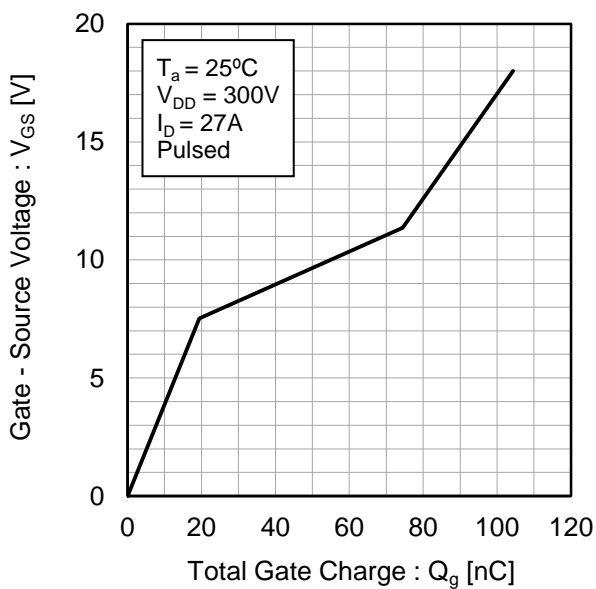
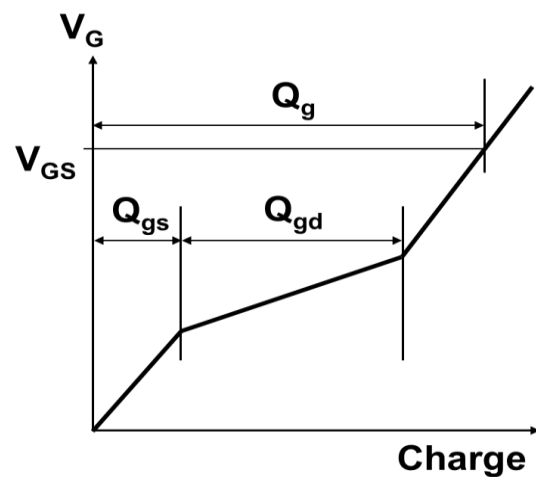


Fig.21 Dynamic Input Characteristics



*Gate Charge Waveform



●Electrical characteristic curves

Fig.22 Typical Switching Time vs. External Gate Resistance

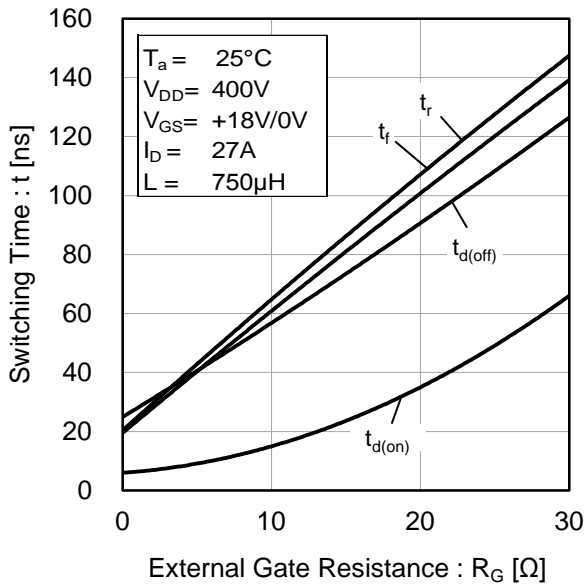


Fig.23 Typical Switching Loss vs. Drain - Source Voltage

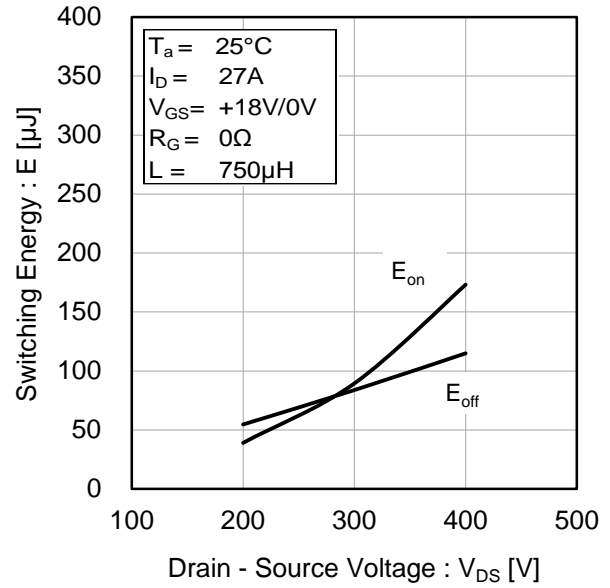


Fig.24 Typical Switching Loss vs. Drain Current

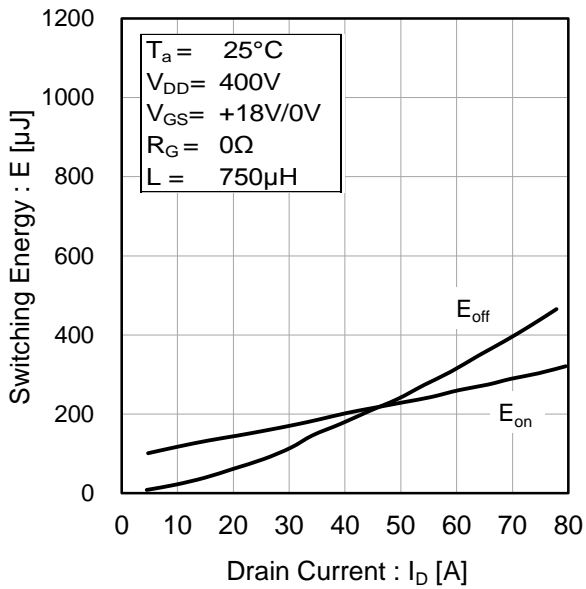
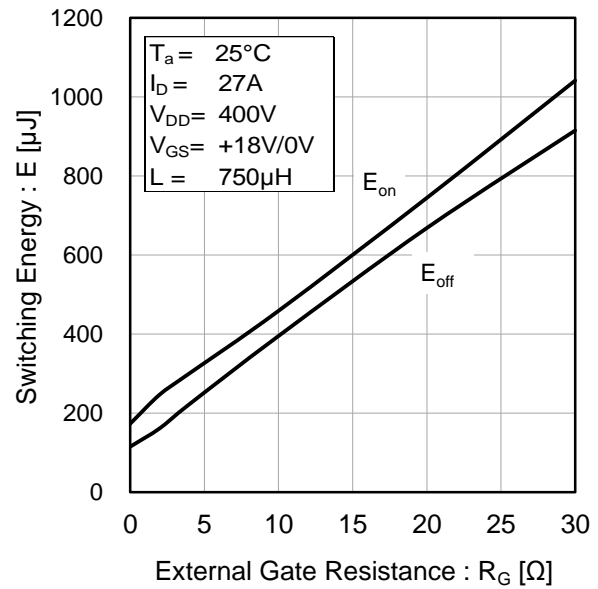


Fig.25 Typical Switching Loss vs. External Gate Resistance



● Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

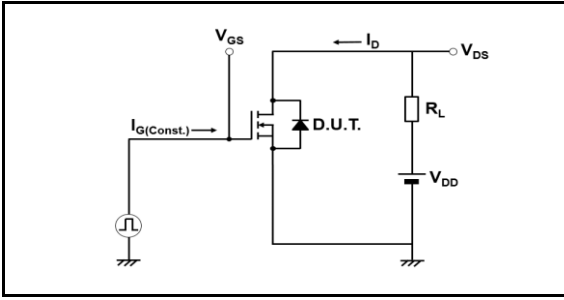


Fig.2-1 Switching Characteristics Measurement Circuit

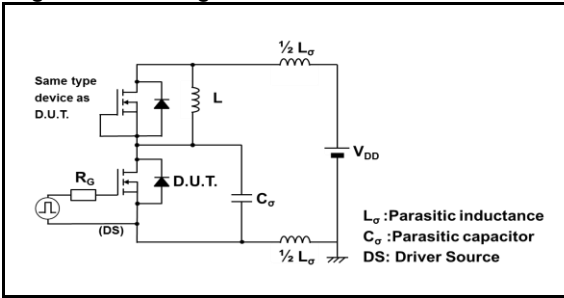


Fig.2-2 Waveforms for Switching Time

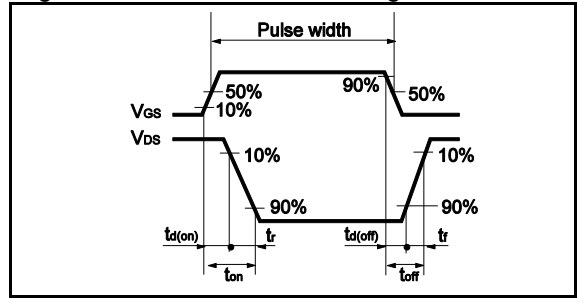


Fig.2-3 Waveforms for Switching Energy Loss

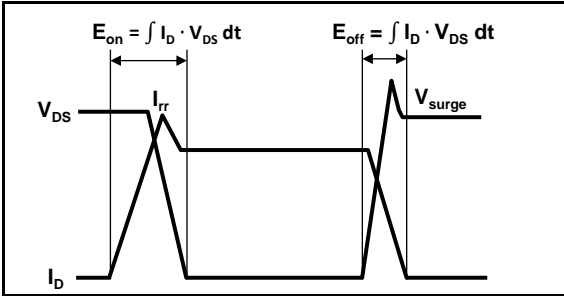


Fig.3-1 Reverse Recovery Time Measurement Circuit

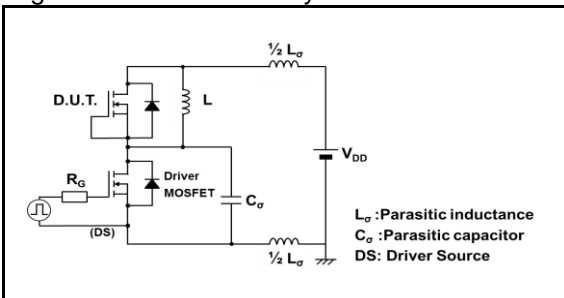
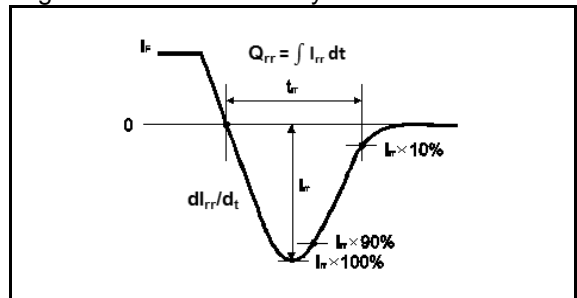


Fig.3-2 Reverse Recovery Waveform



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