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# MINI EVALUATION BOARD USER MANUAL

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## FOR RADIO MODULES

EV order code	Module order code	Product Name
2611069024001	2611011024000	Proteus-III
2611119024011	2611011024010	Proteus-III-SPI
2611079021001	2611011021000	Thyone-I
2611129024021	2611011024020	Setebos-I

VERSION 1.3

JUNE 29, 2021

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## **MUST READ**

### **Check for firmware updates**

Before using the product make sure you use the most recent firmware version, data sheet and user manual. This is especially important for Wireless Connectivity products that were not purchased directly from Würth Elektronik eiSos. A firmware update on these respective products may be required.

We strongly recommend to include in the customer system design, the possibility for a firmware update of the product.

## Revision history

Manual version	HW version	Notes	Date
1.0	1.0	<ul style="list-style-type: none"> <li>Initial version</li> </ul>	October 2020
1.1	1.0	<ul style="list-style-type: none"> <li>Order number is updated in the schematic 3.7.</li> <li>Bill of materials is added in chapter 3.9.</li> <li>LFXO crystal part number is updated in chapter 3.6.5.</li> <li>Input capacitance variables <math>C_{XC1}</math> and <math>C_{XC2}</math> are changed to <math>C_{XL1}</math> and <math>C_{XL2}</math> respectively 3.6.5.</li> </ul>	January 2021
1.2	1.0	<ul style="list-style-type: none"> <li>Added information on Proteus-III-SPI radio module</li> <li>Chapter LFXO crystal is updated.</li> </ul>	February 2021
1.3	1.0	<ul style="list-style-type: none"> <li>Added information on Setebos-I radio module</li> </ul>	June 2021

## Abbreviations and abstract

Abbreviation	Name	Description
Bluetooth LE	Bluetooth Low Energy	
COM port	Communication port	
EV	Evaluation	
ESD	Electro Static Discharge	
FTDI	Future Technology Devices International	
GND	Ground	
HIGH	High signal level	
IO	Input & Output	
JTAG	Joint Test Action Group	
LED	Light Emitting Diode	
LFCLK	Low frequency clock	
LFXO	Low frequency crystal oscillator	
LOW	Low signal level	
NFC	Near Field Communication	
OTA	Over the air	
PC	Personal Computer	
PCB	Printed Circuit Board	
RF	Radio frequency	Describes everything relating to the wireless transmission.
RPS	Radio Protocol Selection	Pin of the Setebos-I module for selection of radio protocol (Proprietary or Bluetooth Low Energy)
SMA	SubMiniature version A	
SPI	Serial Peripheral Interface	
SWD	Serial Wire Debug	
THT	Through-hole technology	
UART	Universal Asynchronous Receiver Transmitter	Universal Asynchronous Receiver Transmitter allows communicating with the module of a specific interface.
USB	Universal Serial Bus	
VDD	Voltage Drain Drain	Supply voltage

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# 1 Supported radio modules

The evaluation board described in this manual can be used to evaluate the following products:

Order code	Product Name	Description
2611011024000	Proteus-III	Bluetooth® LE 5.1 radio module with smart antenna configuration
2611011024000	Proteus-III-SPI	Variant of the Proteus-III radio module providing a SPI instead of an UART for host connection
2611011021000	Thyone-I	2.4 GHz proprietary radio module with smart antenna configuration
2611011024020	Setebos-I	2.4 GHz combo module Bluetooth® LE & proprietary with smart antenna configuration

Order code	Product Name
2611069024001	Mini EV Board Proteus-III
2611119024011	Mini EV Board Proteus-III-SPI
2611079021001	Mini EV Board Thyone-I
2611129024021	Mini EV Board Setebos-I

Table 1: Compatibility

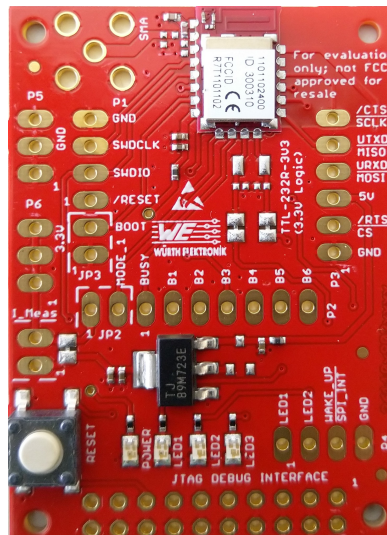


Figure 1: Product image

Content 2611069024001	Quantity
Mini Evaluation board with Proteus-III On-board PCB Antenna connection	1
Packaging: ESD safe cover	1

Table 2: Content Mini EV Board Proteus-III

Content 2611119024011	Quantity
Mini Evaluation board with Proteus-III-SPI On-board PCB Antenna connection	1
Packaging: ESD safe cover	1

Table 3: Content Mini EV Board Proteus-III-SPI

Content 2611079021001	Quantity
Mini Evaluation board with Thyone-I On-board PCB Antenna connection	1
Packaging: ESD safe cover	1

Table 4: Content Mini EV Board Thyone-I

Content 2611129024021	Quantity
Mini Evaluation board with Setebos-I On-board PCB Antenna connection	1
Packaging: ESD safe cover	1

Table 5: Content Mini EV Board Setebos-I



## 2 Functional description

The mini evaluation board is an intuitive, application oriented and cost effective version of the evaluation kit. It offers the user the possibility to develop hard- and software for the corresponding radio module.

By default, the mini evaluation board is not assembled with connectors and pin headers. The necessary components shall be assembled by the user based on the need and application.

The mini evaluation board is intended for experienced developers, as additional accessories and basic soldering skills are necessary to take the board into operation.

Accessories required:

- Additional assembly components listed in the table 6.  
Connector kit article number: 699100.
- Soldering equipment
- (optional) TTL-232R-3V3 FTDI cable [1] in case of Proteus-III, Thyone-I or Setebos-I

For the connection to a micro controller or PC the evaluation board is equipped with placeholders for standard 2.54mm headers which is connected to the pins of the radio module. Pin headers can be soldered to the placeholders to access the module pins. Jumpers allow to set the module into defined modes.

In case of Proteus-III, Thyone-I and Setebos-I, the mini evaluation board can be connected to an USB port of a PC using TTL-232R-3V3 FTDI cable.

### 2.1 Taking into operation - PC (Proteus-III/Thyone-I/Setebos-I only)

To take the mini evaluation board into operation using a PC, additional assembly as described in the chapter 3.2 is necessary.

The corresponding FTDI driver package ([www.ftdichip.com/Drivers/VCP.htm](http://www.ftdichip.com/Drivers/VCP.htm)) has to be installed on the PC.

The installation guidelines shall be followed:

(<https://www.ftdichip.com/Support/Documents/InstallGuides.htm>)

The TTL-232R-3V3 FTDI cable [1] shall be connected to the connector P3 of the evaluation board. It is important that the VDD is stable and able to reliably supply the module's static and peak current consumption as specified by the module manual.

For Setebos-I, the pin B1 on connector P2 assumes the function RPS (Radio Protocol Selection): the logic level on this pin shall be used to determine whether the module should boot the Proprietary firmware (and work as a Thyone-I module) or the Bluetooth Low Energy 5.1 firmware (and work as a Proteus-III module). Please refer to chapter 3.6.7 for further details.



Incorrect orientation of TTL-232R-3V3 FTDI cable will damage the RF module.

The next step is to connect the evaluation board to the PC using the TTL-232R-3V3 FTDI cable. In that way a COM port can be detected and installed on the PC. In the device manager the COM port name of the TTL-232R-3V3 FTDI cable can be found. A COM port shall appear for example: "COM12" in Windows systems or "/dev/ttyUSB0" in Linux systems.

The WE-SmartCommander [4] or any other serial terminal program (like hterm for Windows) has to be run and the corresponding COM port has to be opened using the default settings of the mounted radio module.



When taking the Setebos-I module into operation with the WE-SmartCommander, either the Proteus-III or the Thyone-I module has to be selected on the graphical interface, depending on the mode defined through the logic level of the RPS pin.

After the module is powered through the TTL-232R-3V3 FTDI cable or an alternative power supply, the reset button should be pressed to ensure a clean start-up of the module.

The detailed module specific quick start instructions can be found in the corresponding user manuals [3] [2] [5].

## 2.2 Taking into operation - Host controller

To take the mini evaluation board into operation using a host controller, additional assembly as described in the chapter 3.2 is necessary.

An external power supply shall be connected to the evaluation board. The power supply option 3 or 4 from the table 18 shall be used. It is important that the VDD is stable and able to reliably supply the module's static and peak current consumption as specified by the module manual.

The next step is to connect the communication lines of the module to the host controller. The headers P1, P2 and P3 can be used to connect the module pins and host.

For Setebos-I, the pin B1 on connector P2 assumes the function RPS (Radio Protocol Selection): the logic level on this pin shall be used to determine whether the module should boot the Proprietary firmware (and work as a Thyone-I module) or the Bluetooth Low Energy 5.1 firmware (and work as a Proteus-III module). Please refer to chapter 3.6.7 for further details.

The detailed module specific quick start instructions can be found in the corresponding user manuals [3] [2] [5].

Feel free to check our youtube channel for video tutorials, hands-ons and webinars relating to our products:

[www.youtube.com/user/WuerthElektronik/videos](http://www.youtube.com/user/WuerthElektronik/videos)

## 3 Development board

### 3.1 Block diagram

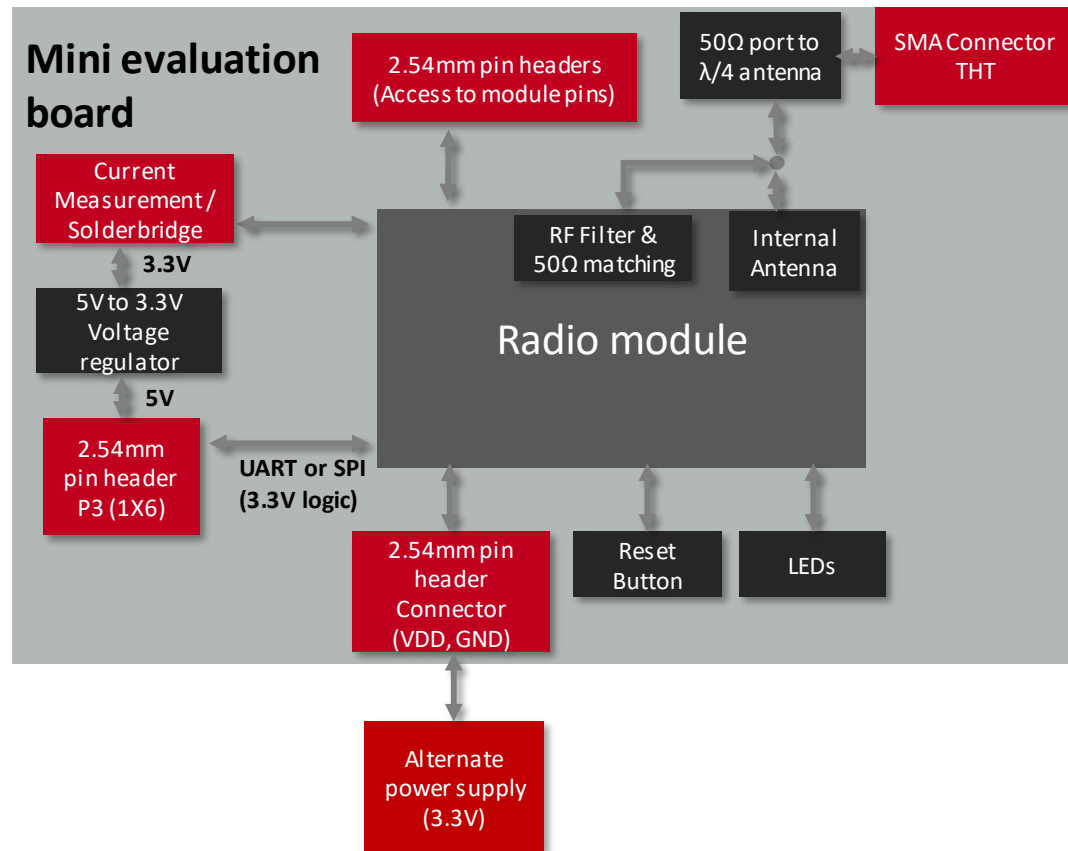


Figure 2: Block diagram

### 3.2 Additional assembly

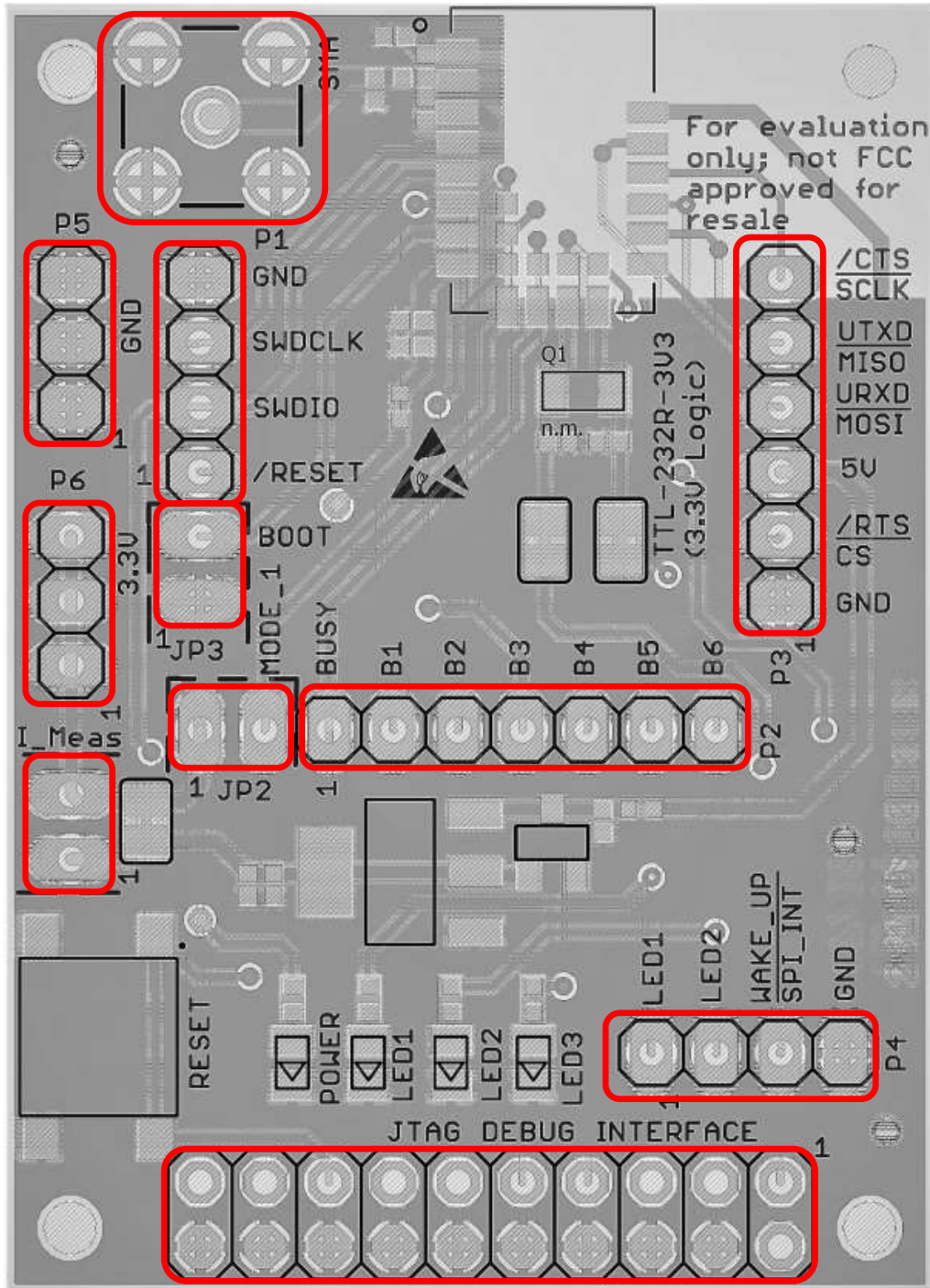


Figure 3: Additional assembly

The table 6 lists the additional assembly components for the evaluation board. All the components listed in the table 6 are THT components.

Placeholder	Function	Pins	WE Article Number
I_Meas	Current measurement	1X2	61300211121
JP2	Boot mode set	1X2	61300211121
JP3	Operation mode set	1X2	61300211121
JTAG	JTAG Debug interface	2X10	61302021121
P1	Access to module pins	1X6	61300611121
P2	Access to module pins	1X7	61300711121
P3	TTL-232R-3V3 FTDI cable connection / Access to module pins	1X6	61300411121
P4	Access to module pins	1X4	61300411121
P5	Ground connection	1X3	61300311121
P6	3.3V Connection	1X3	61300311121
SMA	External antenna connection	1X4	60312002114503
2.54mm Jumpers	I_Meas, JP2, JP3	1X3	60900213421

Table 6: Additional assembly components

Connector kit containing all the components listed in the table 6 is available.  
Connector kit article number: 699100.



Based on the necessity the required components shall be assembled.



Holes with 2mm diameter on all the four corners are available for Spacer or Standoff connections

### 3.3 Connectors and ports

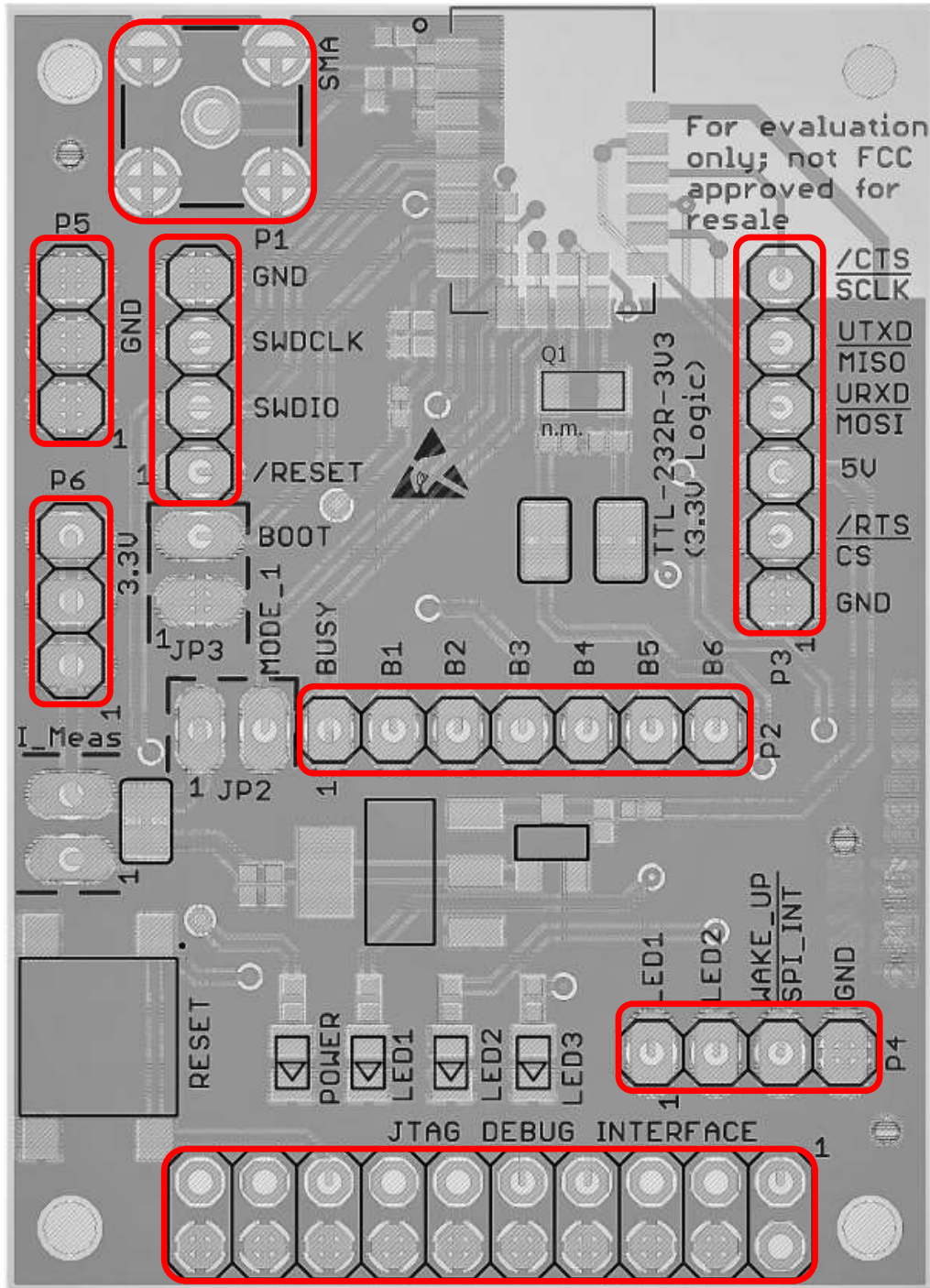


Figure 4: Connectors

### 3.3.1 P1, P2, P4: Module access pins

P1	nRF52840	Function
1	P0.18	RESET
2	SWDIO	SWDIO
3	SWCLK	SWCLK
4		GND

Table 7: Pin header P1

P2	nRF52840	Function
1	P0.22	BUSY
2	P0.09	B1, RSVD (Proteus-III / Thyone-I) RPS (Radio Protocol Select, Setebos-I)
3	P0.10	B2, RSVD
4	P0.23	B3, RSVD
5	P1.00	B4, RSVD
6	P0.21	B5, RSVD
7	P0.07	B6, RSVD

Table 8: Pin header P2

P4	nRF52840	Function
1	P0.00/XL1	LED1
2	P0.01/XL2	LED2
3	P0.03	WAKE_UP / SPI_INT
4		GND

Table 9: Pin header P4



In case of Setebos-I, the pin B1/RPS on pin header P2 is used to select the radio protocol (Proprietary of Bluetooth Low Energy 5.1)

### 3.3.2 P3: TTL-232R-3V3 FTDI cable connector

P3	nRF52840	Function
1		GND
2	P0.11	/RTS (SPI_CS)
3		5V
4	P1.09	URXD (SPI_MOSI)
5	P1.08	UTXD (SPI_MISO)
6	P0.12	/CTS (SPI_SCLK)

Table 10: Pin header P3



In case of Proteus-III, Thyone-I or Setebos-I, the pin header P3 is used to connect the TTL-232R-3V3 FTDI cable. P3 can also be used for direct host connection.

### 3.3.3 P5, P6: Alternative power supply connection

P5	Connection
1,2,3	GND

Table 11: Pin header P5

P6	Connection
1,2,3	3.3V power supply

Table 12: Pin header P6

All the information related to the power supply are described in the chapter 3.6.1.

### 3.3.4 JTAG Debugging Interface

JTAG	nRF52840	Function
1		VDD
7	SWDIO	SWDIO
9	SWCLK	SWCLK
15	P0.18	RESET
4,6,8,10,12,14,16,18,20		GND
2,3,5,11,13,17,19		Not Connected

Table 13: JTAG debugging interface



### 3.3.5 SMA

SMA connector is used to connect an external antenna.  
The 2.4 GHz antenna Himalia (2600130021) is a perfect match.

SMA	Connection
Inner	Module RF pin
Outer	4 x GND

Table 14: Pin header SMA



In order to use an external SMA antenna, 22 pF capacitor (0402) on position C1 shall be populated. C2, C8, C11 and C12 should be left unpopulated.



Optional: Experts have the possibility to use C11, C1 and C8 for additional filtering or fine tuning.

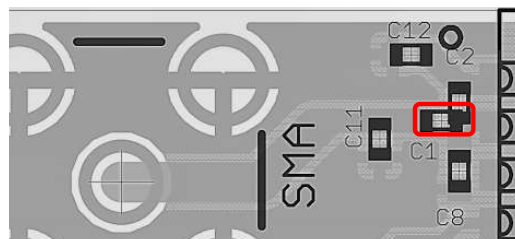


Figure 5: Capacitor connection to external antenna



In order to use the internal PCB antenna of the module, a 2.4 nH inductor (0402) on position C2 and a 0.3 pF capacitor (0402) on position C12 shall be populated respectively. C1, C8 and C11 should be left unpopulated.

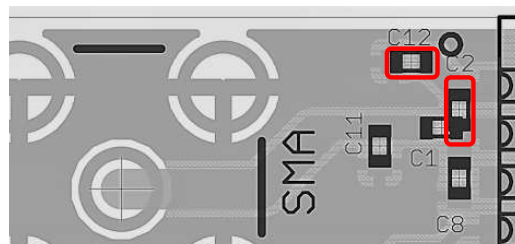


Figure 6: Capacitor connection to internal antenna

### 3.4 Jumpers

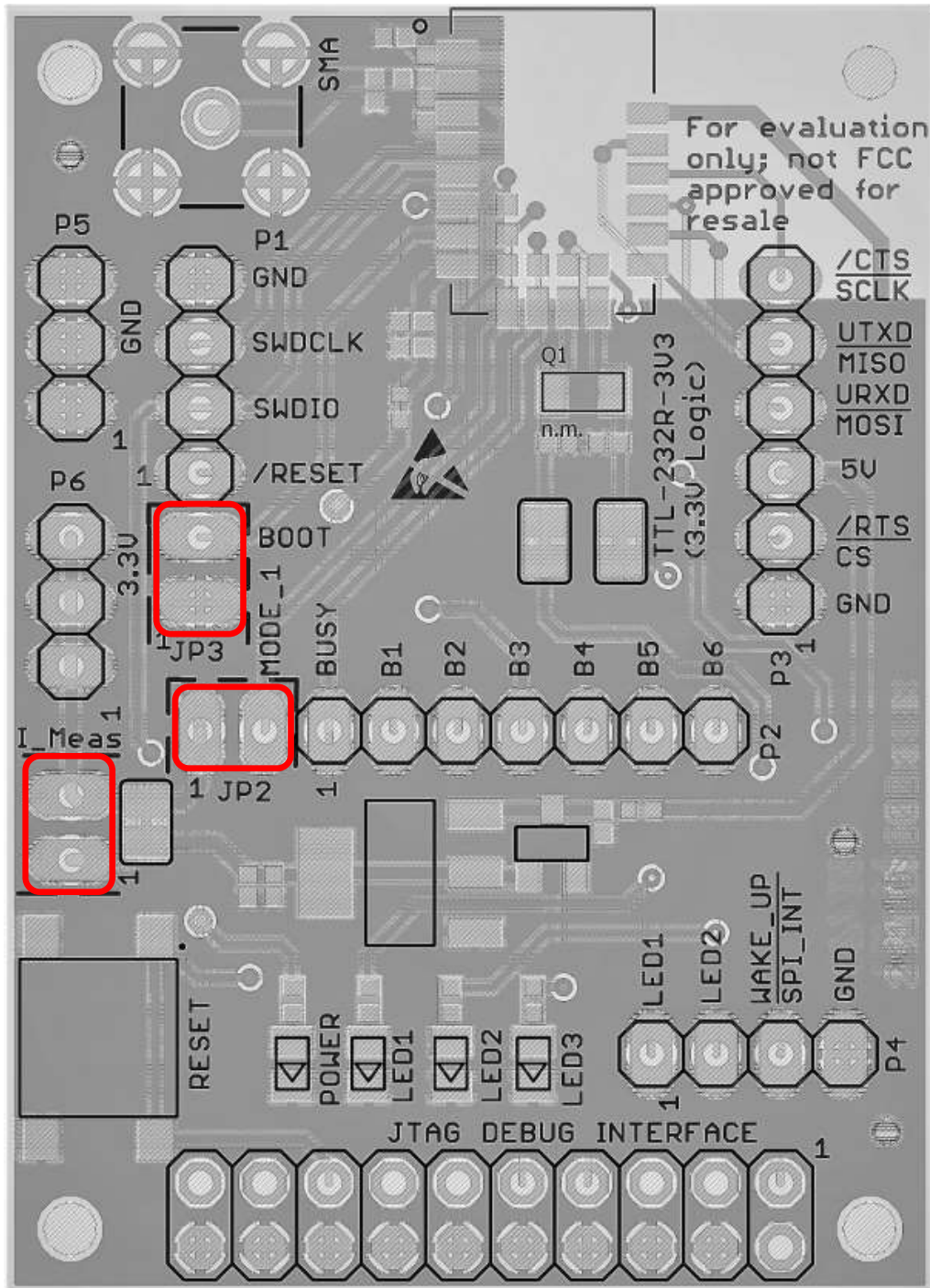


Figure 7: Jumpers

### 3.4.1 I\_Meas: Current measurement

By default, I\_Meas pin header is not assembled and solder bridge near the I\_Meas jumper SJ2 is connected. To measure module power consumption and to disconnect the power LED, the resistor R4 shall be desoldered.

The power LED is on the VDD line of the module and shows that the module is sourced. To measure module power consumption and to disconnect the power LED, the resistor R4 shall be desoldered.

If the module is sourced through the P3 connector, the current consumption of the module can be measured on the I\_Meas pin header. For this measurement, the solder bridge SJ2 shall be disconnected using a cutter and a current meter shall be connected to the I\_Meas pin header. For normal operation a jumper is set on the I\_Meas pin header.

I_Meas	Function
1	3.3V LDO Output
2	VDD

Table 15: Pin header I\_Meas

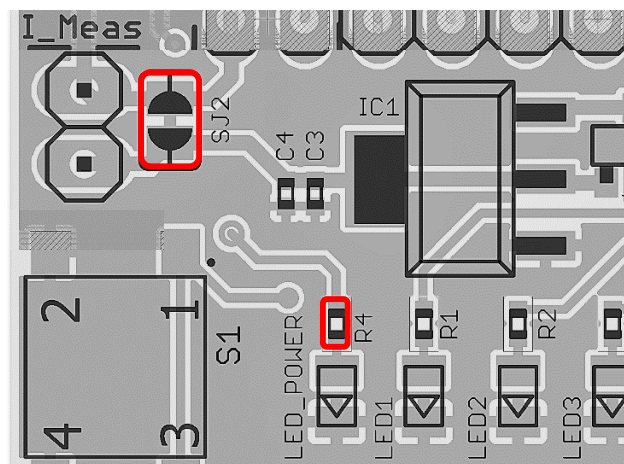


Figure 8: Current measurement



By default the solder bridge SJ2 is connected. For power consumption measurement, the solder bridge shall be disconnected using a cutter and resistor R4 shall be desoldered.



Current measurement using I\_Meas jumper is possible only if the module is powered through connector P3.

### 3.4.2 JP2: Operation mode

By default, JP2 is not assembled. If Jumper JP2 is set during power up or reset, the module starts in transparent mode. For command mode operation, JP2 shall be left open during power up or reset.

JP2	nRF52840	Function
1		VDD
2	P0.19	MODE_1

Table 16: Pin header JP2

### 3.4.3 JP3: Boot mode

By default, JP3 is not assembled. If jumper JP3 is set during power up, the module starts in bootloader mode. For command mode operation the module JP3 shall be left open during power up.



- For Proteus-III (2611011024000) and Proteus-III-SPI (2611011024010) module, bootloader mode allows only over the air (OTA) firmware update.
- For Thyone-I module (2611011021000), bootloader mode allows firmware update only via UART.

JP3	nRF52840	Function
1		GND
2	P0.02	BOOT

Table 17: Pin header JP3

### 3.5 Reset button

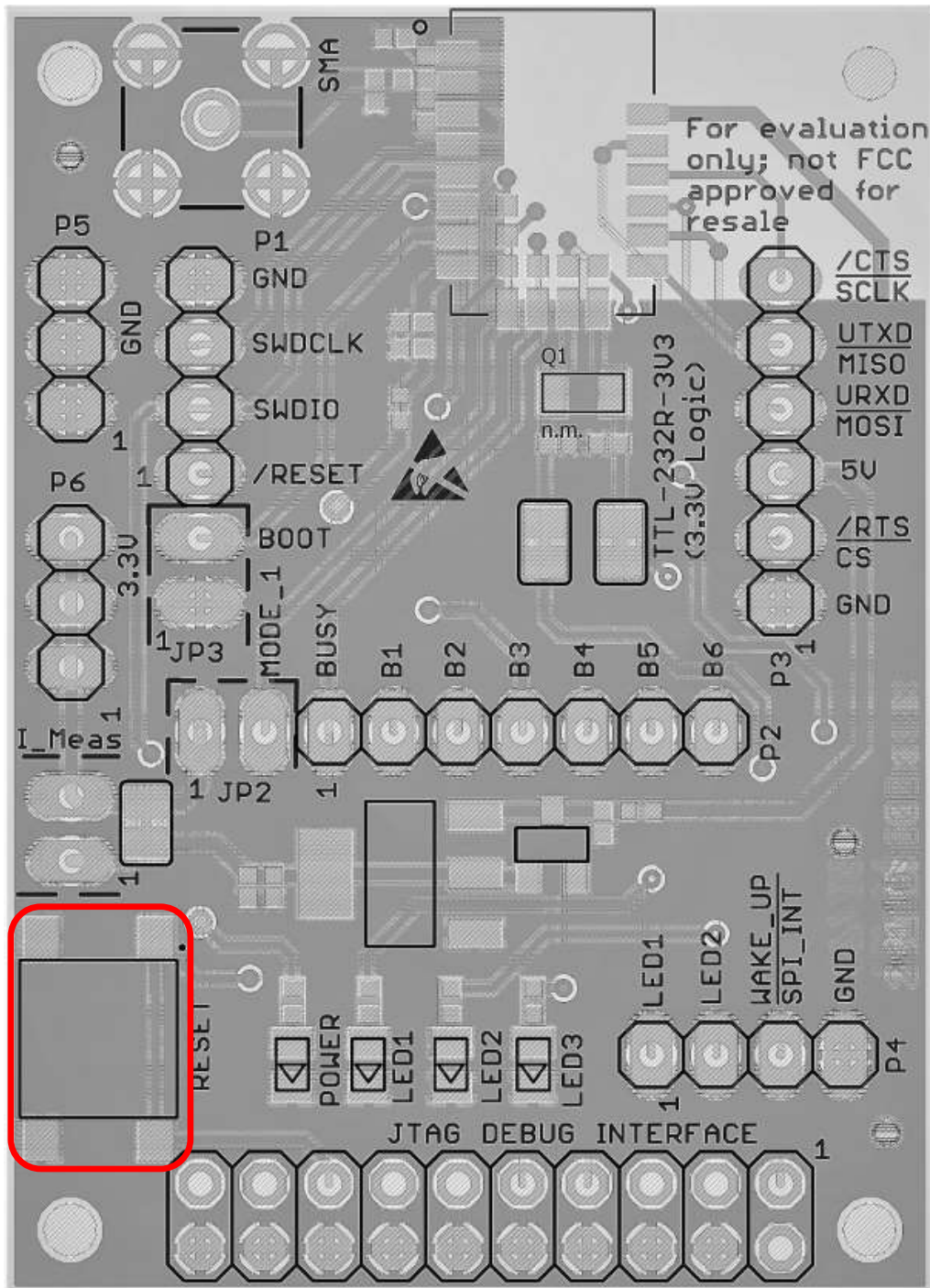


Figure 9: Buttons

On IC level the active low reset input is configured with a pull up resistor. The module provides a */RESET* pin that is connected to this button so that the module can be restarted properly. Please refer to the module specific manual for detailed information [2] [5] [3].

## 3.6 Function blocks

### 3.6.1 Power supply

The mini evaluation board can be powered either by TTL-232R-3V3 cable or by an external power supply. The table 18 lists the connection for different power supply options.

Nr.	Power supply	Connector	I_Meas jumper	Solder bridge	I_Meas current meas.	Supported modules
1	TTL-232R-3V3 cable	P3	Open	Connected	No	Proteus-III / Thyone-I / Setebos-I
2	TTL-232R-3V3 cable	P3	Set	disconnected	Yes	Proteus-III / Thyone-I / Setebos-I
3	External supply	P3 Pin-3 (5V) P3 Pin-1 (GND)	Set	disconnected	Yes	All
4	External supply	P5 (GND) P6 (VDD)	Open	Open	No	All

Table 18: Power supply option



By default solder bridge is connected and pin headers are not assembled.

#### 3.6.1.1 Connector P3, power supply through TTL-232R-3V3

The evaluation board can be sourced by TTL-232R-3V3 cable through P3 connector. TTL-232R-3V3 cable powers the board with 5V supply. The integrated voltage regulator regulates the connected voltage 5V down to 3.3V and supplies the remaining parts of the circuit. If the module is sourced, the power *Power LED* lights up.

#### 3.6.1.2 Connector P3, power supply through external source

The evaluation board can be sourced by an external power supply through the P3 connector Pin-3 (5V) and P3 Pin-1 (GND). If the module is sourced, the power *Power LED* lights up.

#### 3.6.1.3 Connectors P5 and P6, power supply through external source

The development board can be sourced by an external power supply through the P5 (GND) and P6 (3.3V) connector. If the module is sourced, the power *Power LED* lights up.



To use this option I\_Meas jumper and SJ2 shall be left open.

### 3.6.2 UART / USB

The TTL-232R-3V3 cable is used for USB/UART connection between PC and the evaluation board. The IO level of the TTL-232R-3V3 cable is 3.3V. Using the FTDI-driver the PC will show a virtual COM-Port which can be used to communicate with the module.

### 3.6.3 UART direct

If a micro-controller is to be connected to the module, use the *P3* connector. The UART of the host can be directly connected to P3 (Pins are labelled on the evaluation board). The module *RXD* line must be handled accordingly by your host (i.e. pulled up while inactive and during module boot-up).

Beware of IO level compatibility. The host must obey the values stated in the module's manual. Especially the IO level restrictions must be implemented by a host system (i.e. using a level shifter to use the allowed IO levels).

### 3.6.4 SPI direct

If a micro-controller is to be connected to the module, use the *P3* connector and *WAKE\_UP/SPI\_INT* (*P4* pin 3). The SPI of the host can be directly connected to these pins. For easy orientation the pins are labelled on the evaluation board with its functions.

Beware of IO level compatibility. The host must obey the values stated in the module's manual. Especially the IO level restrictions must be implemented by a host system (i.e. using a level shifter to use the allowed IO levels).

### 3.6.5 LFXO crystal

For higher LFCLK accuracy (better than  $\pm 250$ ppm) a low frequency crystal oscillator of 32.768kHz (LFXO) shall be used. A crystal, 3.2 × 1.6mm package, for example *830009706*, can be placed on the evaluation board to position Q1. The needed load capacitance can be reached with capacitors C9 and C10, 0402 package.

nRF52840 pin P0.00/XL1 and P0.01/XL12 are connected to module pad LED\_1 and LED\_2 respectively. If an LFXO is mounted to the EV board the solder bridge SJ1 and SJ3 shall be disconnected using a cutter, therefore the LED1 and LED2 function is no longer available.



For standard firmware the external crystal is not needed. To enable use of the LFXO a custom firmware is required.

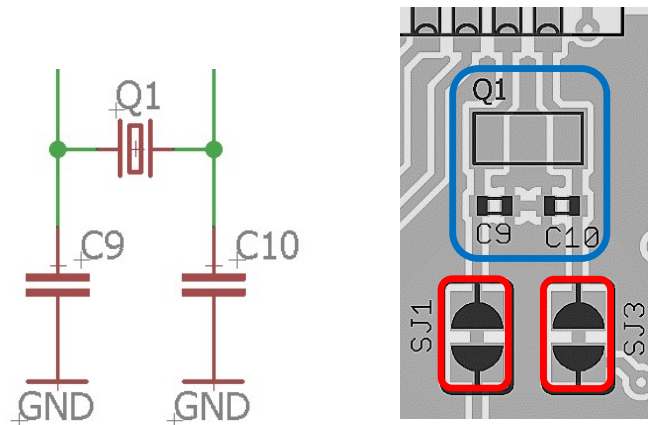


Figure 10: LFXO assembly



By default the LED\_1 and LED\_2 functions of the module are used.



To use the LFXO function, the two solder bridges near the LFXO place holder shall be disconnected using a cutter.

The input capacitance of the pad LED\_1 and LED\_2 are 4 pF. The values of C9 and C10 can be calculated as follows.

The load capacitance of LFXO is given by

$$C_l = \frac{C_{9l} * C_{10l}}{C_{9l} + C_{10l}} \quad (1)$$

If  $C_{9l} = C_{10l} = C$ , then

$$C_l = \frac{C}{2} \quad (2)$$

whereas,

$$C_9 = C - C_{XL1} - C_{PCB} \quad (3)$$

$$C_{10} = C - C_{XL2} - C_{PCB} \quad (4)$$

$C_l$  = Load capacitance of LFXO crystal.

$C_{XL1}$  = Input capacitance of Pad LED\_1 (4 pF)

$C_{XL2}$  = Input capacitance of Pad LED\_2 (4 pF)

$C_{PCB}$  = Parasitic capacitance of PCB Parasitic capacitance of the PCB can vary depending on design and track length. It can vary from 0.5 pF to 2 pF.

For the crystal 830009706 with load capacitance of 9 pF and parasitic capacitance of 2 pF. The value of C9 and C10 results in 12 pF which was also tested on the evaluation board.

Depending on parasitic capacitance of PCB, a capacitance of 12 pF may be a good starting value for C9 and C10.



### 3.6.5.1 LFXO Design guidelines

1. LFXO shall be placed away from high frequency components and traces.
2. The ground connection for the load capacitor shall be short using ground vias.
3. The crystal shall be placed close to the module.
4. PCB traces between module and the crystal shall be kept short.
5. Load capacitors shall be low leakage and temperature stable (NPO or COG) type.
6. The differential traces shall be kept to the same length.
7. Ground area shall be placed under crystal and connected to the main ground plane.
8. Open traces to the pins shall be avoided to reduce parasitic capacitance and coupling effects.
9. Ground area shall be used between the crystal traces and other PCB traces for better decoupling.

### 3.6.6 Programming interface

The evaluation board provides a place holder for 2×10 pin connector. It can be used to connect directly to a JTAG flash adapter used for development. Please take care of the correct mounting of the flash adapter. The recommended flash adapter is one of the "Segger J-Link" family.

### 3.6.7 Radio Protocol Selection (Setebos-I only)

For Setebos-I only, the pin B1/RPS on the connector P2 shall be used to select the radio protocol used by the module.

- A low level during and shortly after reset starts the module with Bluetooth Low Energy 5.1 firmware: the module works as a Proteus-III.
- A high level during and shortly after reset starts the module with Proprietary firmware: the module works as a Thyone-I.

By default, B1/RPS pin is pulled down and the module works therefore as a Proteus-III.

### 3.7 Schematic

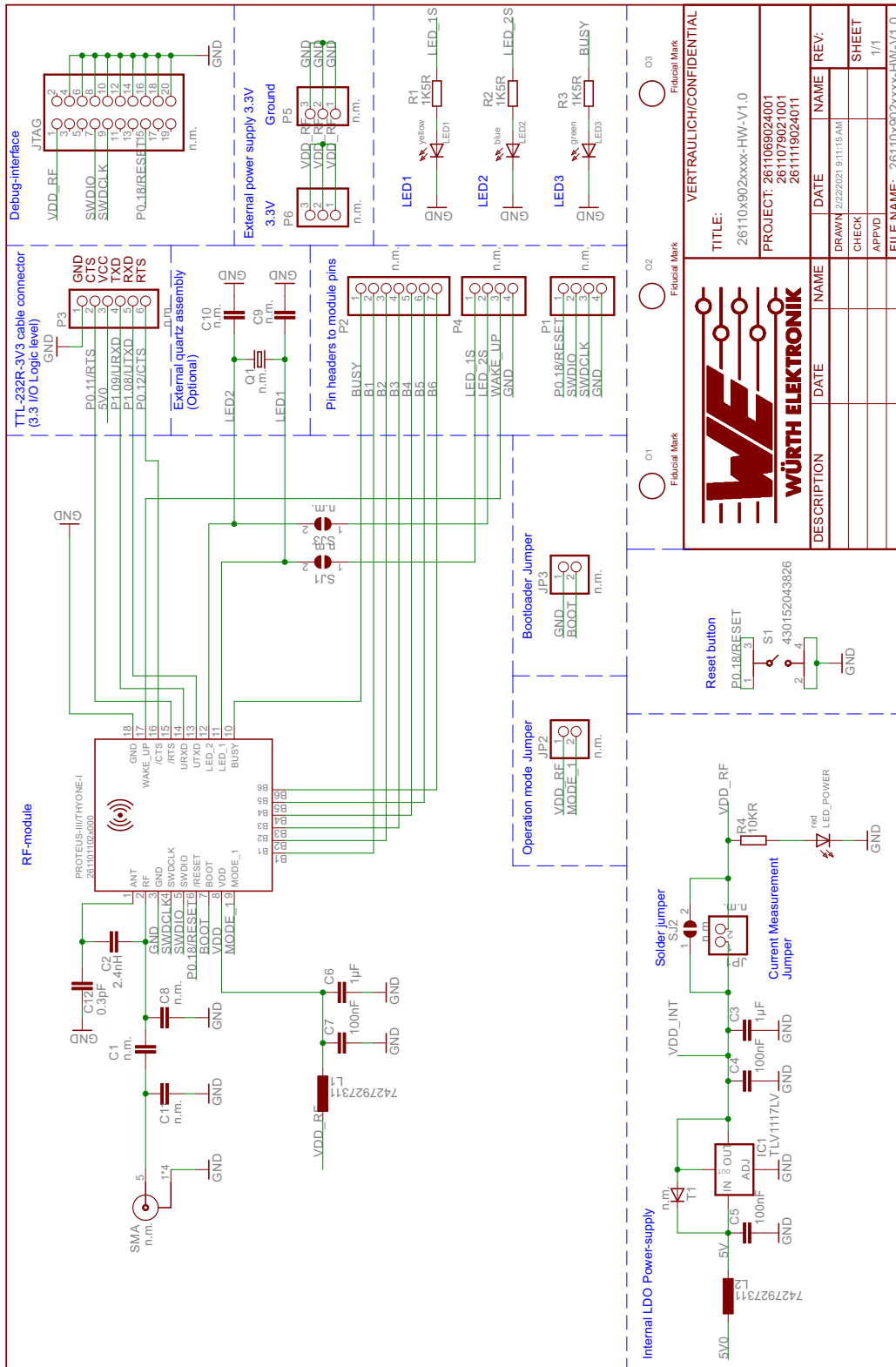


Figure 11: Schematic sheet

### 3.8 Layout

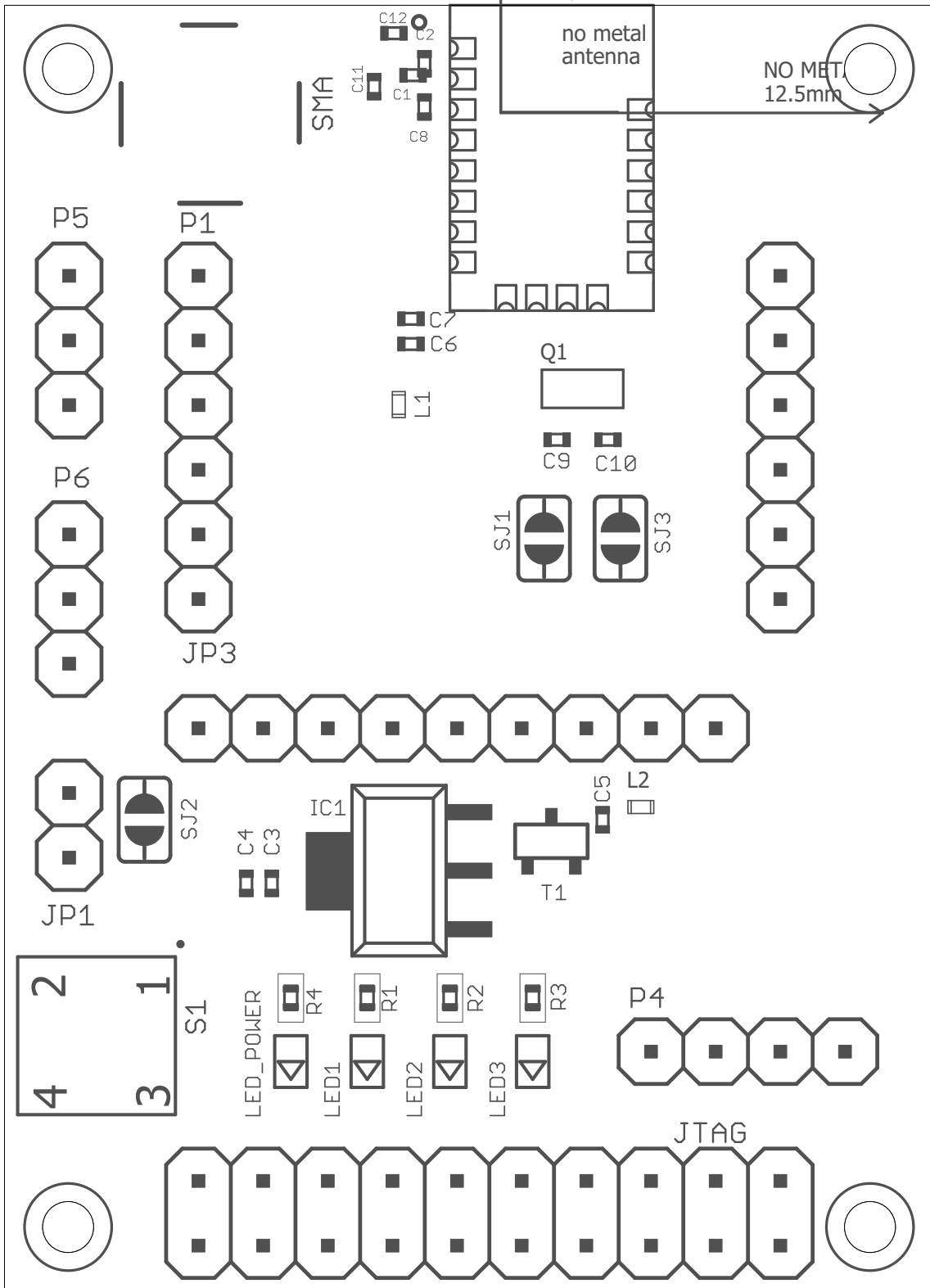


Figure 12: Assembly diagram

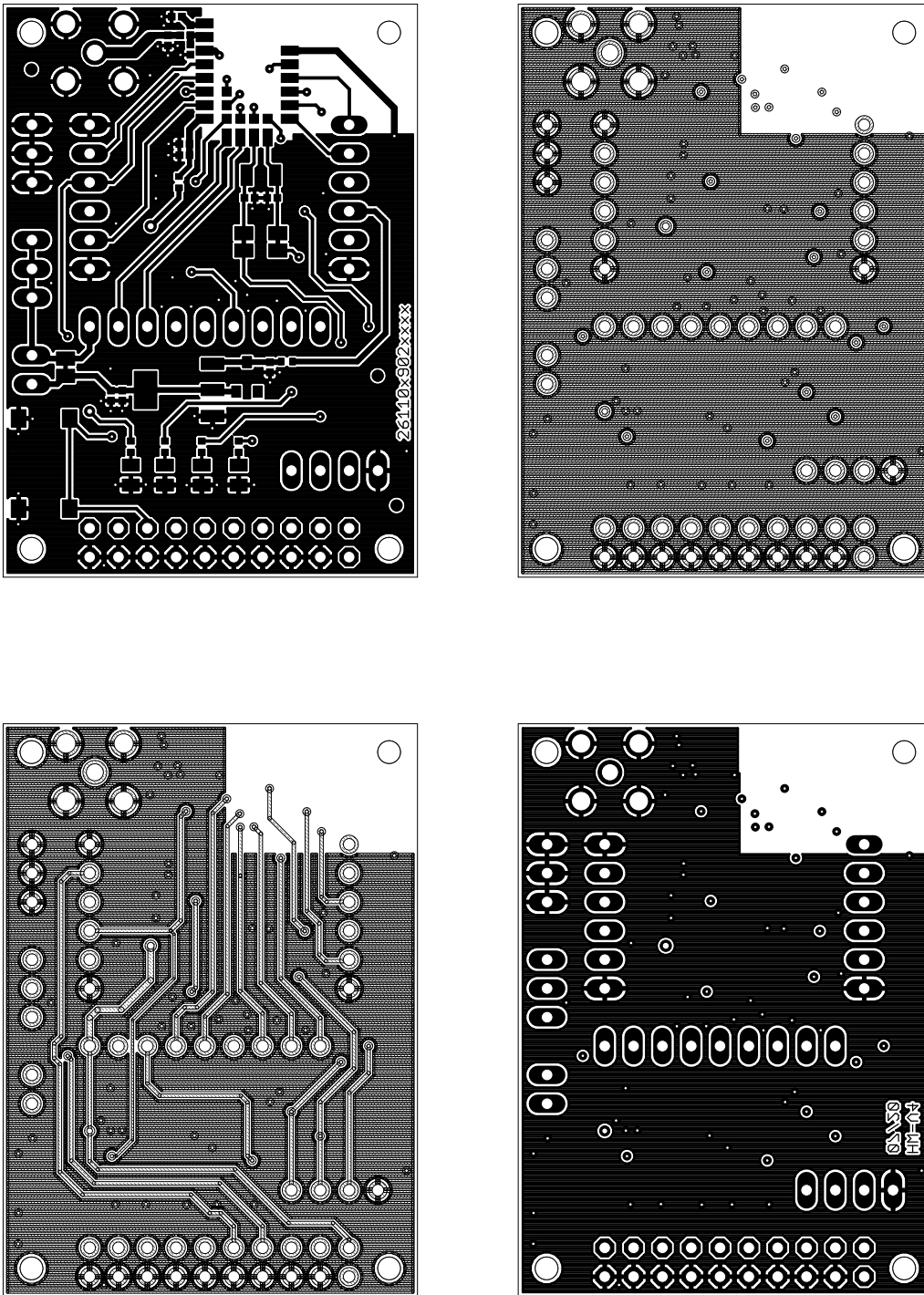


Figure 13: Top,bottom & internal layers

### 3.9 Bill of materials

Part	Value	PACK	MANUFACTURER	NR
C1	n.m.	0402		
C2	2.4nH	0402	Würth Elektronik	7447820024
C3	1µF	0402	Würth Elektronik	885012105012
C4	100nF	0402	Würth Elektronik	885012205037
C5	100nF	0402	Würth Elektronik	885012205037
C6	1µF	0402	Würth Elektronik	885012105012
C7	100nF	0402	Würth Elektronik	885012205037
C8	n.m.	0402		
C9	n.m.	0402		
C10	n.m.	0402		
C11	n.m.	0402		
C12	0.3pF	0402	Würth Elektronik	885392005001
IC1	LDO, 3V3		Diodes Incorporated On Semiconductor Texas Instruments	AZ1117IH-3.3TRG1 NCP1117ST33T3G TLV1117LV
JP1	n.m.			
JP2	n.m.			
JP3	n.m.			
JTAG	n.m.			
L1	7427927311	0402	Würth Elektronik	7427927311
L2	7427927311	0402	Würth Elektronik	7427927311
LED1	yellow		Würth Elektronik	150080YS75000
LED2	blue		Würth Elektronik	150080BS75000
LED3	green		Würth Elektronik	150080GS75000
LED_POWER	red		Würth Elektronik	150080RS75000
O1	OPT_MARKE			
O2	OPT_MARKE			
O3	OPT_MARKE			
P1	not mounted			
P2	not mounted			
P3	not mounted			
P4	not mounted			
P5	not mounted			
P6	not mounted			
Proteus-III /Thyone-I /Setebos-I	261101102x0x0		Würth Elektronik	261101102x0x0 (EV Board dependant)
Q1	not mounted			
R1	1K5R	0402		
R2	1K5R	0402		
R3	1K5R	0402		
R4	10KR	0402		
S1	430152043826		Würth Elektronik	430152043826
SJ1	not mounted			
SJ2	not mounted			
SJ3	not mounted			
SMA	not mounted		Würth Elektronik	
T1	not mounted			

## 4 Regulatory compliance information

### 4.1 European Conformity

Pursuant to Article 1 (2.) of the EU directive 2014/53/EU, Article 1 (2.) the directive does not apply to equipment listed in Annex I (4.): custom-built evaluation kits designed for professionals to be used solely at research and development facilities for such purposes.

### 4.2 FCC

Pursuant to §2.803 (c) of Title 47 Chapter I Subchapter A Part 2 Subpart I, the evaluation kit falls under the FCC exception. Therefore it is marked as "For evaluation only; not FCC approved for resale".

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## 5 References

- [1] TTL-232R-3V3 FTDI cable. [https://www.ftdichip.com/Support/Documents/DataSheets/Cables/DS\\_TTL-232R\\_CABLES.pdf](https://www.ftdichip.com/Support/Documents/DataSheets/Cables/DS_TTL-232R_CABLES.pdf).
- [2] Würth Elektronik. Proteus-III user manual. <https://www.we-online.de/katalog/de/manual/2611011024000>.
- [3] Würth Elektronik. Setebos-I user manual. <https://www.we-online.de/katalog/de/manual/2611011024020>.
- [4] Würth Elektronik. Smart Commander PC tool. <https://www.we-online.com/SmartCommander>.
- [5] Würth Elektronik. Thyone-I user manual. <https://www.we-online.de/katalog/de/manual/2611011021000>.

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### 6.3 Best care and attention

Any product-specific data sheets, manuals, application notes, PCN's, warnings and cautions must be strictly observed in the most recent versions and matching to the products firmware revisions. This documents can be downloaded from the product specific sections on the wireless connectivity homepage.

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Some products within the product range may contain substances, which are subject to restrictions in certain jurisdictions in order to serve specific technical requirements. Necessary information is available on request. In this case, the field sales engineer or the internal sales person in charge should be contacted who will be happy to support in this matter.



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