SCT4045DEHR



Automotive Grade N-channel SiC power MOSFET

Datasheet

V _{DSS}	750V
R _{DS(on)} (Typ.)	45mΩ
Ι _D ^{*1}	34A
P_D	115W

Outline TO-247N

(1) Gate (2) Drain (3) Source

*1 Body Diode

(2) q

(3) 9

Features

- 1) Qualified to AEC-Q101
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Fast reverse recovery
- 5) Easy to parallel
- 6) Simple to drive
- 7) Pb-free lead plating; RoHS compliant

Application

- Automobile
- Switch mode power supplies

●Packaging specifications

•Inner circuit

	ging opcomouncine	
	Packing	Tube
	Reel size (mm)	-
Type	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT4045DE

●Absolute maximum ratings (T_c = 25°C)

Parameter		Symbol	Value	Unit
Drain - source voltage		V_{DSS}	750	V
Continuous drain and source current	\/ _\/	, , *1	34	А
$T_c = 100$ °C	$V_{GS} = V_{GS_on}$	I _D , I _S *1	24	А
Pulsed drain current	$V_{GS} = V_{GS_on}$	I _{D,pulse} *2	61	Α
Body diode pulsed forward current	$V_{GS} = 0 V$	I _{S,pulse} *3	34	А
Body diode surge forward current	$V_{GS} = 0 V$	I _{S,pulse} *4	61	А
Gate - source voltage (DC)		V_{GSS_DC}	-4 to +21	V
Gate - source surge voltage (t _{surge} < 300ns)		$V_{GSS_surge}^{*5}$	-4 to +23	V
Recommended turn-on gate - source drive voltage		${\sf V_{GS_on}}^{*6}$	+15 to +18	V
Recommended turn-off gate - source drive voltage		V_{GS_off}	0	V
Virtual junction temperature		T_{vj}	175	°C
Range of storage temperature		T_{stg}	-40 to +175	°C

●Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Values	Unit	
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	W	$V_{GS} = 0 \text{ V}, I_D = 5.3\text{mA}$				V
	V _{(BR)DSS}	$T_{vj} = 25^{\circ}C$	750	-	-	V
		$V_{GS} = 0 \text{ V}, V_{DS} = 750 \text{V}$				
Zero Gate voltage Drain current	I _{DSS}	$T_{vj} = 25^{\circ}C$	-	1	80	μA
Diam current		T _{vj} = 150°C	-	10	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V$, $V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$	ı	ı	-100	nA
Gate threshold voltage	$V_{GS(th)}^{*7}$	$V_{DS} = 10V, I_{D} = 8.89 \text{mA}$	2.8	ı	4.8	V
		$V_{GS} = 18V, I_{D} = 17A$				
Static Drain - Source on - state resistance	R _{DS(on)} *8	$T_{vj} = 25^{\circ}C$	-	45	59	mΩ
on state registration		T _{vj} = 150°C	-	77	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	4	-	Ω

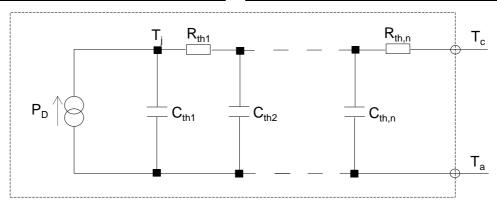
●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	$R_{thJC}^{}^{*9}}$	-	0.98	1.3	K/W

● Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	1.7 ×10 ⁻¹	
R _{th2}	4.1 ×10 ⁻¹	K/W
R _{th3}	4.0 ×10 ⁻¹	

Symbol	Value	Unit
C_{th1}	3.6 ×10 ⁻⁴	
C_{th2}	1.5 ×10 ⁻³	Ws/K
C_{th3}	6.5 × 10 ⁻³	



Electrical characteristics ($T_{vj} = 25$ °C unless otherwise specified)

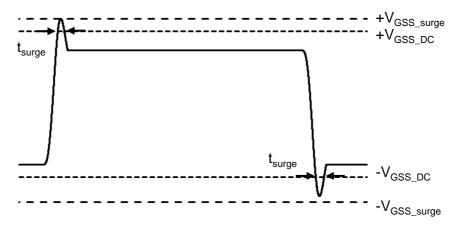
Parameter	Symbol	Conditions		Values		Unit
- Farameter		Conditions	Min.	Тур.	Max.	Offic
Transconductance	g _{fs} *8	$V_{DS} = 10V, I_{D} = 17A$	-	9.3	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	1460	-	
Output capacitance	C _{oss}	V _{DS} = 500V	-	69	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	5	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 500V	1	90	-	pF
Total Gate charge	Qg *8	$V_{DS} = 500V$ $I_{D} = 17A$	ı	63	-	
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	ı	14	-	nC
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	19	-	
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 500V$ $I_{D} = 17A$	ı	5.8	-	
Rise time	t _r *8	$V_{GS} = +18V / 0V$	ı	28	-	ns
Turn - off delay time	t _{d(off)} *8	$R_G = 3.3\Omega$, $L = 250\mu H$ E_{on} includes diode reverse recovery $L_{\sigma} = 50nH$, $C_{\sigma} = 10pF$	-	29	-	113
Fall time	t _f *8		ı	12	-	
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	210	-	1
Turn - off switching loss	E _{off} *8		-	33	-	μJ

●Body diode electrical characteristics (Source-Drain) (T_{vi} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	200		Values	
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V _{SD} *8	$V_{GS} = 0V, I_D = 17A$	-	3.3	-	V
Reverse recovery time	t _{rr} *8	$I_F = 17A$ $V_R = 500V$	ı	19	ı	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 1700A/µs	ı	86	ı	nC
Peak reverse recovery current	I _{rrm} *8	L_{σ} = 50nH, C_{σ} = 10pF See Fig. 3-1, 3-2.	-	9	-	А

^{*1} Limited by maximum T_{vj} and for Max. R_{thJC} .

*5 Example of acceptable V_{GS} waveform



- * 6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying $V_{GS} = 21V$ for 100ms.
- *8 Pulsed
- *9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf

^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

^{*3} Only for body-diode, Repititive pulse, PW ≤ 500ns, Duty cycle ≤ 5%

^{*4} When used as a protective function, PW ≤ 10µs

Fig.1 Power Dissipation Derating Curve

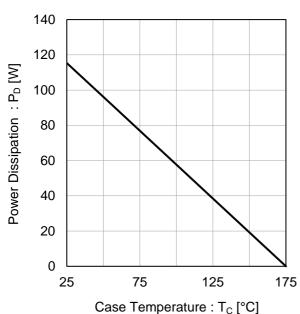


Fig.2 Maximum Safe Operating Area

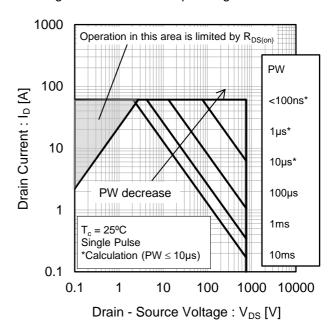
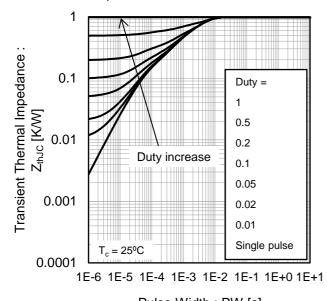
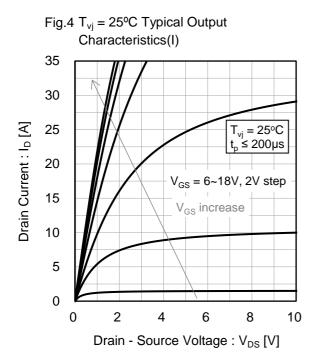


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width



Pulse Width: PW [s]

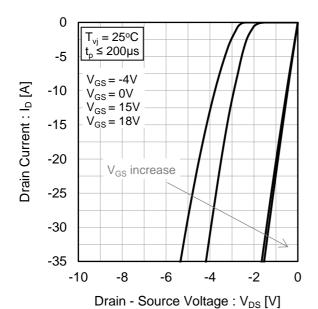


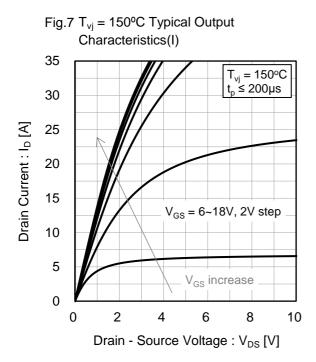
Characteristics(II)

15 $V_{\text{os}} = 25^{\circ}\text{C}$ $V_{\text{p}} \leq 200 \mu\text{s}$ $V_{\text{gs}} = 6 \sim 18 \text{V}, 2 \text{V step}$

Fig.5 $T_{vj} = 25^{\circ}C$ Typical Output

Fig.6 T_{vj} = 25°C 3rd Quadrant Characteristics





Characteristics(II)

15 $V_{gs} = 6 \sim 18V, 2V \text{ step}$ $V_{gs} = 6 \sim 18V, 2V \text{ step}$ Drain - Source Voltage : V_{DS} [V]

Fig.8 T_{vj} = 150°C Typical Output

Fig.9 $T_{vj} = 150^{\circ}$ C 3rd Quadrant Characteristics $T_{vj} = 150^{\circ}C$ ≤ 200µs -5 $V_{GS} = -4V$ Drain Current: I_D [A] -10 $V_{GS} = 0V$ $V_{GS} = 15V$ $V_{GS} = 18V$ -15 -20 V_{GS} increase -25 -30 -35 -10 -8 -6 -2 0 -4 Drain - Source Voltage: V_{DS} [V]

vs. Gate - Source Voltage 6 Body Diode Forward Voltage : V_{SD} [V] $I_D = 17A$ $t_p \le 200 \mu s$ 5 4 3 T_{vi}=150°C 2 1 T_{vi}=25°C 0 -4 0 4 8 12 20 16 Gate - Source Voltage : V_{GS} [V]

Fig.10 Body Diode Forward Voltage

Fig.11 Typical Transfer Characteristics (I)

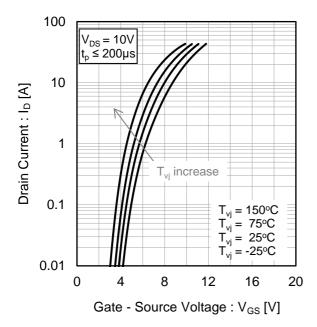


Fig.12 Typical Transfer Characteristics (II)

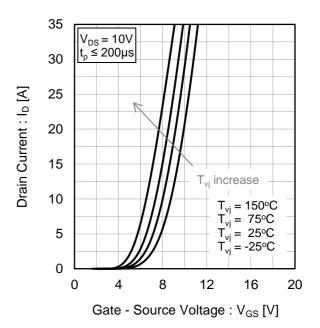


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

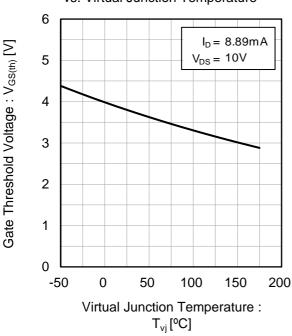


Fig.14 Transconductance vs. Drain Current

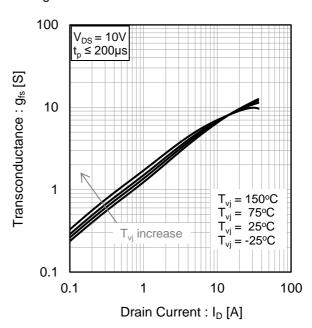


Fig.15 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

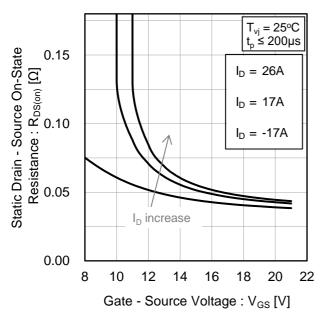


Fig.16 Static Drain - Source On - State Resistance vs. Virtual Junction Temperature

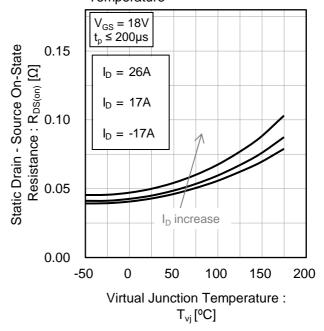


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

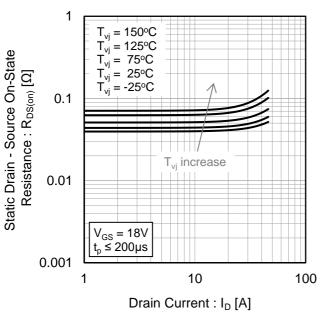
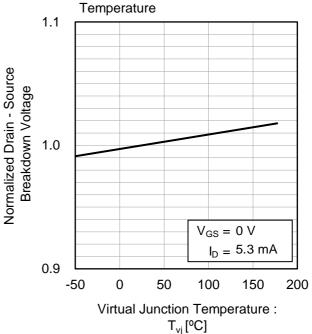
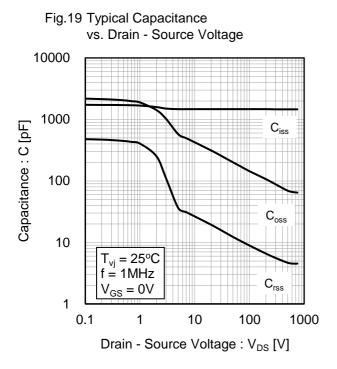


Fig.18 Normalized Drain - Source Breakdown Voltage vs. Virtual Junction





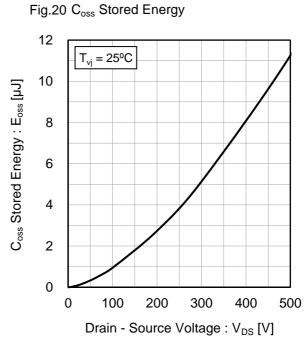


Fig.21 Dynamic Input Characteristics

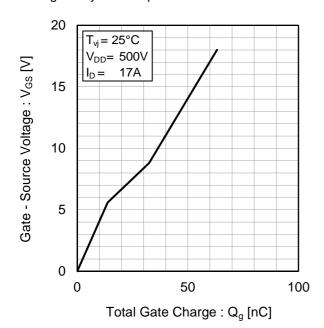


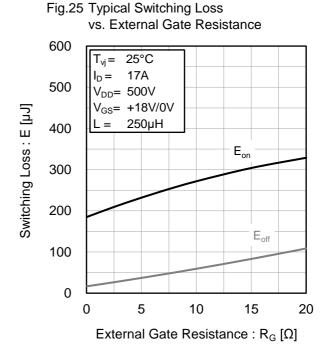
Fig.22 Typical Switching Time

vs. External Gate Resistance 100 25°C 17A $I_D =$ $V_{DD} = 500V$ t_{d(off)} 80 V_{GS}= +18V/0V Switching Time: t [ns] 250µH 60 $t_{d(on)}$ 40 20 0 5 10 15 20 External Gate Resistance : $R_G [\Omega]$

vs. Drain - Source Voltage 600 25°C 17A $I_D =$ 500 V_{GS}= +18V/0V $R_G = 3.3\Omega$ Switching Loss: E [µJ] 250µH L = 400 300 E_{on} 200 100 $\mathsf{E}_{\mathsf{off}}$ 0 100 200 300 400 500 Drain - Source Voltage: V_{DS} [V]

Fig.23 Typical Switching Loss

Fig.24 Typical Switching Loss vs. Drain Current 600 25°C $T_{vj} =$ $V_{DD} = 500V$ 500 +18V/0V $V_{GS} =$ E_{on} Switching Loss: E [µJ] $R_G =$ 3.3Ω 400 250µH 300 200 $\mathsf{E}_{\mathrm{off}}$ 100 0 0 5 15 20 10 25 30 35 Drain Current: ID [A]



Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

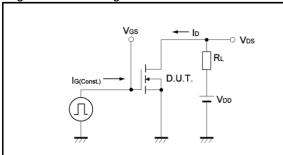


Fig.2-1 Switching Characteristics Measurement Circuit

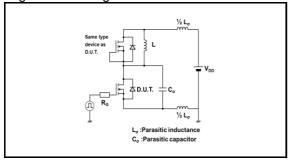


Fig.2-3 Waveforms for Switching Energy Loss

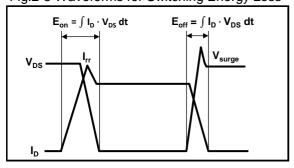


Fig.3-1 Reverse Recovery Time Measurement Circuit

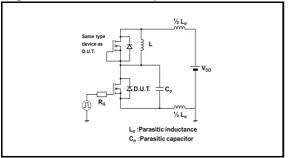


Fig.1-2 Gate Charge Waveform

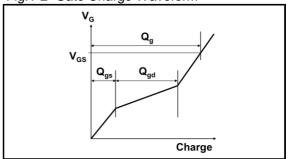


Fig.2-2 Waveforms for Switching Time

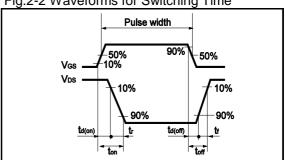
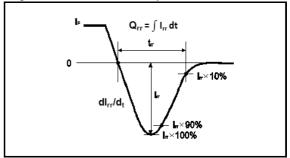
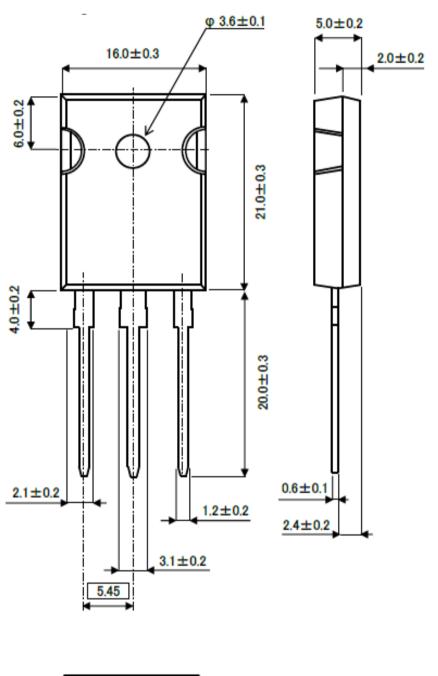


Fig.3-2 Reverse Recovery Waveform

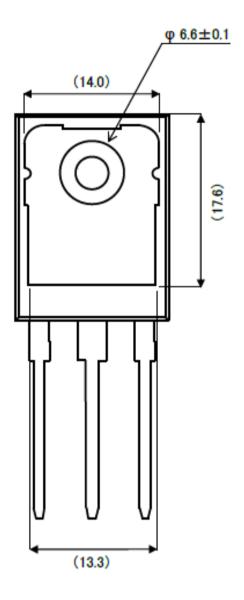


Package Dimensions



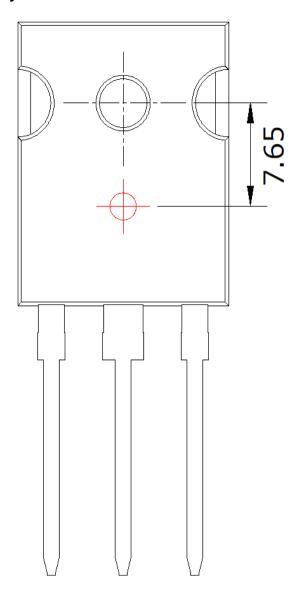


Unit: mm



Unit: mm

●Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- •If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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