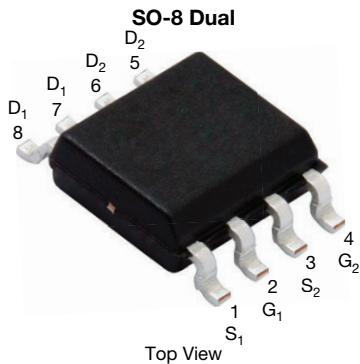


## Dual N-Channel 60 V (D-S) MOSFET



### FEATURES

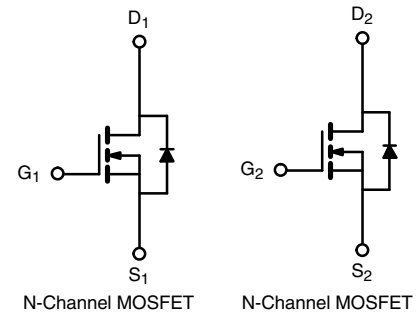
- TrenchFET® Gen IV power MOSFET
- Fully lead (Pb)-free device
- Optimized  $Q_g$ ,  $Q_{gd}$ , and  $Q_{gd}/Q_{gs}$  ratio reduces switching related power loss
- 100 %  $R_g$  and UIS tested
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Synchronous rectification
- Load switch
- Motor drive control
- Battery management



PRODUCT SUMMARY	
$V_{DS}$ (V)	60
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 10$ V	0.029
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5$ V	0.038
$Q_g$ typ. (nC)	3.3
$I_D$ (A) <sup>a</sup>	8
Configuration	Dual

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	Si9634DY-T1-GE3

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	$V_{DS}$	60	V	
Gate-source voltage	$V_{GS}$	$\pm 20$	V	
Continuous drain current ( $T_J = 150$ °C)	$I_D$	$T_C = 25$ °C	8 <sup>a</sup>	A
		$T_C = 70$ °C	6.6	
		$T_A = 25$ °C	6.2 <sup>b, c</sup>	
		$T_A = 70$ °C	5 <sup>b, c</sup>	
Pulsed drain current ( $t = 100$ $\mu$ s)	$I_{DM}$	32	A	
Continuous source-drain diode current	$I_S$	$T_C = 25$ °C	3	A
		$T_A = 25$ °C	1.7 <sup>b, c</sup>	
Single pulse avalanche current	$I_{AS}$	10	A	
Single pulse avalanche energy	$E_{AS}$	5	mJ	
Maximum power dissipation	$P_D$	$T_C = 25$ °C	3.6	W
		$T_C = 70$ °C	2.3	
		$T_A = 25$ °C	2 <sup>b, c</sup>	
		$T_A = 70$ °C	1.3 <sup>b, c</sup>	
Operating junction and storage temperature range	$T_J, T_{stg}$	-55 to +150	°C	
Soldering recommendations (peak temperature) <sup>c</sup>		260	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient <sup>b, e</sup>	$R_{thJA}$	50	62.5	°C/W
Maximum junction-to-foot (drain)	$R_{thJF}$	28	35	

#### Notes

- Package limited
- Surface mounted on 1" x 1" FR4 board
- $t = 10$  s
- Maximum under steady state conditions is 110 °C/W



SPECIFICATIONS ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-source breakdown voltage	$V_{DS}$	$V_{GS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$	60	-	-	V
$V_{DS}$ temperature coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	33	-	mV/ $^\circ\text{C}$
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	-4.8	-	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$	1	-	3	V
Gate-source leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}$ , $V_{GS} = \pm 20$	-	-	100	nA
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 70\text{ }^\circ\text{C}$	-	-	15	
Drain-source on-state resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$ , $I_D = 5\text{ A}$	-	0.022	0.029	$\Omega$
		$V_{GS} = 4.5\text{ V}$ , $I_D = 4\text{ A}$	-	0.029	0.038	
Forward transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10\text{ V}$ , $I_D = 10\text{ A}$	-	23	-	S
<b>Dynamic <sup>b</sup></b>						
Input capacitance	$C_{iss}$	$V_{DS} = 30\text{ V}$ , $V_{GS} = 0\text{ V}$ , $f = 1\text{ MHz}$	-	420	-	pF
Output capacitance	$C_{oss}$		-	92	-	
Reverse transfer capacitance	$C_{rss}$		-	4	-	
Total gate charge	$Q_g$	$V_{DS} = 30\text{ V}$ , $V_{GS} = 10\text{ V}$ , $I_D = 5\text{ A}$	-	7.1	11	nC
		$V_{DS} = 30\text{ V}$ , $V_{GS} = 4.5\text{ V}$ , $I_D = 5\text{ A}$	-	3.3	5	
Gate-source charge	$Q_{gs}$		-	1.7	-	
Gate-drain charge	$Q_{gd}$		-	0.9	-	
Gate resistance	$R_g$	$f = 1\text{ MHz}$	0.3	1.6	3.2	$\Omega$
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 30\text{ V}$ , $R_L = 6\text{ }\Omega$ , $I_D \cong 5\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 1\text{ }\Omega$	-	10	20	ns
Rise time	$t_r$		-	5	10	
Turn-off delay time	$t_{d(off)}$		-	15	30	
Fall time	$t_f$		-	5	10	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 30\text{ V}$ , $R_L = 6\text{ }\Omega$ , $I_D \cong 5\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\text{ }\Omega$	-	12	25	
Rise time	$t_r$		-	16	35	
Turn-off delay time	$t_{d(off)}$		-	11	25	
Fall time	$t_f$		-	5	10	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous source-drain diode current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$	-	-	8	A
Pulse diode forward current	$I_{SM}$		-	-	32	
Body diode voltage	$V_{SD}$	$I_S = 2\text{ A}$ , $V_{GS} = 0\text{ V}$	-	0.8	1.2	V
Body diode reverse recovery time	$t_{rr}$	$I_F = 5\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$ , $T_J = 25\text{ }^\circ\text{C}$	-	14	30	ns
Body diode reverse recovery charge	$Q_{rr}$		-	10	20	nC
Reverse recovery fall time	$t_a$		-	8	-	ns
Reverse recovery rise time	$t_b$		-	6	-	

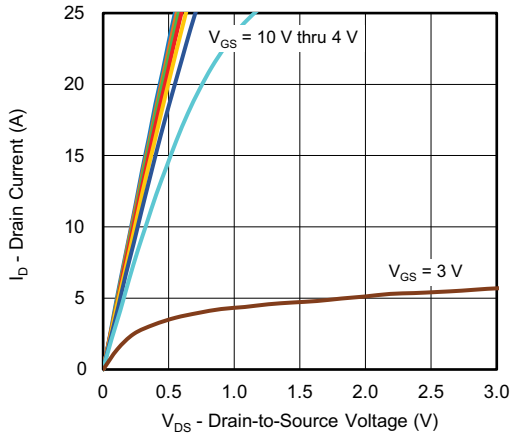
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$   
b. Guaranteed by design, not subject to production testing

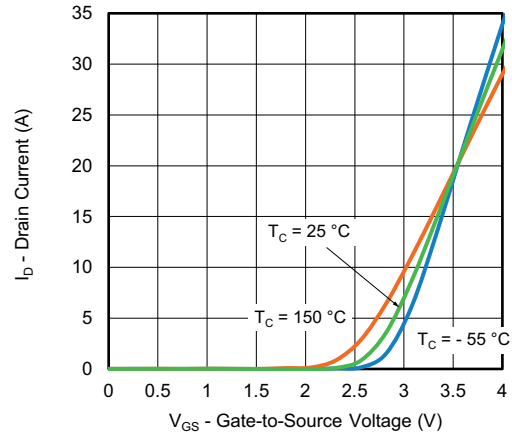
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



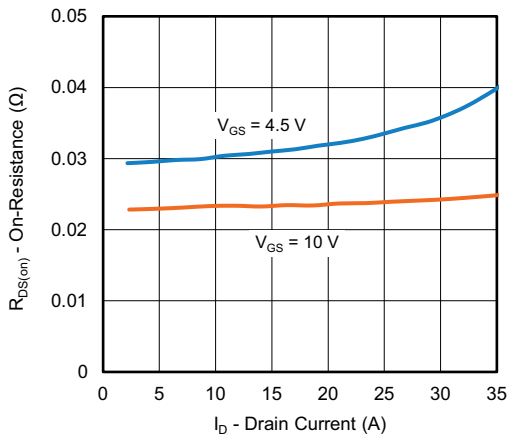
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



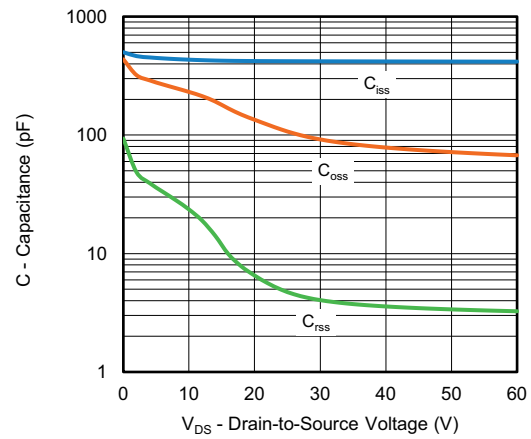
**Output Characteristics**



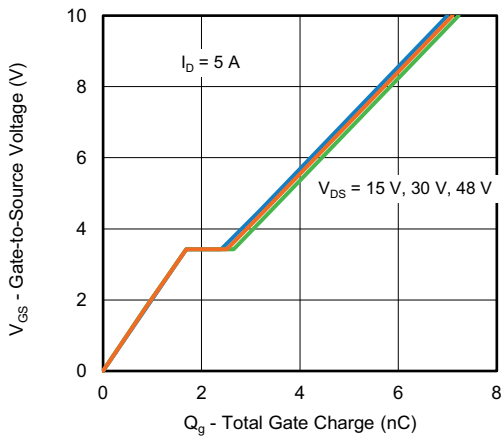
**Transfer Characteristics**



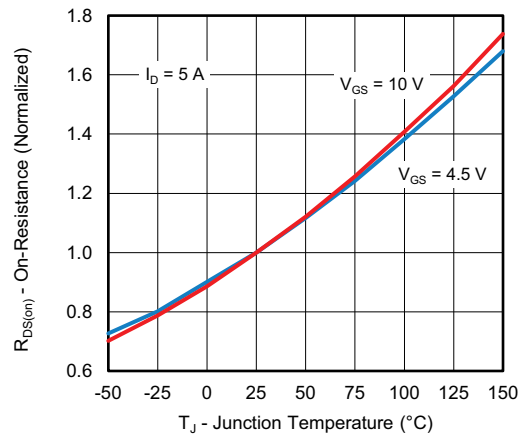
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



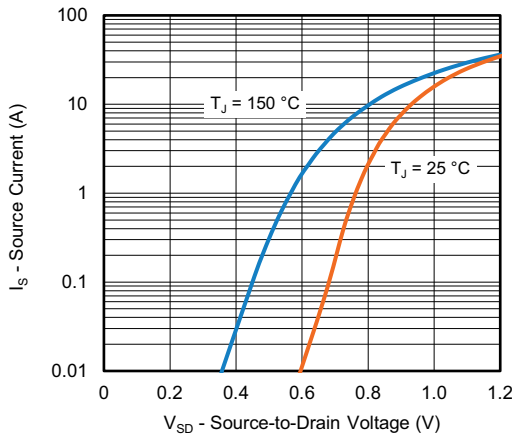
**Gate Charge**



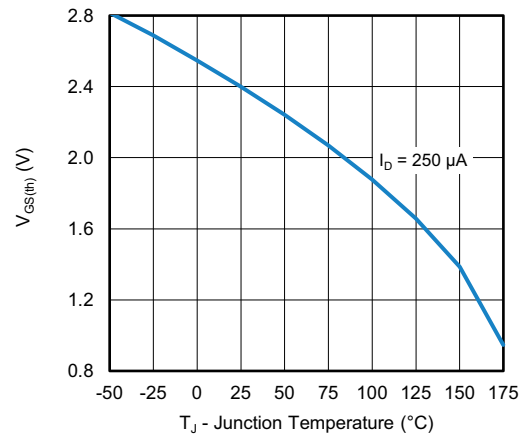
**On-Resistance vs. Junction Temperature**



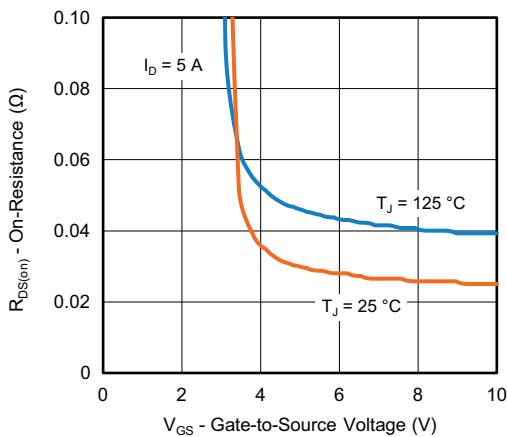
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



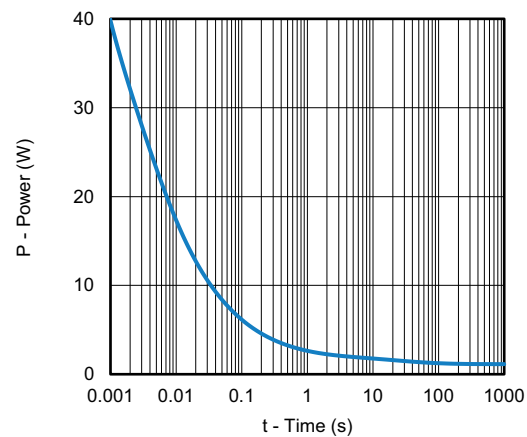
Source-Drain Diode Forward Voltage



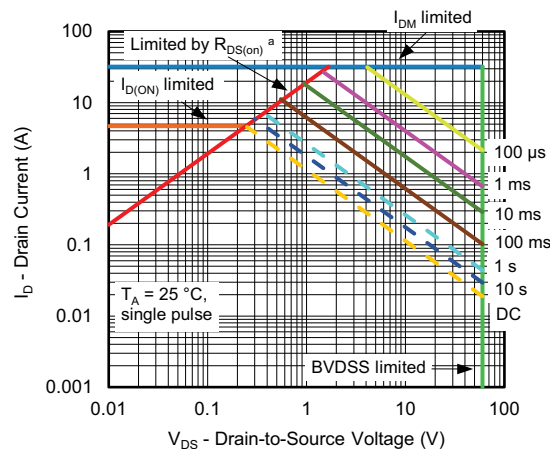
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



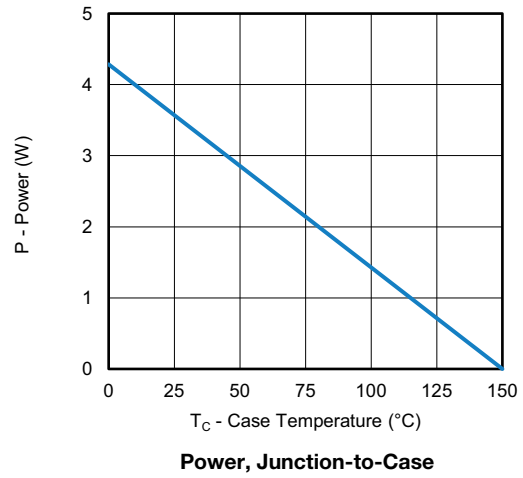
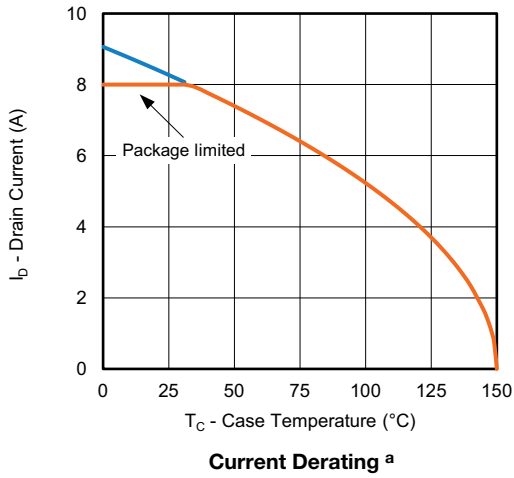
Safe Operating Area, Junction-to-Ambient

Note

a.  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

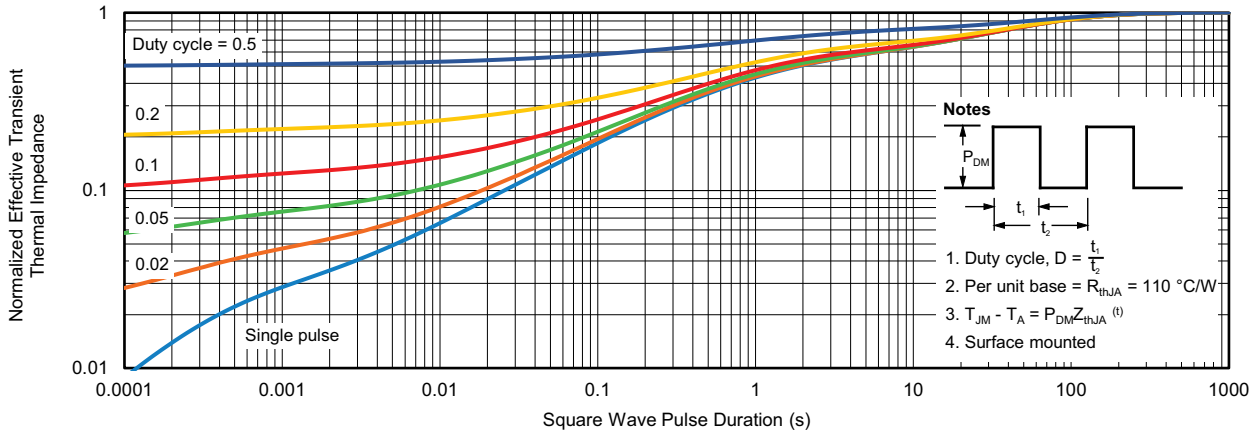


**Note**

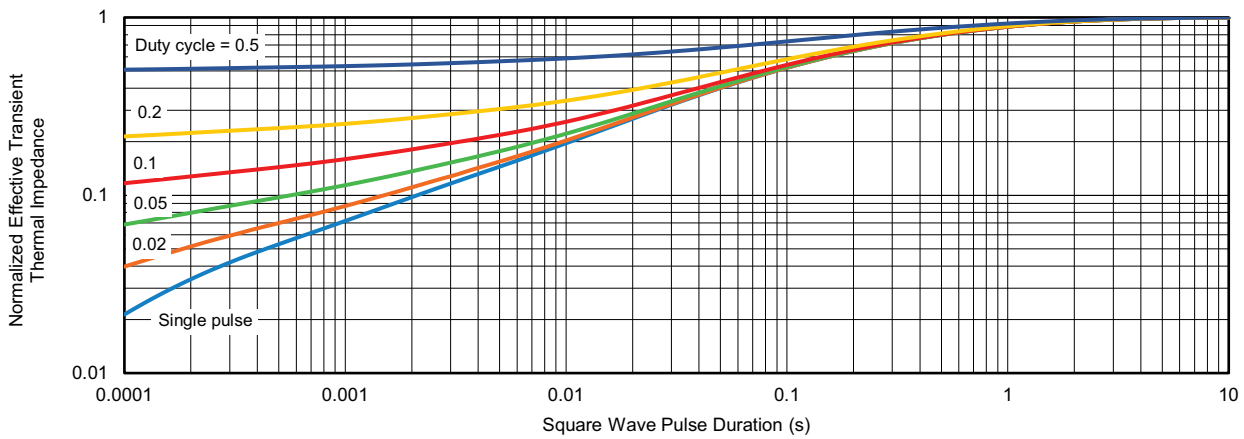
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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