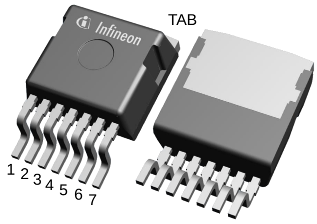
CoolSiC™ 1200 V SiC Trench MOSFET : Silicon Carbide MOSFET



Halogen-free
Green
Lead-free
RoHS

Features

- V_{DSS} = 1200 V at T_{vj} = -55...175 °C
- I_{DDC} = 30 A at T_C = 25°C
- $R_{DS(on)}$ = 80 mΩ at V_{GS} = 20 V, T_{vj} = 25°C
- New performance-optimized chip technology (Gen1p) with improved R_{DSon}* A FOM
- Best in class switching energy for lower switching losses and reduced cooling efforts
- Lowest device capacitances for higher switching speeds and higher power density
- A combination of low C_{rss}/C_{iss} ratio and high $V_{GS(th)}$ to avoid parasitic turn-on and enable unipolar gate driving
- Reduced total gate charge Q_{Gtot} for lower driving power and losses
- Increased recommended turn-on voltage ($V_{GS(on)}$ = 20 V) for lower $R_{DS(on)}$
- .XT die attach technology for best in class thermal performance
- Low package stray inductance for faster and cleaner switching
- Sense (Kelvin) source pin for better gate control and reduced switching losses
- Minimal creepage distance 5.85 mm (material group II) to fit 800 V applications without coating
- SMT package for automated assembly and reduced system costs

Potential applications

- On-board charger
- DC/DC converter
- Auxiliary drives

Product validation

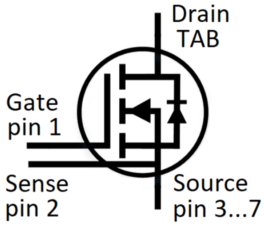
- Qualified for Automotive Applications. Product Validation according to AEC-Q100/101

Description

Pin definition:

- Pin 1 - Gate
- Pin 2 - Kelvin sense contact
- Pin 3…7 - Source
- Tab - Drain

Note: The source and sense pins are not exchangeable, their exchange might lead to malfunction



Type	Package	Marking
AIMBG120R080M1	PG-TO263-7-HV-ND5.8	AS80MM1

Table of contents

1 Package

Table 1 Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Storage temperature	T_{stg}		-55		150	°C
Soldering temperature	T_{sold}				260	°C
MOSFET/body diode thermal resistance, junction-case	$R_{th(j-c)}$			0.68	0.89	K/W

Note: *Not subject to production test. Parameter verified by design/characterization.*

2 MOSFET

Table 2 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Drain-source voltage	V_{DSS}	T_{vj} = -55...175 °C		1200	V
Continuous DC drain current for $R_{th(j-c,max)}$, limited by $T_{vj(max)}$	I_{DDC}	V_{GS} = 20 V	T_c = 25 °C	30	A
			T_c = 100 °C	21	
Peak drain current, t_p limited by $T_{vj(max)}$	I_{DM}	V_{GS} = 20 V		78	A
Gate-source voltage, max. transient voltage[1)]	V_{GS}	$t_p \leq 0.5$ µs, $D < 0.01$		-10...25	V
Gate-source voltage, max. static voltage	V_{GS}			-5...23	V
Avalanche energy, single pulse	E_{AS}	I_D = 7.6 A, V_{DD} = 50 V, L = 4.76 mH		136	mJ
Power dissipation, limited by $T_{vj(max)}$	P_{tot}		T_c = 25 °C	168	W
			T_c = 100 °C	84	

1) **Important note:** The selection of positive and negative gate-source voltages impacts the long-term behavior of the device. The design guidelines described in Application Note AN2018-09 must be considered to ensure sound operation of the device over the planned lifetime.

Table 3 Recommended values

Parameter	Symbol	Note or test condition	Values	Unit
Recommended turn-on gate voltage	$V_{GS(on)}$		20	V
Recommended turn-off gate voltage	$V_{GS(off)}$		0	V

Table 4 Characteristic values

Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
Drain-source on-state resistance[1)]	$R_{DS(on)}$	I_D = 10 A	T_{vj} = 25 °C, $V_{GS(on)}$ = 20 V		80	100	mΩ
			T_{vj} = 100 °C, $V_{GS(on)}$ = 20 V		112		
			T_{vj} = 175 °C, $V_{GS(on)}$ = 20 V		159		
			T_{vj} = 25 °C, $V_{GS(on)}$ = 18 V		84		
Gate-source threshold voltage[1)]	$V_{GS(th)}$	I_D = 3.8 mA, $V_{DS} = V_{GS}$ (tested after 1 ms pulse at V_{GS} = 20 V)	T_{vj} = 25 °C	3.5	4.3	5.1	V
			T_{vj} = 175 °C		3.8		
Zero gate-voltage drain current	I_{DSS}	V_{DS} = 1200 V, V_{GS} = 0 V	T_{vj} = 25 °C		0.3	10	µA
			T_{vj} = 175 °C		0.9		
Gate leakage current	I_{GSS}	V_{DS} = 0 V	V_{GS} = 25 V			100	nA
			V_{GS} = -10 V			-100	
Forward transconductance	g_{fs}	I_D = 10 A, V_{DS} = 20 V			8		S
Short-circuit withstand time[2)]	t_{SC}	V_{DD} ≤ 800 V, $V_{DS,peak}$ < 1200 V, $T_{vj(start)}$ = 25 °C, $R_{G,ext}$ = 2 Ω	$V_{GS(on)}$ = 20 V		1.5		µs
			$V_{GS(on)}$ = 18 V		2		
			$V_{GS(on)}$ = 15 V		2.5		
Internal gate resistance	$R_{G,int}$	f = 1 MHz, V_{AC} = 25 mV			3.7		Ω
Input capacitance	C_{iss}	V_{DD} = 800 V, V_{GS} = 0 V, f = 100 kHz, V_{AC} = 25 mV			671		pF
Output capacitance	C_{oss}	V_{DD} = 800 V, V_{GS} = 0 V, f = 100 kHz, V_{AC} = 25 mV			35		pF
Reverse transfer capacitance	C_{rss}	V_{DD} = 800 V, V_{GS} = 0 V, f = 100 kHz, V_{AC} = 25 mV			2		pF
C_{oss} stored energy	E_{oss}	V_{DD} = 800 V, V_{GS} = 0 V, f = 100 kHz, V_{AC} = 25 mV			15		µJ
Total gate charge	Q_G	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, turn-on pulse			24		nC
Plateau gate charge	$Q_{GS(pl)}$	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, turn-on pulse			7		nC
Gate-to-drain charge	Q_{GD}	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, turn-on pulse			4		nC
Turn-on delay time	$t_{d(on)}$	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		5		ns
			T_{vj} = 175 °C		8		

(table continues...)

Table 4 (continued) Characteristic values

Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
Rise time	t_r	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		5		ns
			T_{vj} = 175 °C		7		
Turn-off delay time	$t_{d(off)}$	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		11		ns
			T_{vj} = 175 °C		11		
Fall time	t_f	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		10		ns
			T_{vj} = 175 °C		11		
Turn-on energy	E_{on}	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		56		µJ
			T_{vj} = 175 °C		66		
Turn-off energy	E_{off}	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		37		µJ
			T_{vj} = 175 °C		37		
Total switching energy	E_{tot}	V_{DD} = 800 V, I_D = 10 A, V_{GS} = 0/20 V, $R_{GS(on)}$ = 2 Ω, $R_{GS(off)}$ = 2 Ω, L_σ = 15 nH	T_{vj} = 25 °C		93		µJ
			T_{vj} = 175 °C		103		
Virtual junction temperature	T_{vj}			-55		175	°C

1) **Important note:** The selection of positive and negative gate-source voltages impacts the long-term behavior of the device. The design guidelines described in Application Note AN2018-09 must be considered to ensure sound operation of the device over the planned lifetime.

2) verified by the design/characterization.

Note: Characteristics at T_{vj} = 25°C, unless otherwise specified.

3 Body diode (MOSFET)

Table 5 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Drain-source voltage	V_{DSS}	T_{vj} = -55...175 °C		1200	V
Continuous reverse drain current for $R_{th(j-c,max)}$, limited by $T_{vj(max)}$	I_{SDC}	V_{GS} = 0 V	T_c = 25 °C	22	A
			T_c = 100 °C	17	

(table continues...)

Table 5 (continued) Maximum rated values

Parameter	Symbol	Note or test condition	Values	Unit
Peak reverse drain current, t_p limited by $T_{vj(max)}$	I_{SM}	V_{GS} = 0 V	22	A

Table 6 Characteristic values

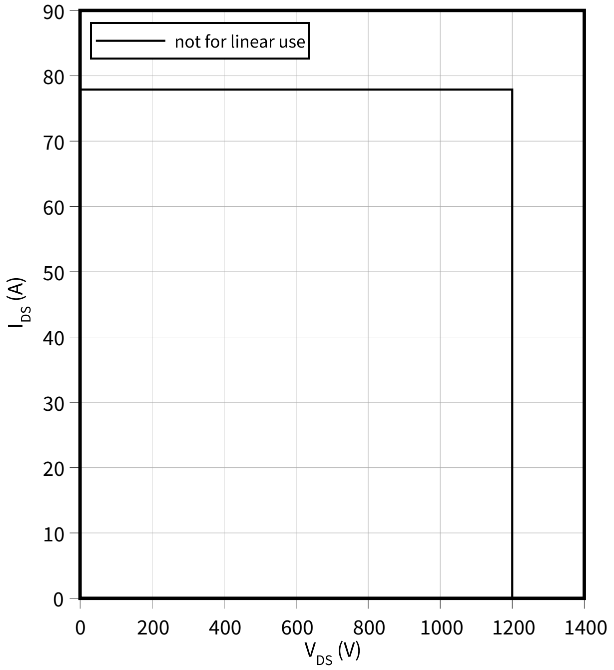
Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
Drain-source reverse voltage	V_{SD}	I_{SD} = 10 A, V_{GS} = 0 V	T_{vj} = 25 °C		3.9	5	V
			T_{vj} = 100 °C		3.8		
			T_{vj} = 175 °C		3.7		
MOSFET forward recovery charge	Q_{fr}	V_{DD} = 800 V, I_{SD} = 10 A, V_{GS} = 0 V, di_{SD}/dt = 3000 A/µs, Q_{fr} includes also Q_C	T_{vj} = 25 °C		85		nC
			T_{vj} = 175 °C		130		
MOSFET peak forward recovery current	I_{frm}	V_{DD} = 800 V, I_{SD} = 10 A, V_{GS} = 0 V, di_{SD}/dt = 3000 A/µs, Q_{fr} includes also Q_C	T_{vj} = 25 °C		10		A
			T_{vj} = 175 °C		12		
Virtual junction temperature	T_{vj}			-55		175	°C

4 Characteristics diagrams

Reverse bias safe operating area (RBSOA)

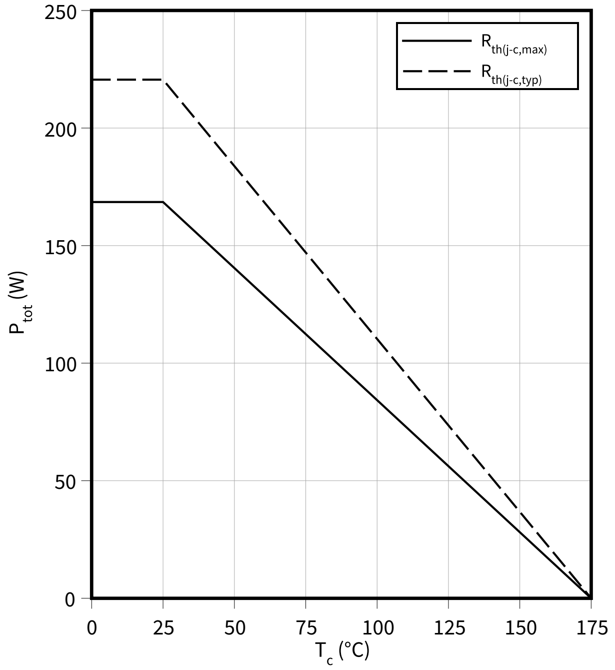
$I_{DS} = f(V_{DS})$

$T_{vj} \leq 175$ °C, $V_{GS} = 0/20$ V, $T_c = 25$ °C



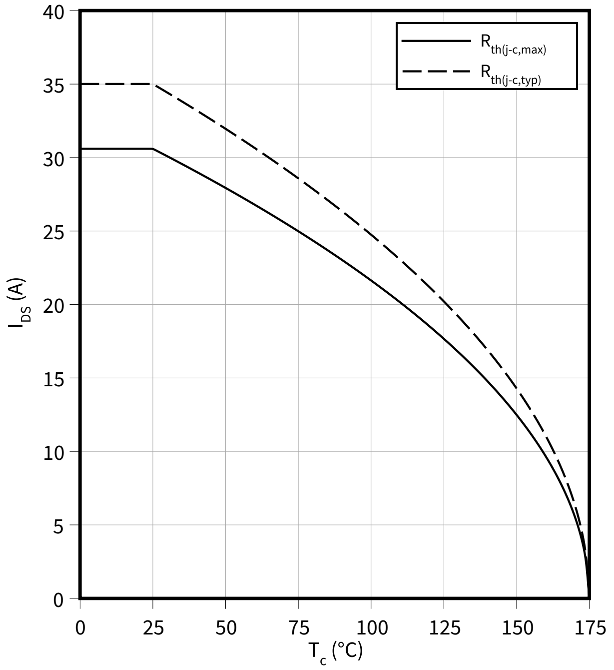
Power dissipation as a function of case temperature

$P_{tot} = f(T_c)$



Maximum DC drain to source current as a function of case temperature

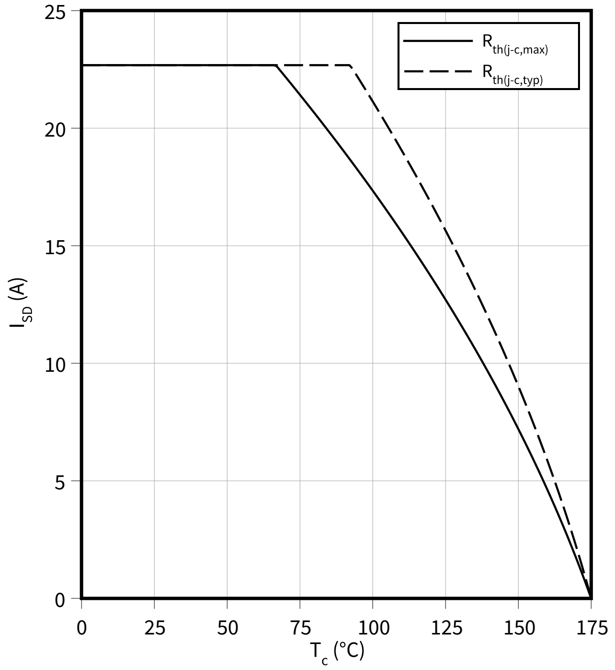
$I_{DS} = f(T_c)$



Maximum source to drain current as a function of case temperature

$I_{SD} = f(T_c)$

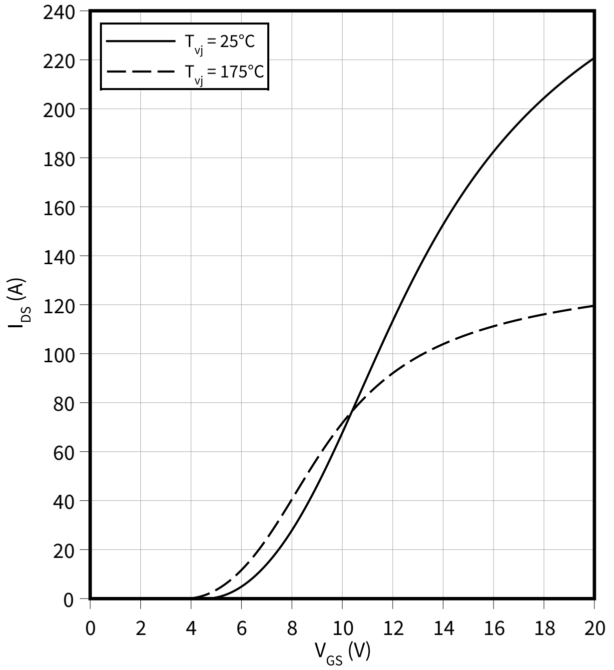
$V_{GS} = 0$ V



Typical transfer characteristic

$I_{DS} = f(V_{GS})$

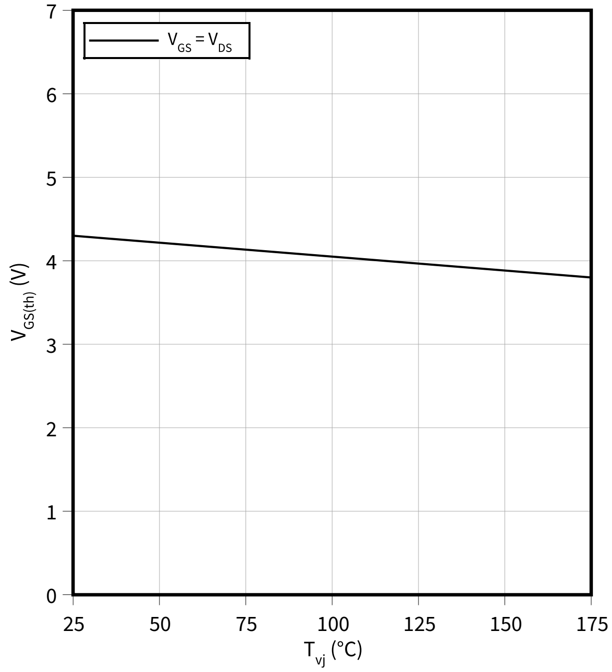
$V_{DS} = 20$ V



Typical gate-source threshold voltage as a function of junction temperature

$V_{GS(th)} = f(T_{vj})$

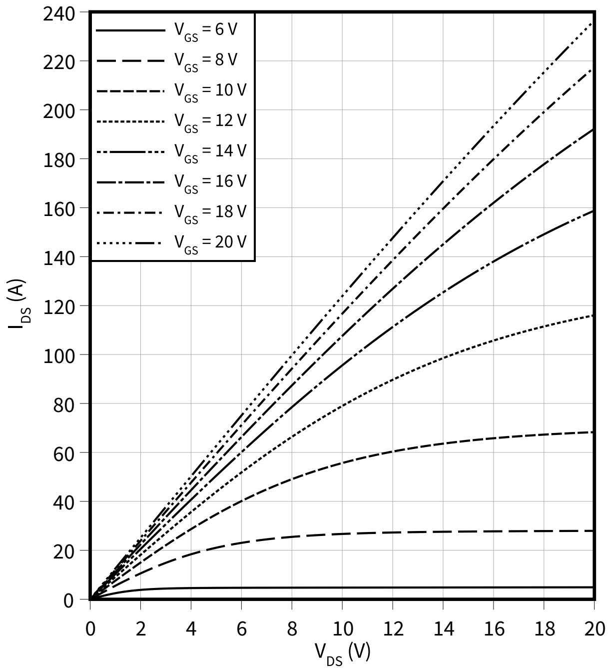
$I_D = 3.8$ mA



Typical output characteristic, V_{GS} as parameter

$I_{DS} = f(V_{DS})$

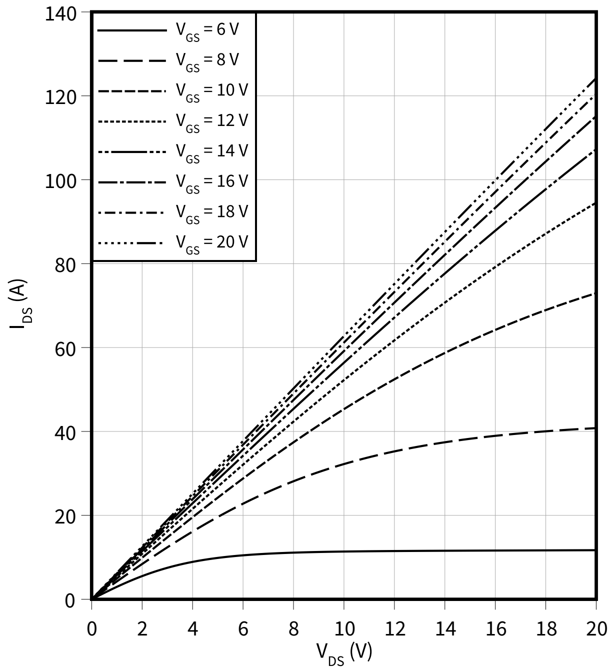
$T_{vj} = 25$ °C



Typical output characteristic, V_{GS} as parameter

$I_{DS} = f(V_{DS})$

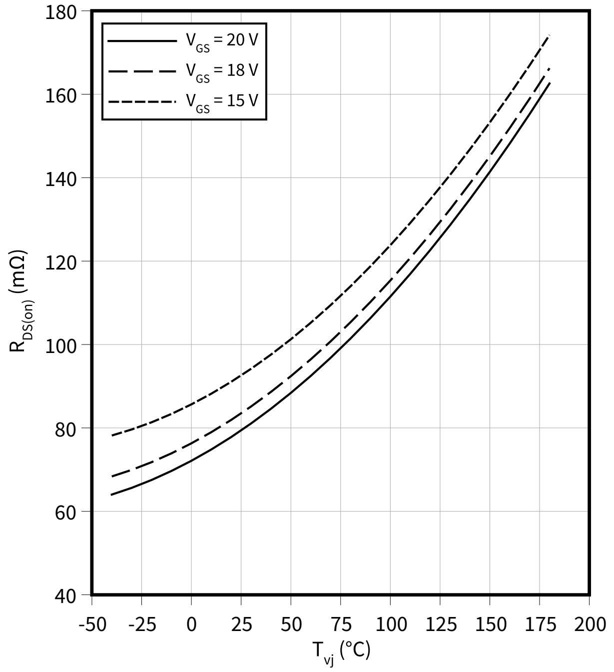
$T_{vj} = 175$ °C



Typical on-state resistance as a function of junction temperature

$R_{DS(on)} = f(T_{vj})$

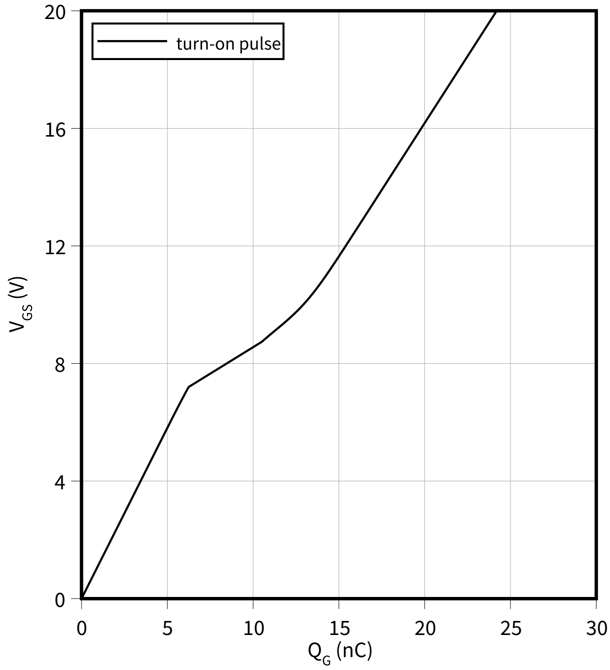
$I_D = 10$ A



Typical gate charge

$V_{GS} = f(Q_G)$

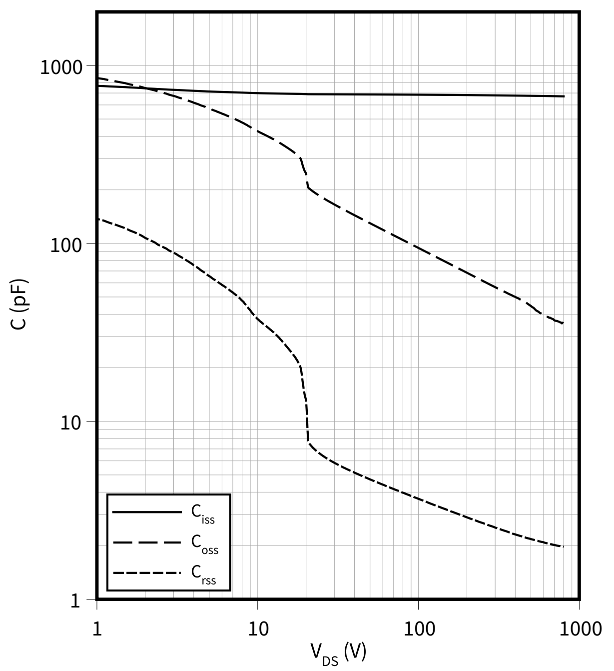
$I_D = 10$ A, $V_{DS} = 800$ V



Typical capacitance as a function of drain-source voltage

$C = f(V_{DS})$

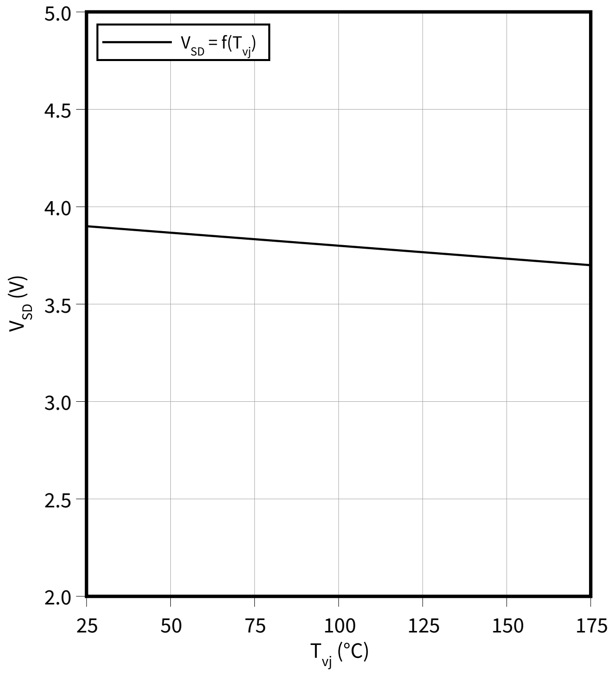
$f = 100$ kHz, $V_{GS} = 0$ V

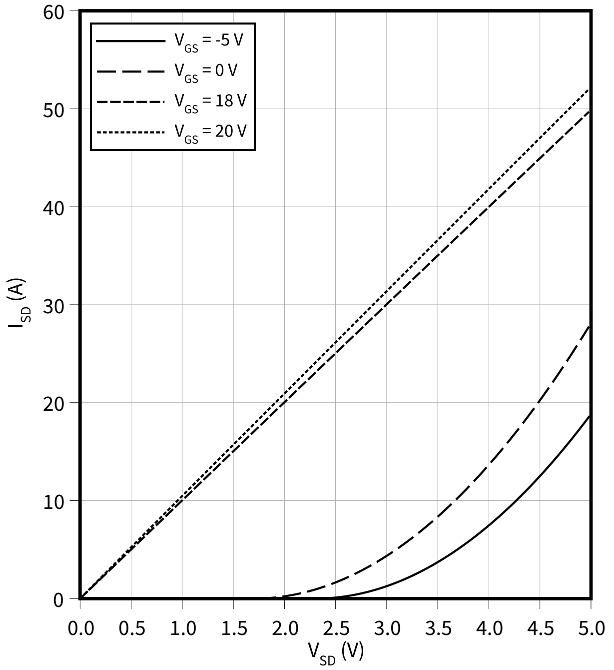
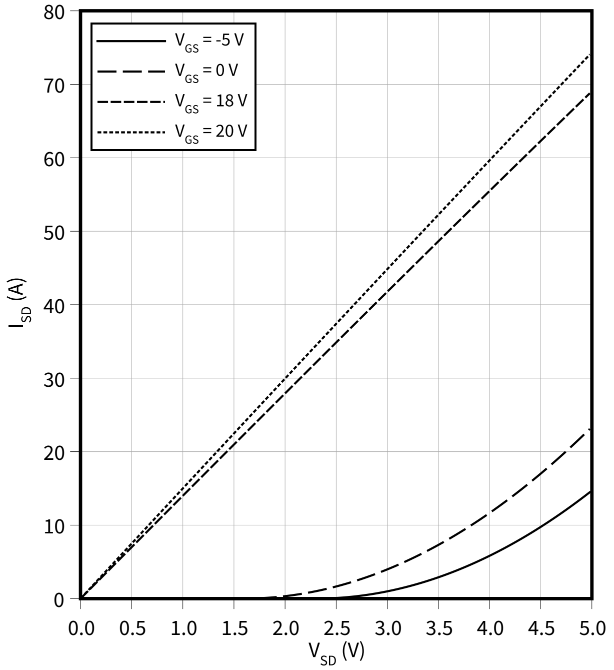
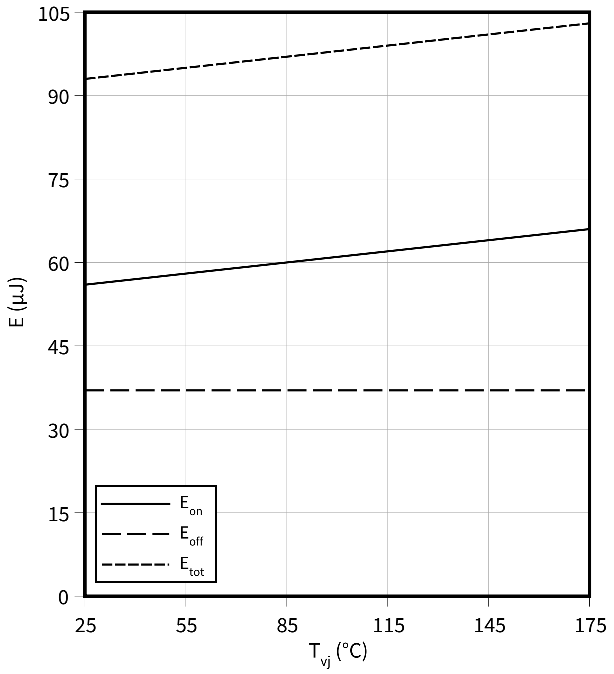
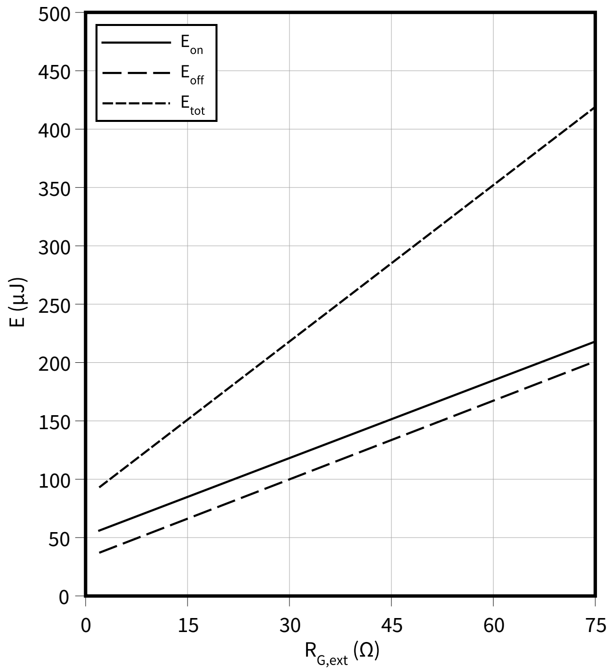


Typical reverse drain voltage as function of junction temperature

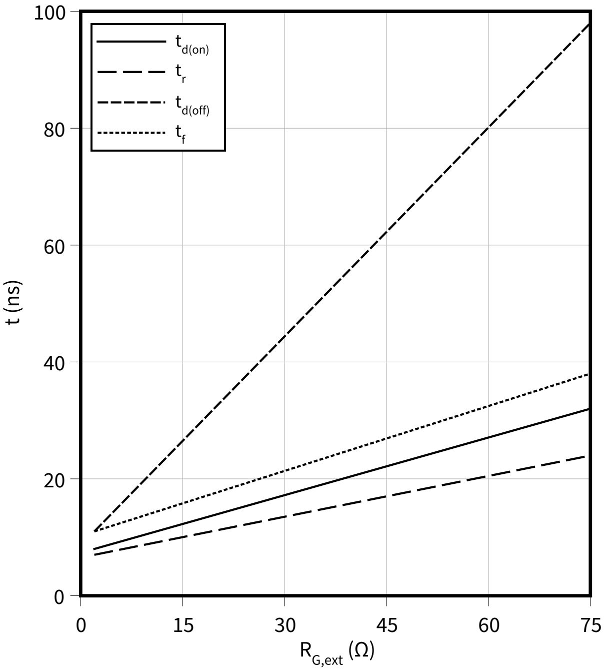
$V_{SD} = f(T_{vj})$

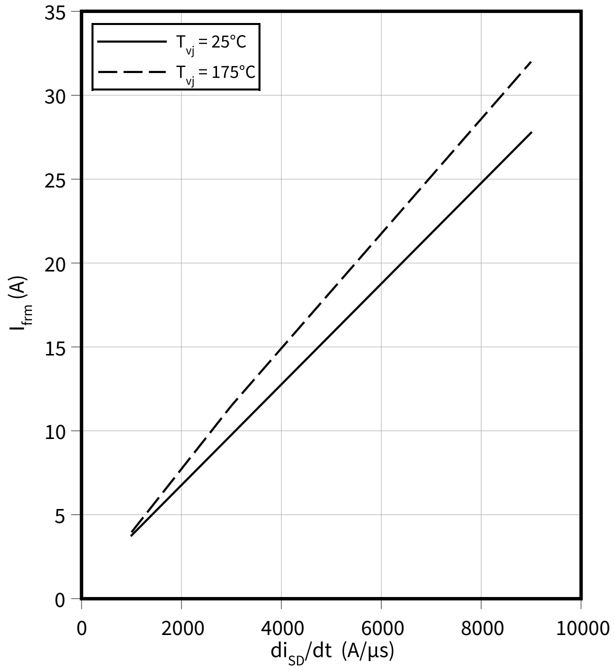
$I_{SD} = 10$ A, $V_{GS} = 0$ V



Typical reverse drain current as function of reverse drain voltage, V_{GS} as parameter $I_{SD} = f(V_{SD})$ $T_{vj} = 175\ °C$	**Typical reverse drain current as function of reverse drain voltage, V_{GS} as parameter** $I_{SD} = f(V_{SD})$ $T_{vj} = 25\ °C$
 	
Typical switching energy as a function of junction temperature, test circuit in Fig. F, 2nd device own body diode: V_{GS} = 0 V $E = f(T_{vj})$ $V_{GS} = 0/20\ V$, $I_D = 10\ A$, $R_{G,ext} = 2\ \Omega$, $V_{DD} = 800\ V$	**Typical switching energy as a function of gate resistance, test circuit in Fig. F, 2nd device own body diode: V_{GS} = 0 V** $E = f(R_{G,ext})$ $V_{GS} = 0/20\ V$, $I_D = 10\ A$, $T_{vj} = 175\ °C$, $V_{DD} = 800\ V$
 	

Typical switching times as a function of gate resistance, test circuit in Fig. F, 2nd device own body diode: $V_{GS} = 0$ V	**Typical reverse recovery current as a function of reverse drain current slope, test circuit in Fig. F, 2nd device own body diode: $V_{GS} = 0$ V**
$t = f(R_{G,ext})$	$I_{frm} = f(di_{SD}/dt)$
$V_{GS} = 0/20$ V, $I_D = 10$ A, $T_{vj} = 175$ °C, $V_{DD} = 800$ V	$V_{GS} = 0/20$ V, $I_{SD} = 10$ A, $V_{DD} = 800$ V

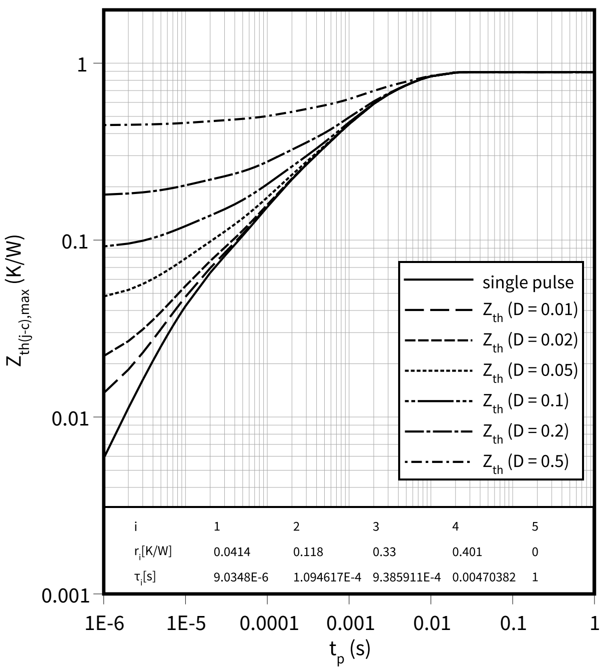




Max. transient thermal impedance (MOSFET/diode)

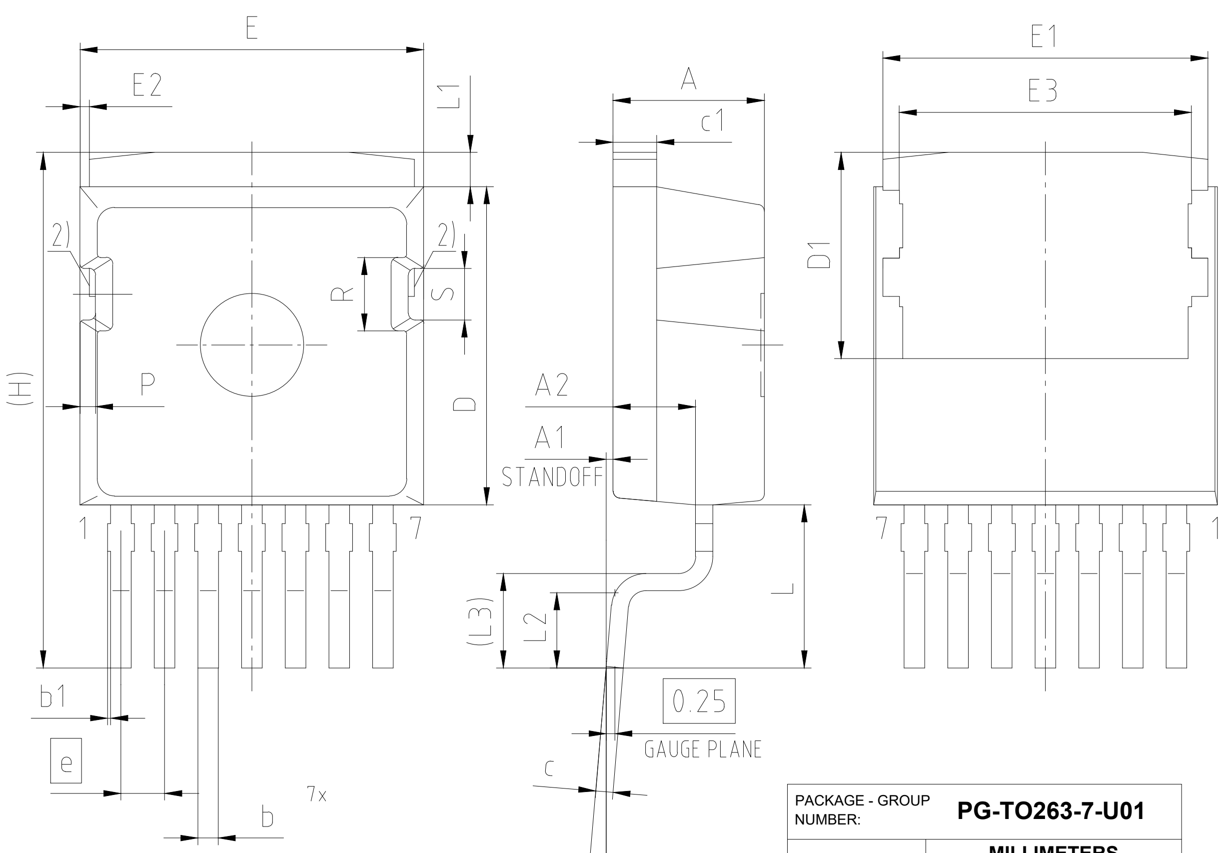
$Z_{th(j-c),max} = f(t_p)$

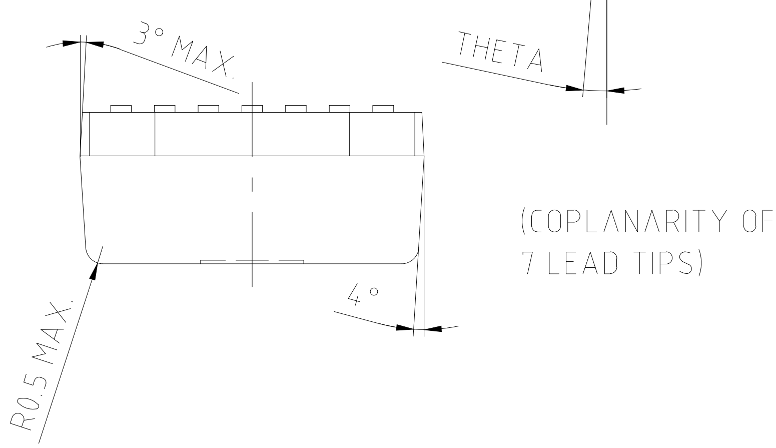
$D = t_p/T$



5 Package outlines

PG-TO263-7-HV-ND5.8





PACKAGE - GROUP NUMBER:	PG-TO263-7-U01	
DIMENSIONS	**MILLIMETERS**	
	MIN.	MAX.
A	4.30	4.50
A1	0.00	0.10
A2	2.30	2.50
b	0.50	0.70
b1	0.00	0.15
c	0.40	0.60
c1	1.17	1.37
D	9.05	9.45
D1	5.90	6.10
E	9.80	10.20
E1	9.36	9.56
E2	0.00	0.30
E3	8.40	8.60
e	1.27	
H	15.00	
L	4.20	5.20
L1	0.70	1.30
L2	1.70	2.30
L3	2.70	
P	0.35	0.55
R	2.03	2.23
S	1.40	1.60
THETA	0.00°	8.00°

NOTES:
1) ALL METAL SURFACES TIN PLATED EXCEPT AREA OF CUT
2) MOLD FILLING IS ACCEPTABLE AT THIS AREA

Figure 1

6 Testing conditions

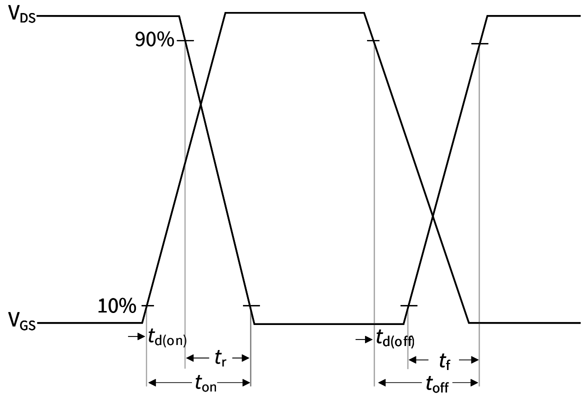


Figure A. **Definition of switching times**

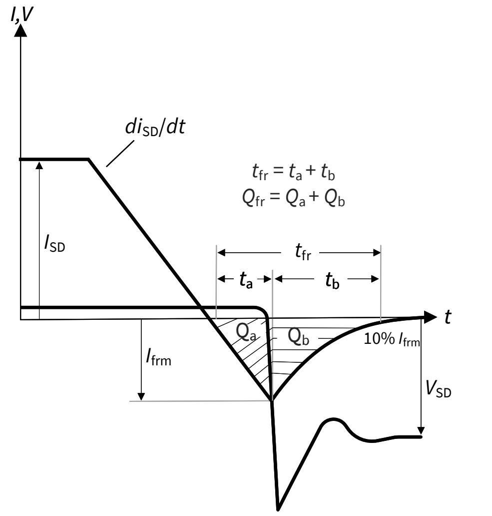


Figure B. **Definition of body diode switching characteristics**

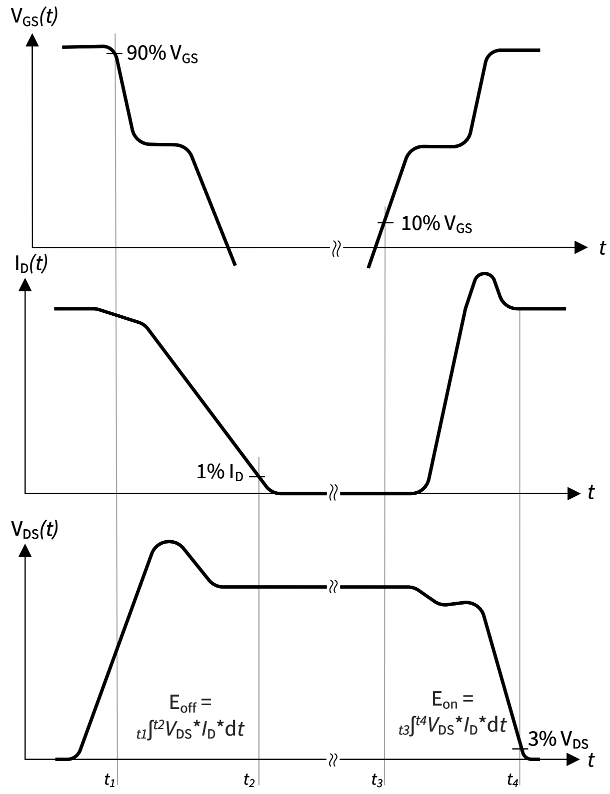


Figure C. **Definition of switching losses**

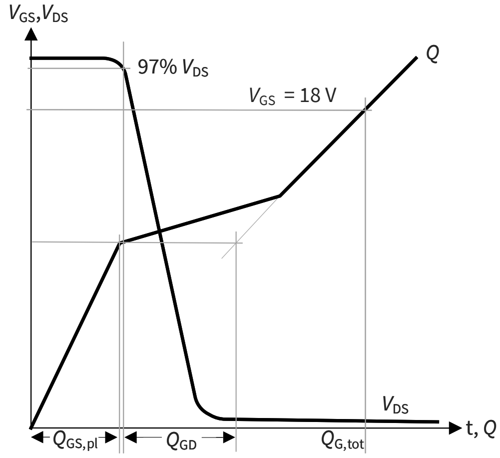


Figure D. **Definition of QGD**

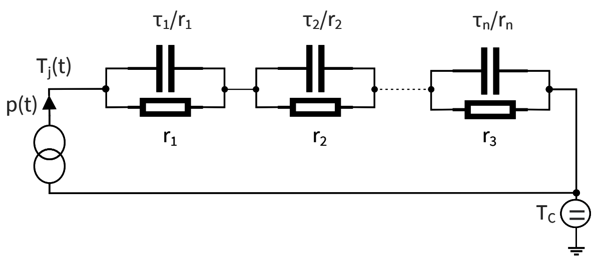


Figure E. **Thermal equivalent circuit**

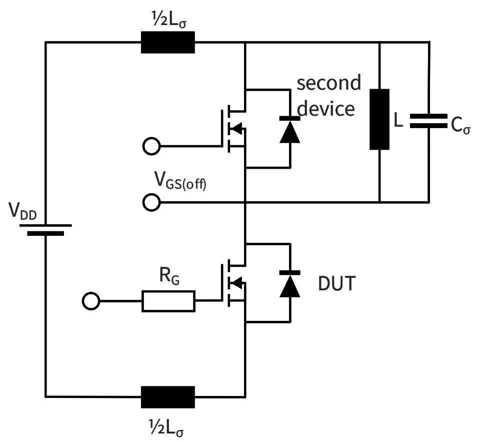


Figure F. **Dynamic test circuit**
Parasitic inductance Lσ,
Parasitic capacitor Cσ,

Figure 2

Revision history

Document revision	Date of release	Description of changes
0.10	2022-11-15	Preliminary datasheet
1.00	2023-04-25	Final datasheet

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