

Power line chokes

Rod core choke

1.8 μH , 1 MHz, 21 A / +125 °C

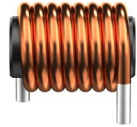
Series/Type: B82116B1224A010

Ordering code:

Date: September 2022

Rated current: 21 A / +125 °C

Rated Inductance: 1.8 μ H / 1 MHz



Construction

- Rod Core Choke
- Ferrite core
- Single layer winding
- Core and winding glued

Features

- High resonance frequency
- Enameled wire in accordance to EN 60317-13, Grade 1
- Wire class 200, UL listed
- Suitable for wave soldering or welding
- Qualified according to AEC-Q200
- RoHS compatible

Applications

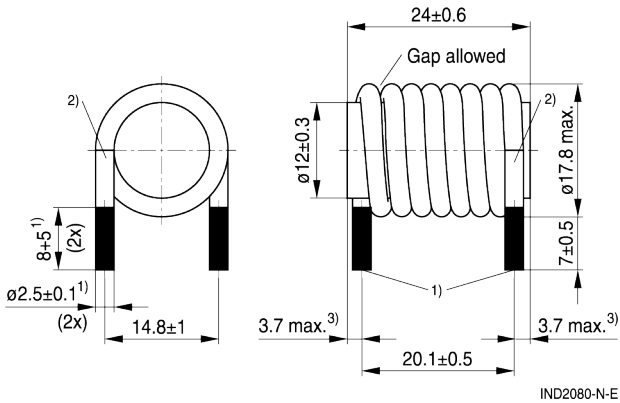
- EMC choke

Terminals

- Ends of winding wire
- Pins hot dip tinned Sn99Cu

Delivery mode

- Trays in cardboard box

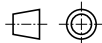
Dimensional drawing


- 1) Solderable tinned with Sn99Cu.
Bare copper wire above the tinned area is permissible.
Valid for both terminals.
- 2) At this position a little press mark on the first turn appears caused by wire holder of winding machine.
Additional holes in the wire coating may appear.
- 3) Core's position is not symmetrical to the coil.
Longitudinal axis of core is not central to the winding.

Part tolerances to ISO 2768-cL / ISO 8015.

Size ISO 14405 (E)

All dimensions in mm



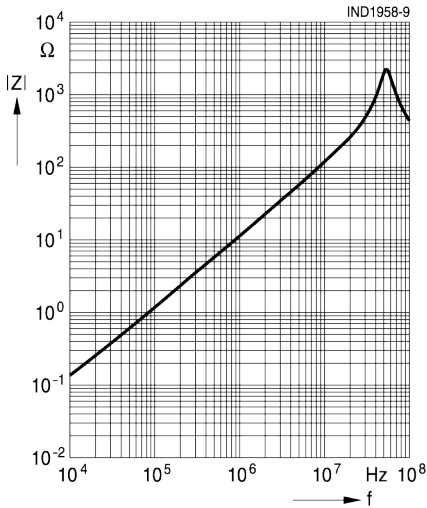
IND1276-L-E

Technical data and measuring conditions

Rated temperature T_R	+125 °C
Rated current I_R	21 A Referred to DC current and rated temperature.
Rated inductance L_R	1.8 μ H / 1 MHz Measured with Agilent 4284A, 1 mA, at +20 °C
Inductance $L_{(1\text{ kHz})}$ (only for information)	2.3 μ H / 1 kHz Measured with Agilent 4284A, 1 mA, at +20 °C
Inductance tolerance	\pm 20% at +20 °C
Inductance decrease $\Delta L/L_0$	\leq 10% at DC magnetic bias with 110 A, +20 °C
DC resistance R_{typ}	1.4 m Ω typ. value, measured at +20 °C
Operating temperature range	-40 °C ... +150 °C
Weight	Approx. 29 g
Wire insulation	Leaks in insulation of wire in accordance to EN 60317-0-1 allowed
Ferrite core surface irregularities	The standard IEC 60424-4 is the basis for the visual inspection of surface irregularities. These surface irregularities have no impact regarding function, manufacture ability and reliability of the component. No further spalling of core material permissible.

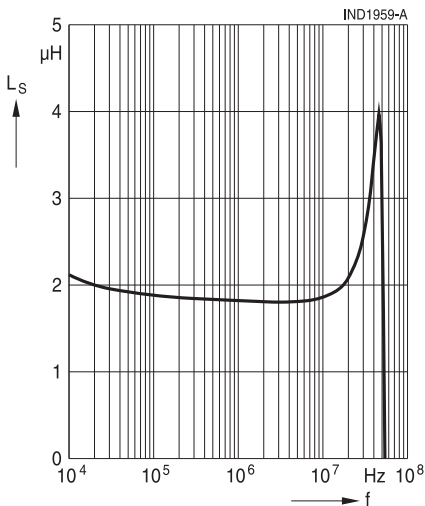
Impedance $|Z|$ versus frequency

(Typical values measured at +20 °C)



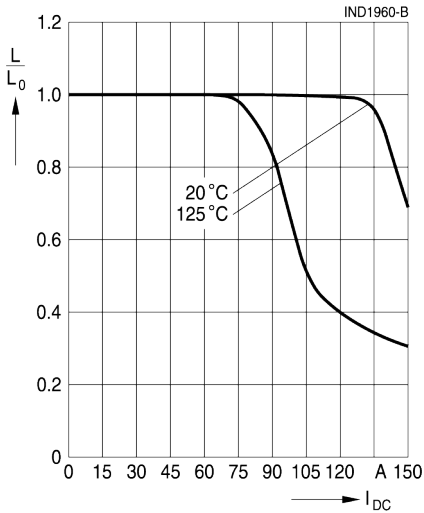
Inductance L_s versus frequency

(Typical values measured at +20 °C)



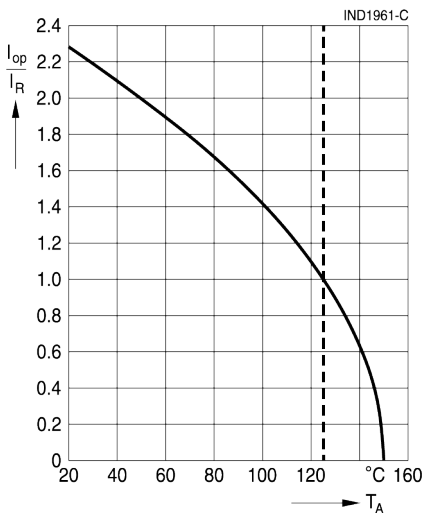
Inductance L/L_0 versus DC bias current I

(Typical values measured at +20 °C, +125 °C, measured with DC BIAS method)



Current derating I_{op}/I_R versus ambient temperature T_A

(Rated temperature $T_R = +125$ °C)



Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
 - Particular attention should be paid to the derating curves given there.
 - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation. Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
 - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
 - It is necessary to check whether the potting material used attacks or destroys the wire, wire insulation, plastics or glue.
 - The effect of the potting material can change the high-frequency behaviour of the components.
 - Many coating materials have a negative effect (chemically and mechanically) on the winding wires, insulation materials and connecting points. Customers are always obligated to determine whether and to what extent their coating materials influence the component. Customers are responsible and bear all risk for the use of the coating material. TDK Electronics does not assume any liability for failures of our components that are caused by the coating material.
- Ceramics / ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.
- Due to product design and applied manufacturing process, appearance, symmetry, and shape of not dimensioned details could vary within same lot, as well discoloration of housing is possible. TDK does not expect detrimental effects on product function or reliability. In case of conflicts, TDK reference standard shall prevail.

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1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
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3. **The warnings, cautions and product-specific notes must be observed.**
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Important notes

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