

HT8KB5

40V Nch+Nch Power MOSFET

V_{DSS}	40V
V DSS	TO V
R _{DS(on)} (Max.)	47mΩ
I _D	±12A
P_D	13W

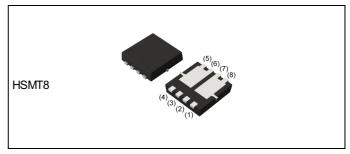
Features

- 1) Low on resistance
- 2) High Power small mold Package (HSMT8)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

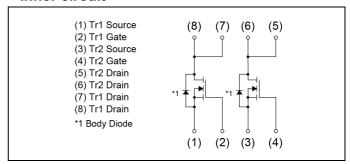
Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	TB1
	Marking	HT8KB5

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	40	V	
Continuous dusin suureent	T _c = 25°C	I _D *1	±12	Α
Continuous drain current	T _a = 25°C	I _D	±5	Α
Pulsed drain current	I _{DP} *2	±20	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche current, single pulse	l _{AS} *3	5.0	Α	
Avalanche energy, single pulse	E _{AS} *3	2.0	mJ	
		P _D *1	13	۱۸/
Power dissipation (total)		P _D *4	2.0	W
Junction temperature	T _j	150	°C	
Operating junction and storage ten	T _{stg}	-55 to +150	°C	

Thermal resistance

Doromotor	Cymbol	Values			l leit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	9.4	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Danamatan	0	0 - 1111 - 11		Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	40	-	-	V	
Breakdown voltage	ΔV _{(BR)DSS}	I _D = 1mA		28.9	_	mV/°C	
temperature coefficient	ΔT _j	referenced to 25°C	ı	20.9	-	IIIV/ C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V		-	1	μA	
Gate - Source leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V		-	±100	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 1mA$		-	2.5	V	
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = 1mA		-4.6		mV/°C	
temperature coefficient	ΔT _j	referenced to 25°C	-	-4.0	-	IIIV/ C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 5.0A	-	36	47	C	
on - state resistance	R _{DS(on)} *5	$V_{GS} = 4.5V, I_D = 5.0A$	-	58	81	mΩ	
Gate resistance	R _G	-	-	2.7	-	Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 5A	2.1	-	-	S	

^{*1} T_c = 25°C, Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Doromotor	Symbol	Conditions		Lloit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	150	-	
Output capacitance	C _{oss}	V _{DS} = 20V	-	85	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	10	-	
Turn - on delay time	t _{d(on)} *5	V _{DD} ≈ 20V,V _{GS} = 10V	-	5.0	-	
Rise time	t _r *5	I _D = 2.5A	-	4.7	-	
Turn - off delay time	t _{d(off)} *5	$R_L = 8.0\Omega$	-	11.0	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	2.7	-	

● Gate charge characteristics (T_a = 25°C) < Tr1 and Tr2>

Doromotor	Cumbal	Conditions -		Values			1 1:4	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Total mate about	O *5		V _{GS} = 10V	-	3.5	-		
Total gate charge	Q_g^{*5}	\mathbf{Q}_{g}	V _{DD} ≈ 20V		-	1.8	-	·- C
Gate - Source charge	Q _{gs} *5	I _D = 5.0A	V _{GS} = 4.5V	-	1.1	-	nC	
Gate - Drain charge	Q _{gd} *5			-	0.3	-		

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T = 25°C	-	1	1.67	^
Pulse forward current	I _{SP} *2	- T _a = 25°C	-	-	20	Α
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 5.0A, V _{GS} = 0V	-	18	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	11	-	nC

Fig.1 Power Dissipation Derating Curve

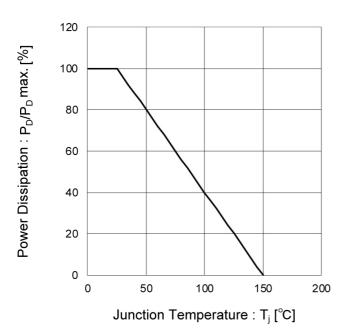


Fig.2 Maximum Safe Operating Area

100 Operation in this area is limited by R_{DS}(on) (V_d 10 $P_{W} = 100 \mu s$ DC Operation $P_W = 1 ms$ ШШ $P_W = 10 ms$ 0.1 T_a=25°C Single Pulse Mounted on a Cu board (40mm × 40mm × 0.8mm) 0.01 0.1

Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal
Resistance vs. Pulse Width

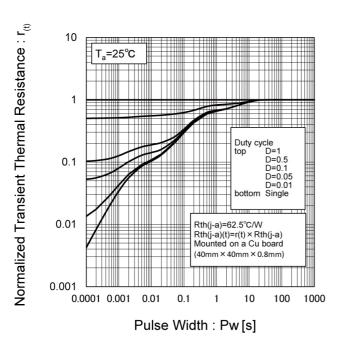
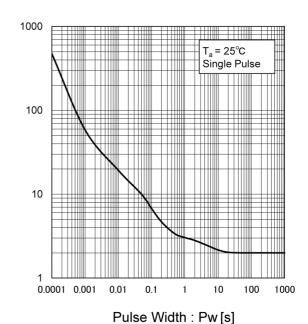


Fig.4 Single Pulse Maximum Power Dissipation

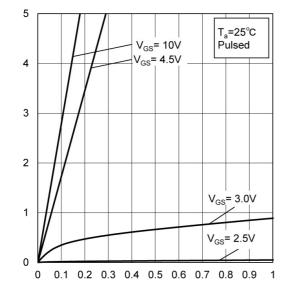


Peak Transient Power : P(W)

Drain Current : I_D [A]

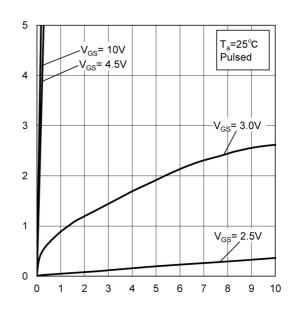
• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

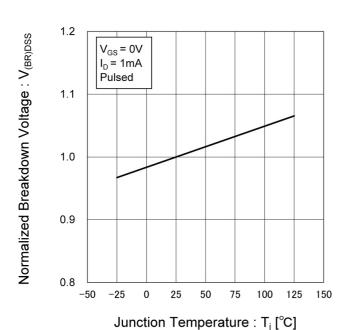


Fig.8 Typical Transfer Characteristics

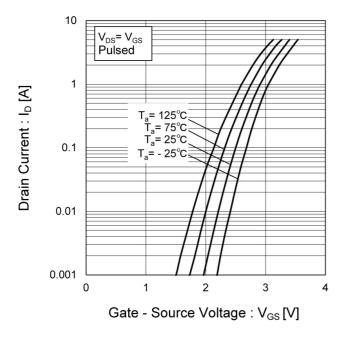


Fig.9 Gate Threshold Voltage vs. Junction Temperature

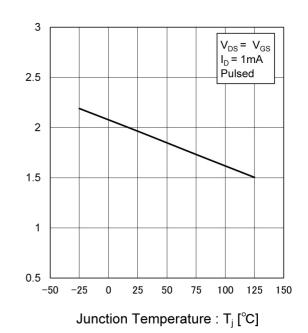
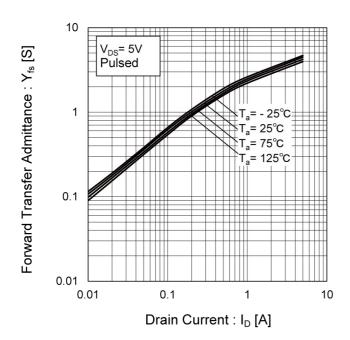


Fig.10 Forward Transfer Admittance vs. Drain Current



Gate Threshold Voltage: VGS(th) [V]

Fig.11 Drain Current Derating Curve

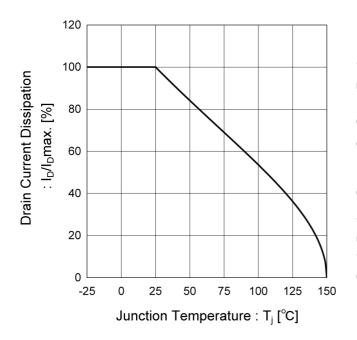


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

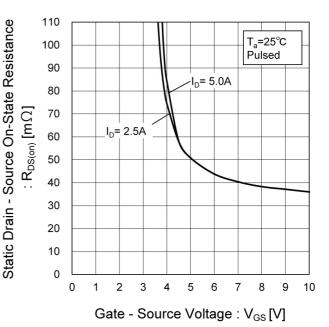


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

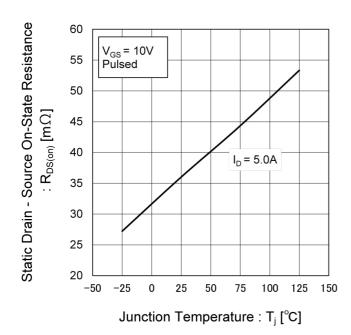


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

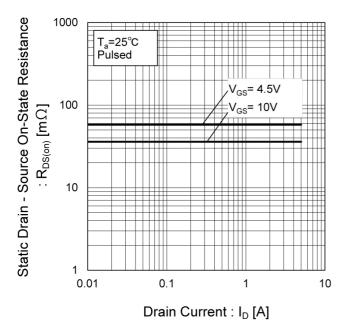


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

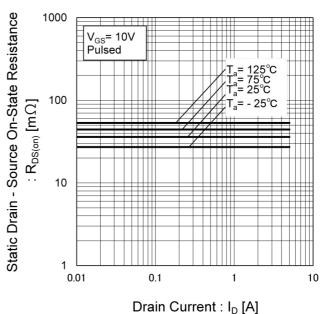


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

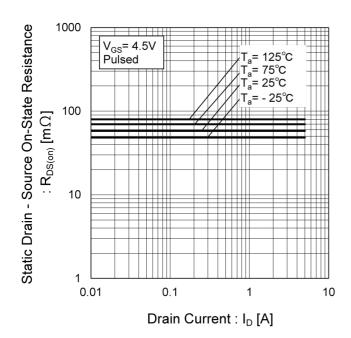


Fig.17 Typical Capacitances vs. Drain - Source Voltage

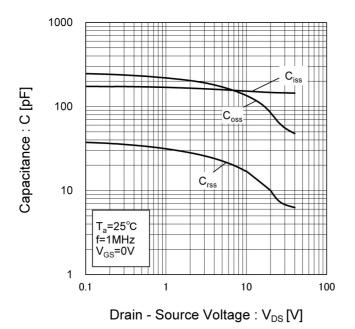


Fig.18 Switching Characteristics

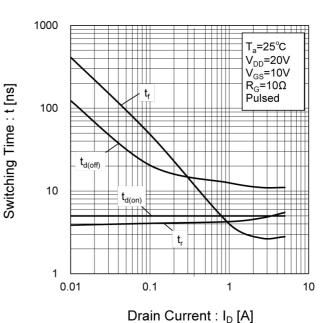


Fig.19 Typical Gate Charge

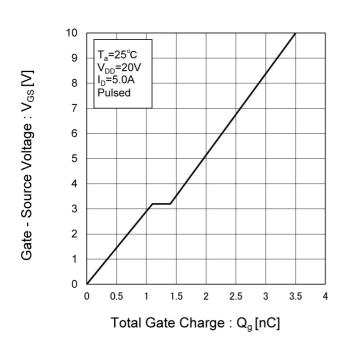
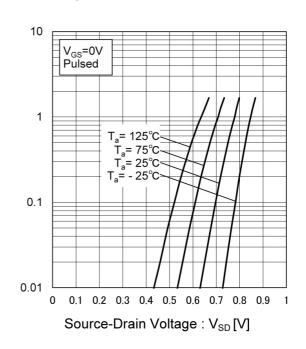


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

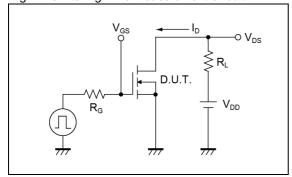


Fig.2-1 Gate Charge Measurement Circuit

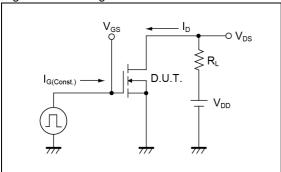


Fig.3-1 Avalanche Measurement Circuit

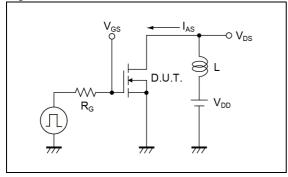


Fig.1-2 Switching Waveforms

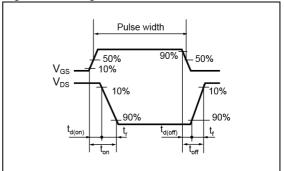


Fig.2-2 Gate Charge Waveform

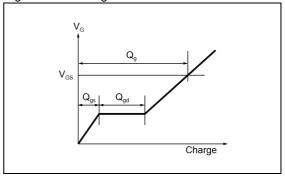
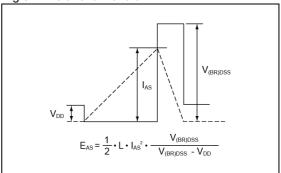


Fig.3-2 Avalanche Waveform

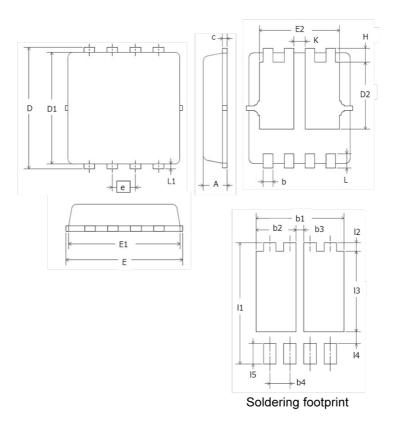


Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

HSMT8 (Dual)



DIM	Milim	eters	Inc	hes
	Min.	Max.	Min.	Max.
Α	0.70	0.80	0.028	0.031
b	0.25	0.35	0.010	0.014
С	0.10	0.25	0.004	0.010
D	3.25	3.45	0.128	0.136
D1	3.00	3.20	0.118	0.126
D2	1.78	1.98	0.070	0.078
E	3.20	3.40	0.126	0.134
E1	3.00	3.20	0.118	0.126
E2	2.39	2.59	0.094	0.102
е	0.0	65	0.0	26
Н	0.30	0.50	0.012	0.020
L	0.30	0.50	0.012	0.020
L1	0.	13	0.005	
K	0.30	-	0.012	-

DIM	Milimeters	Inches
DIIVI	Nom.	Nom.
l1	3.55	0.140
12	0.25	0.010
13	2.35	0.093
14	0.35	0.014
15	0.60	0.024
b1	2.83	0.111
b2	1.29	0.051
b3	0.25	0.010
b4	0.40	0.016

Dimension in mm / inches



Notice

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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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