PESD5V0S5UD-Q

Fivefold ESD protection diode array

3 July 2023 **Product data sheet**

1. General description

Fivefold ElectroStatic Discharge (ESD) protection diode array in a SOT457 (SC-74) small Surface-Mounted Device (SMD) plastic package, designed to protect up to five signal lines from the damage caused by ESD and other transients.

2. Features and benefits

- ESD protection of up to five lines
- Max peak pulse power: P_{PPM} = 200 W
- Ultra low leakage current: I_{RM} = 80 nA
- Low clamping voltage: V_{CL} = 13 V at I_{PPM} = 20 A
- ESD protection up to 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); I_{PPM} = 20 A
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Communication systems
- Portable electronics
- SIM card protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	T_{amb} = 25 °C		-	-	5	V
C_d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C		-	165	220	pF

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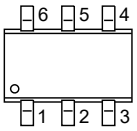
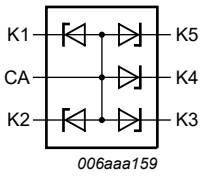
5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode 1	 SC-74; TSOP6 (SOT457)	
2	CA	common anode		
3	K2	cathode 2		
4	K3	cathode 3		
5	K4	cathode 4		
6	K5	cathode 5		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD5V0S5UD-Q	SC-74; TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD5V0S5UD-Q	E2

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P_{PPM}	rated peak pulse power	t_p = 8/20 µs	[1] [2]	-	200	W
I_{PPM}	rated peak pulse current		[1] [2]	-	20	A
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[3] [2]	-	30	kV
		IEC 61000-4-2; air discharge		-	15	kV
		MIL-STD-883; human body model (HBM)		-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[2] Measured from pin 1,3,4,5 or 6 to pin 2.
[3] Device stressed with ten non-repetitive ESD pulses.

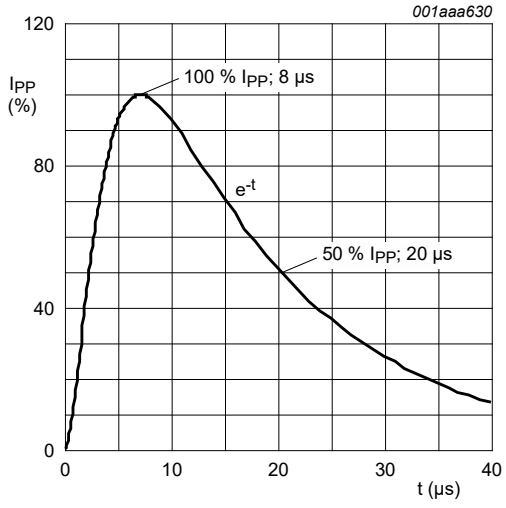


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

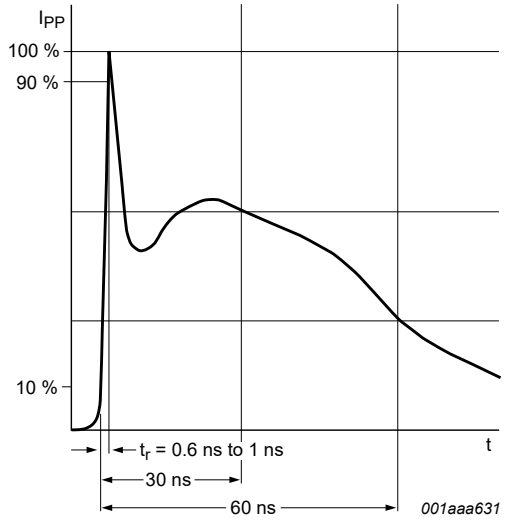


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	T_{amb} = 25 °C		-	-	5	V
V_{BR}	breakdown voltage	I_R = 1 mA; T_{amb} = 25 °C		6.4	6.8	7.2	V
I_{RM}	reverse leakage current	V_{RWM} = 5 V; T_{amb} = 25 °C		-	80	200	nA
C_d	diode capacitance	f = 1 MHz; V_R = 0 V; T_{amb} = 25 °C		-	165	220	pF
V_{CL}	clamping voltage	I_{PP} = 1 A; t_p = 8/20 µs; T_{amb} = 25 °C	[1] [2]	-	-	8	V
		I_{PPM} = 20 A; t_p = 8/20 µs; T_{amb} = 25 °C	[1] [2]	-	-	13	V
R_{diff}	differential resistance	I_R = 5 mA; T_{amb} = 25 °C		-	-	25	Ω

[1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[2] Measured from pin 1,3,4,5 or 6 to pin 2.

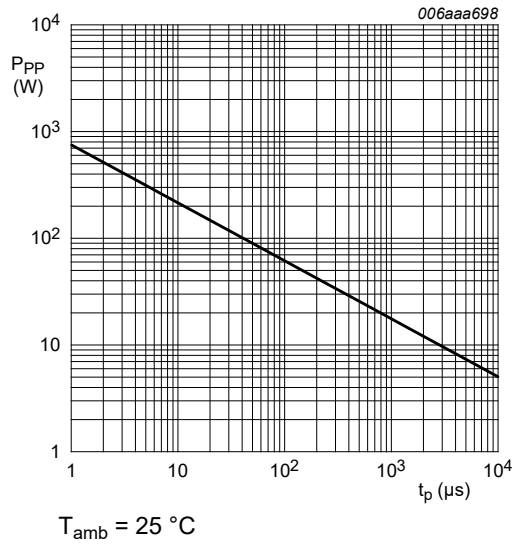


Fig. 3. Peak pulse power as a function of exponential pulse duration; typical values

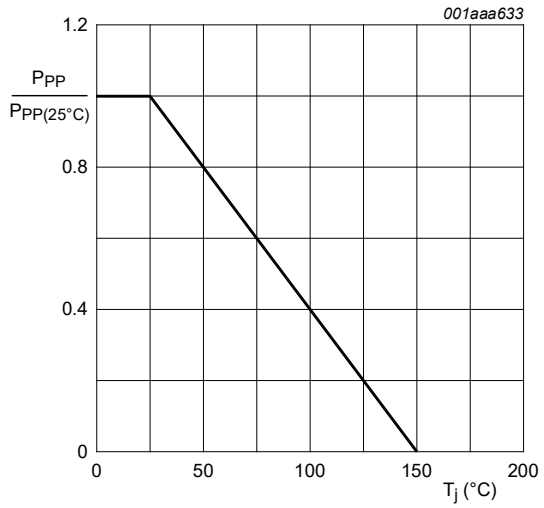
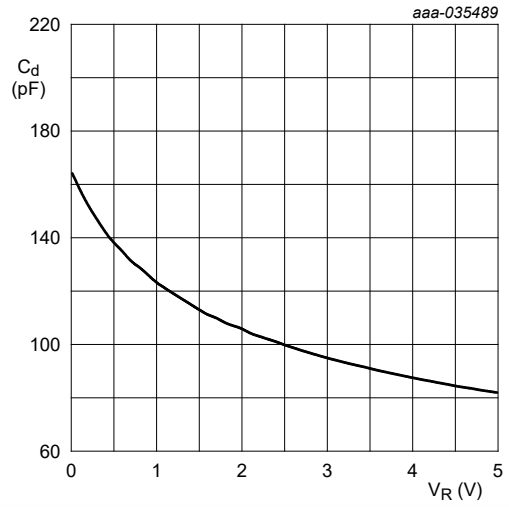


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values



T_{amb} = 25 °C

Fig. 5. Diode capacitance as a function or reverse voltage; typical values

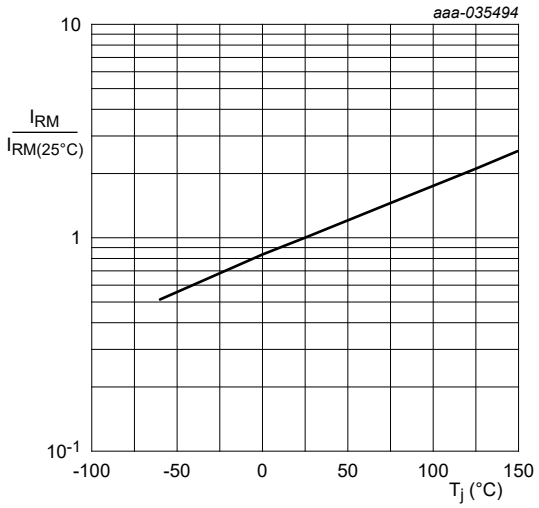


Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

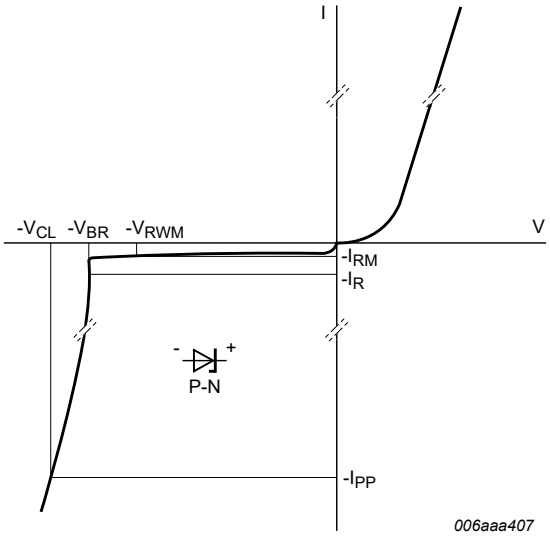


Fig. 7. V-I characteristics for a unidirectional ESD protection diode

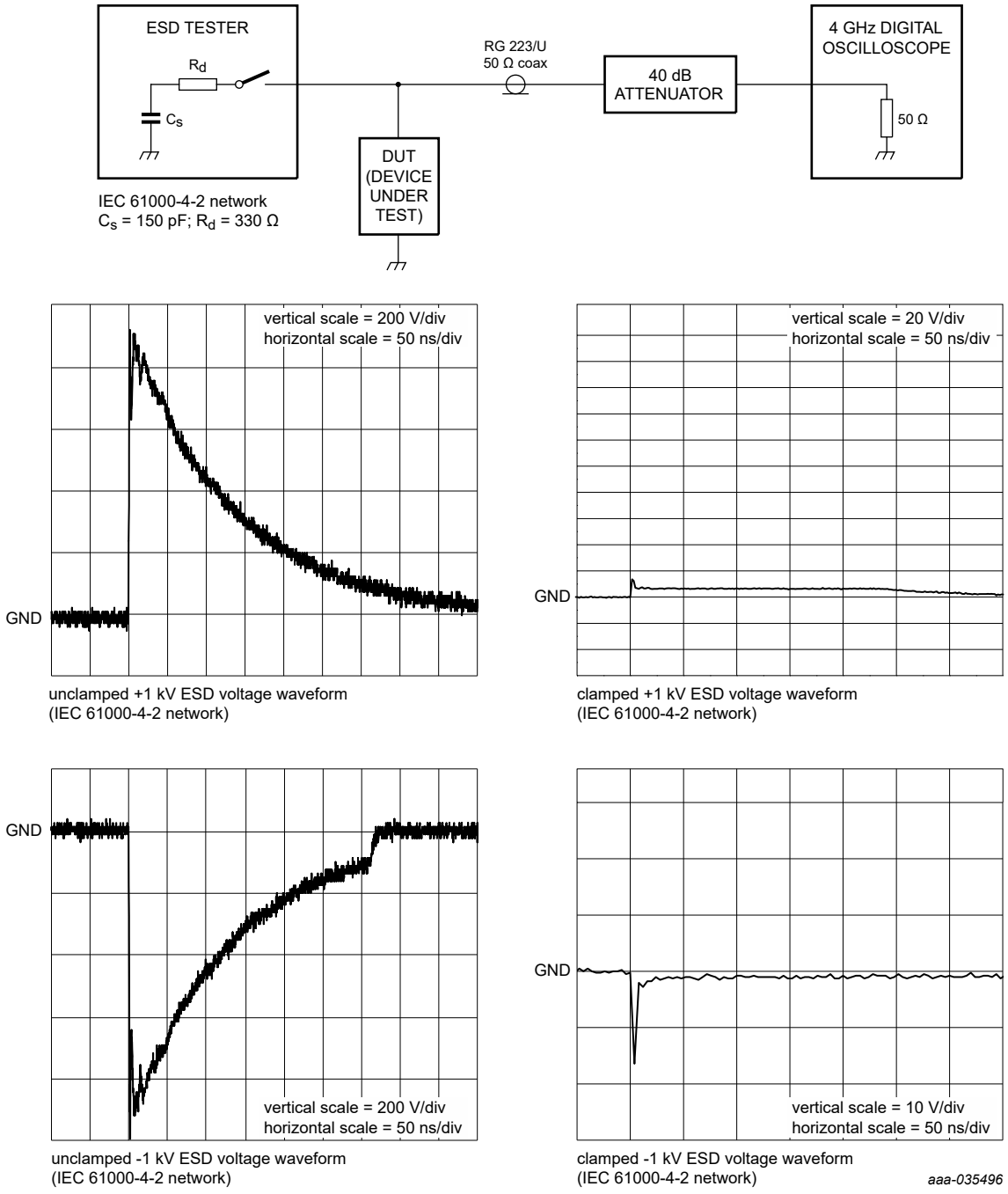


Fig. 8. ESD clamping test setup and waveforms

10. Application information

This device is designed for the protection of up to five unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are both, positive and negative with respect to ground.

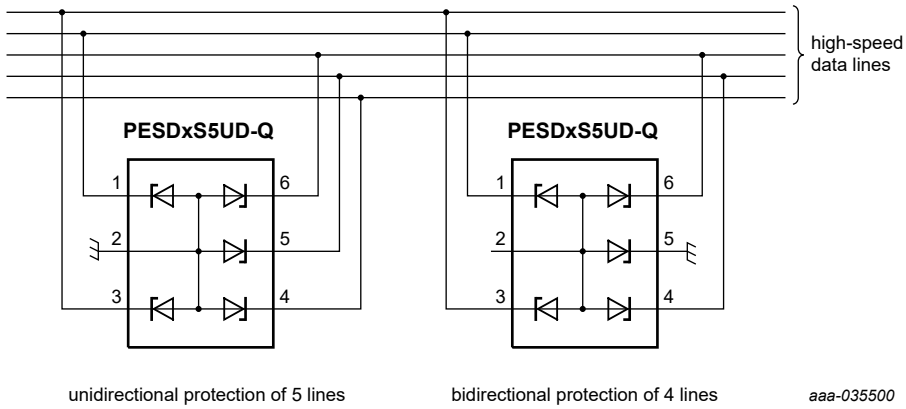


Fig. 9. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

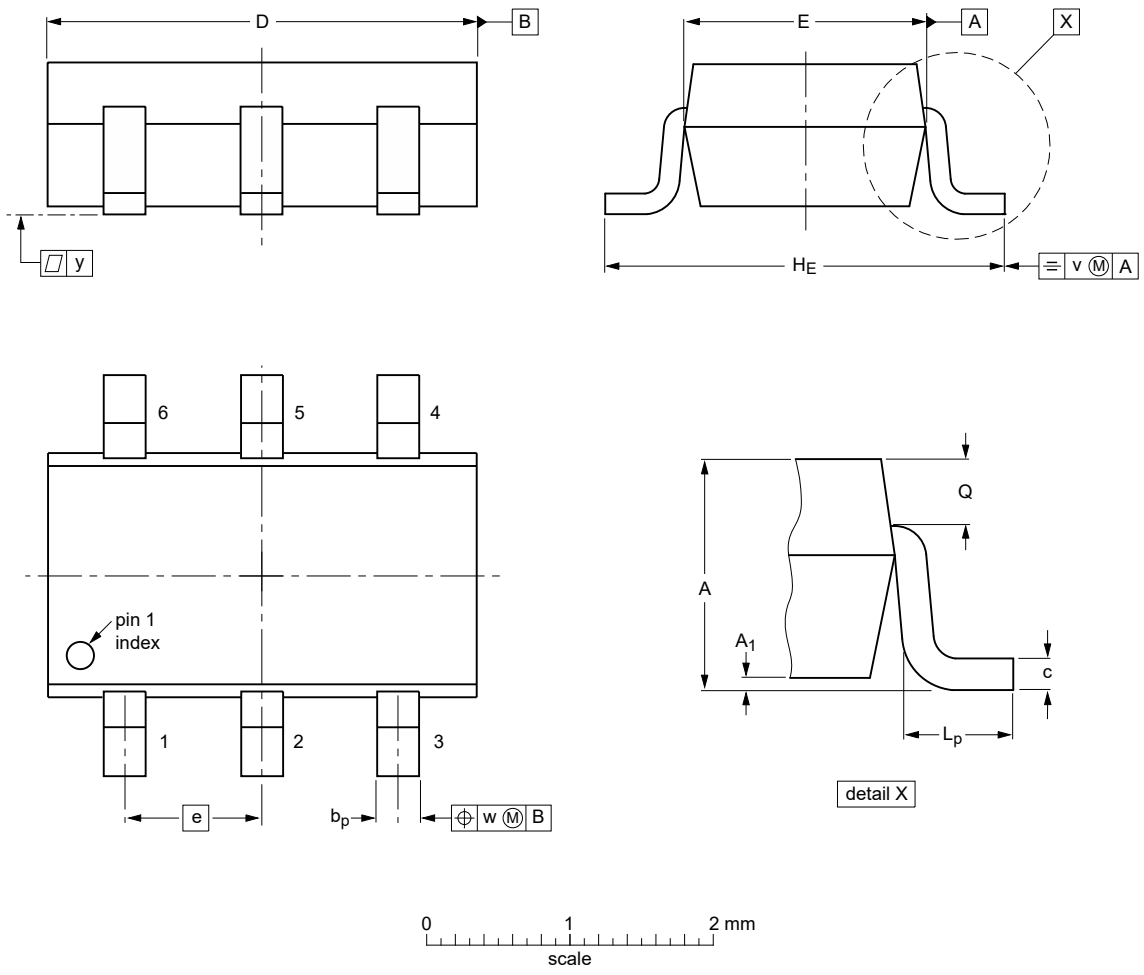
11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

Plastic, surface-mounted package (SC-74; TSOP6); 6 leads **SOT457**



Dimensions (mm are the original dimensions)

Unit		A	A_1	b_p	c	D	E	e	H_E	L_p	Q	v	w	y
mm	max	1.1	0.1	0.40	0.26	3.1	1.7		3.0	0.6	0.33	0.2	0.2	0.1
	nom							0.95						
	min	0.9	0.013	0.25	0.10	2.7	1.3		2.5	0.2	0.23			

sot457_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT457			SC-74			~~06-03-16~~ 18-11-27

Fig. 10. Package outline SC-74; TSOP6 (SOT457)

13. Soldering

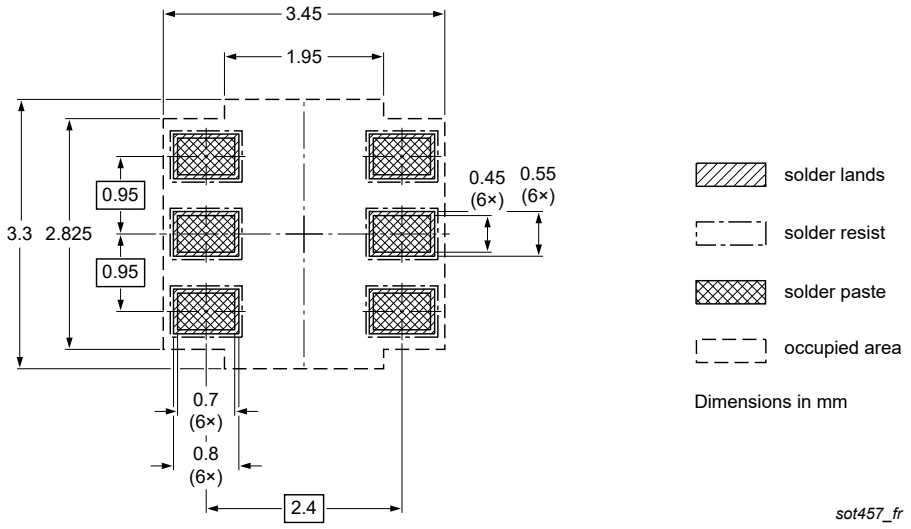


Fig. 11. Reflow soldering footprint for SC-74; TSOP6 (SOT457)

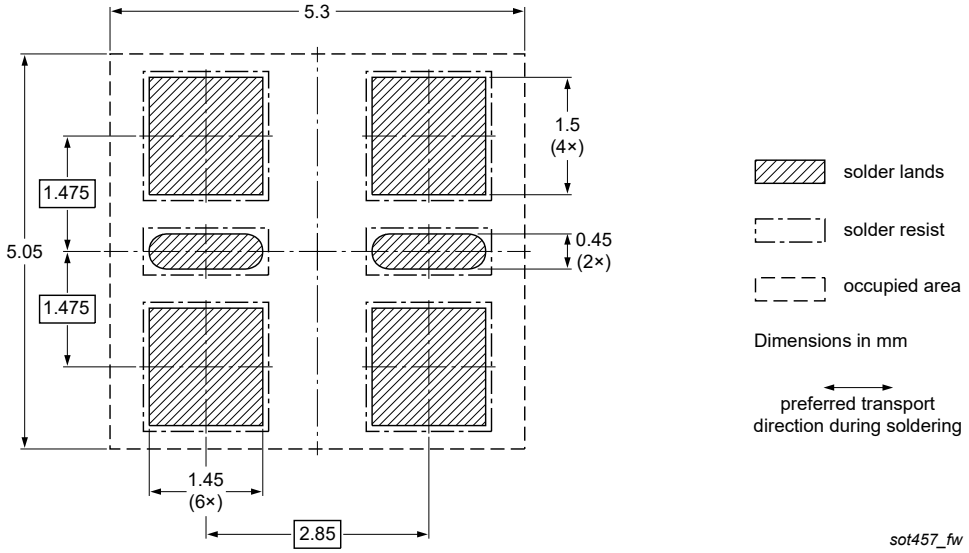


Fig. 12. Wave soldering footprint for SC-74; TSOP6 (SOT457)

14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0S5UD-Q v.1	20230703	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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