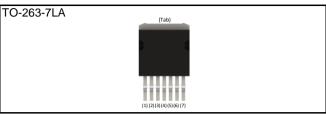


SCT4045DWAHR

Automotive Grade N-channel SiC power MOSFET

V _{DSS}	750V
R _{DS(on)} (Typ.)	45mΩ
Ι _D ^{*1}	31A
P _D	93W

Outline



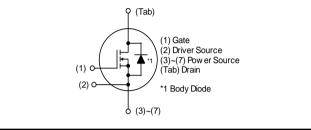
Features

- 1) Qualified to AEC-Q101
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Fast reverse recovery
- 5) Easy to parallel
- 6) Simple to drive
- 7) Pb-free lead plating ; RoHS compliant
- 8) Wide creepage distance = min.4.7 mm

Application

- Automobile
- Switch mode power supplies

Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

Packaging specifications

	Packing	Embossed tape
	Reel size (mm)	330
Tuno	Tape width (mm)	24
Туре	Basic ordering unit (pcs)	1000
	Taping code	TL
	Marking	SCT4045DWA

•Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified.)

	=,				
Parameter		Symbol	Value	Unit	
Drain - source voltage		V _{DSS}	750	V	
Continuous drain		$T_c = 25^{\circ}C$	ı ı *1	31	Α
and source current	$V_{GS} = V_{GS_{on}}$	$T_c = 100^{\circ}C$	Ι _D , Ι _S ^{*1}	22	Α
Pulsed drain current	$V_{GS} = V_{GS_{on}}$	$T_c = 25^{\circ}C$	I _{D,pulse} *2	61	Α
Body diode pulsed forward current $T_c = 25^{\circ}C$		T _c = 25°C	I _{S,pulse} *1,*3	31	Α
Body diode surge forward current		$V_{GS} = 0 V$	1,*4 ^{*1,*4}	61	Α
Gate - source voltage (DC)		V _{GSS_DC}	-4 to +21	V	
Gate - source surge voltage (t _{surge} < 300ns)		V_{GSS_surge} *5	-4 to +23	V	
Recommended turn-on gate - source drive voltage		V _{GS_on} *6	+15 to +18	V	
Recommended turn-off gate - source drive voltage		V _{GS_off}	0	V	
Virtual junction temperature		Τ _{vj}	175	°C	
Range of storage temperature		T _{stg}	-40 to +175	°C	

•Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

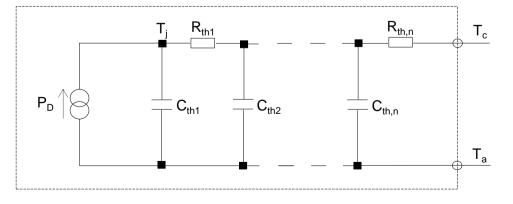
Deremeter	Symbol Conditions -		Values			L Incit	
Parameter			Min.	Тур.	Max.	Unit	
Drain - Source breakdown	V	$V_{GS} = 0 V, I_{D} = 5.3 mA$				V	
voltage	v (BR)DSS	$T_{vj} = 25^{\circ}C$	750	-	-	V	
		$V_{GS} = 0 V, V_{DS} = 750V$					
Zero Gate voltage Drain current	I _{DSS}	T _{vj} = 25°C	-	1	80	μA	
		T _{vj} = 150°C	-	10	-		
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V$, $V_{DS} = 0V$	-	-	100	nA	
Gate - Source leakage current		$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	$V_{GS(th)}{}^{*7}$	$V_{DS} = 10V, I_{D} = 8.89mA$	2.8	-	4.8	V	
		$V_{GS} = 18V, I_{D} = 17A$					
Static Drain - Source on - state resistance	${\sf R}_{\sf DS(on)}$ *8	T _{vj} = 25°C	-	45	59	mΩ	
		T _{vj} = 150°C	-	77	-		
Gate input resistance	R _G	f = 1MHz, open drain	-	4	-	Ω	

Thermal resistance

Parameter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	${\sf R_{thJC}}^{*9}$	-	1.2	1.6	K/W

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	8.9 ×10 ⁻²		C _{th1}	5.3 ×10 ⁻⁴	
R _{th2}	5.7 ×10 ⁻¹	K/W	C _{th2}	2.8 ×10 ⁻³	Ws/K
R _{th3}	5.3 ×10 ⁻¹		C _{th3}	1.5 ×10 ⁻¹	



•Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

Doromotor	Symbol	Symbol Conditions -		Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Transconductance	g _{fs} *8	$V_{DS} = 10V, I_{D} = 17A$	-	9.3	-	S	
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	1460	-		
Output capacitance	C_{oss}	V _{DS} = 500V	-	69	-	pF	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	5	-		
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0V$ $V_{DS} = 0V$ to 500V	-	90	-	pF	
Total Gate charge	Q_g^{*8}	V _{DS} = 500V I _D = 17A	-	63	-		
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	-	14	-	nC	
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	19	-		
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 500V$ $I_{D} = 17A$	-	5.1	-		
Rise time	t _r *8	V _{GS} = +18V / 0V	-	16	-		
Turn - off delay time	t _{d(off)} *8	$R_G = 3.3\Omega$, L = 250µH E _{on} includes diode	-	27	-	ns	
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	10	-		
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	112	-		
Turn - off switching loss	E _{off} *8		-	17	-	μJ	

•Body diode electrical characteristics (Source-Drain) (T_{vi} = 25°C unless otherwise specified)

Parameter	Symbol Conditions		Values			Unit	
Farameter	Зушоо	Conditions	Min.	Тур.	Max.	Unit	
Forward voltage	V_{SD}^{*8}	$V_{GS} = 0V, I_S = 17A$	-	3.3	-	V	
Reverse recovery time	t _{rr} *8	$I_F = 17A$ $V_R = 500V$	-	9.3	-	ns	
Reverse recovery charge	Q _{rr} *8	di/dt = 2900A/µs	-	89	-	nC	
Peak reverse recovery current	: I _{rrm} *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	19	-	А	

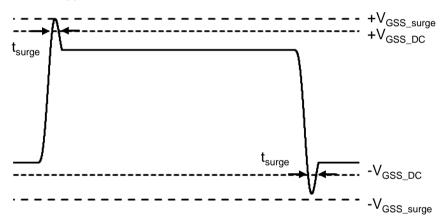
*1 Limited by maximum T_{vj} and for Max. R_{thJC} .

*2 Pulse width and duty cycle are limited by $T_{\nu j,\text{max}}$

*3 Only for body-diode, Repititive pulse, PW \leq 1.5µs, Duty cycle \leq 5%

*4 When used as a protective function, PW \leq 10 μs

*5 Example of acceptable V_{GS} waveform



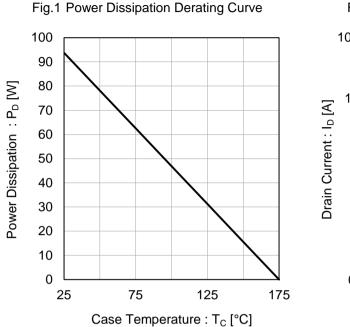
- *6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying V_{GS} = 21V for 100ms.

*8 Pulsed

*9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

 ${\tt URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf}$



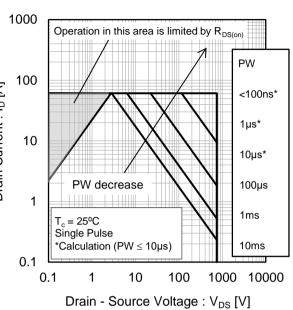
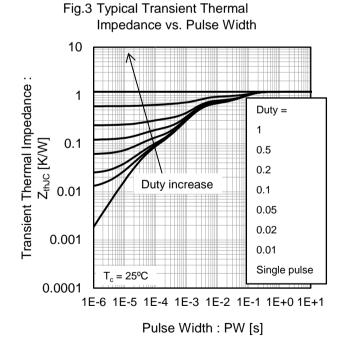
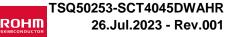


Fig.2 Maximum Safe Operating Area





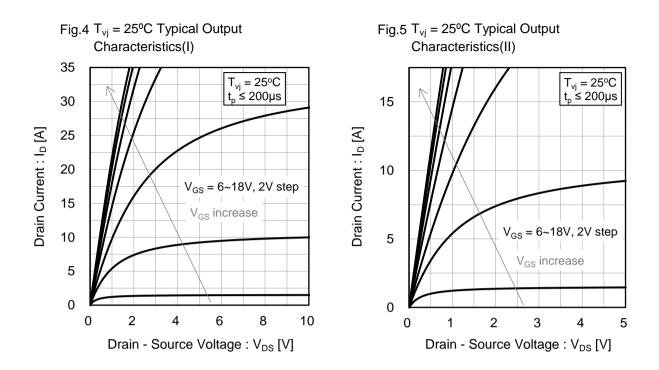
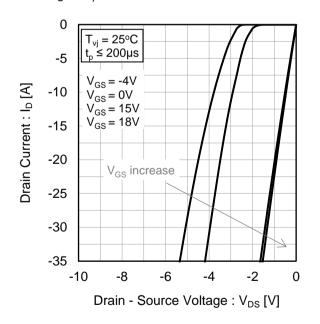
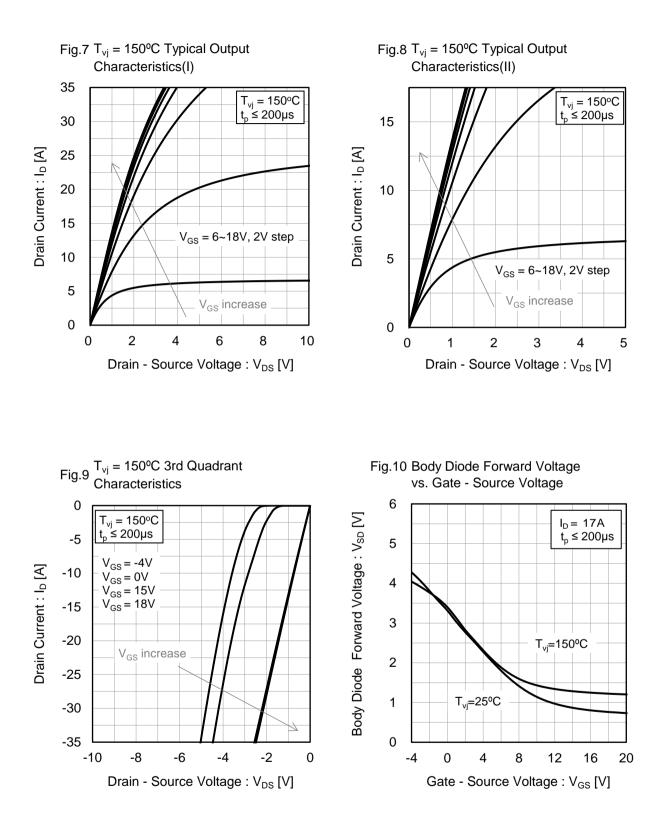


Fig.6 $T_{vj} = 25^{\circ}C$ 3rd Quadrant Characteristics



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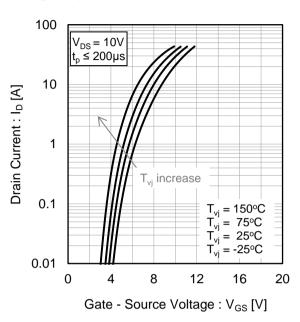
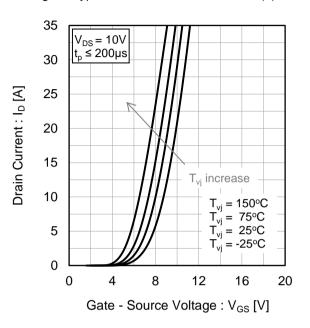


Fig.11 Typical Transfer Characteristics (I)

Fig.12 Typical Transfer Characteristics (II)



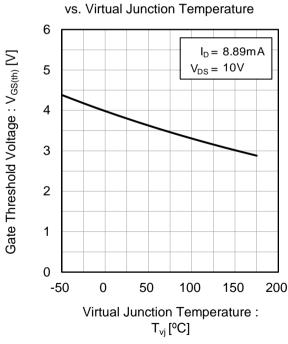
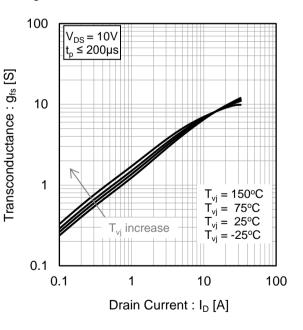
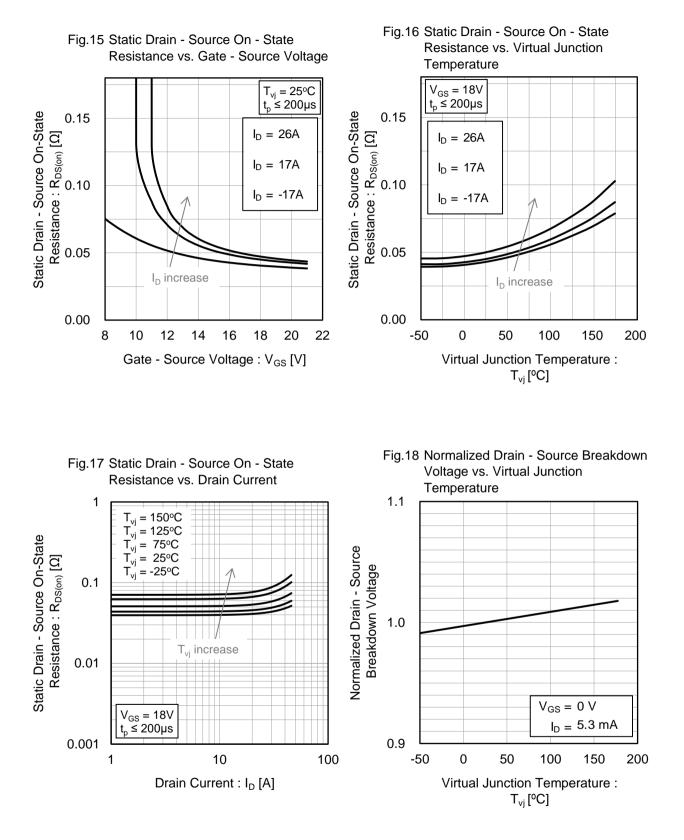
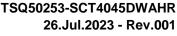


Fig.13 Gate Threshold Voltage

Fig.14 Transconductance vs. Drain Current







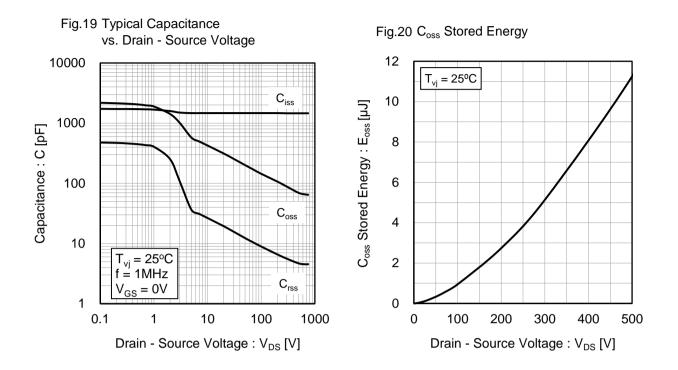
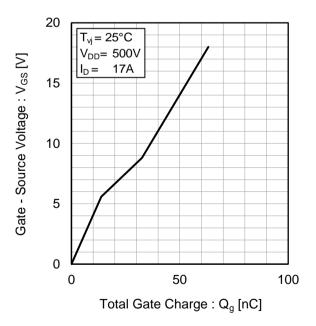
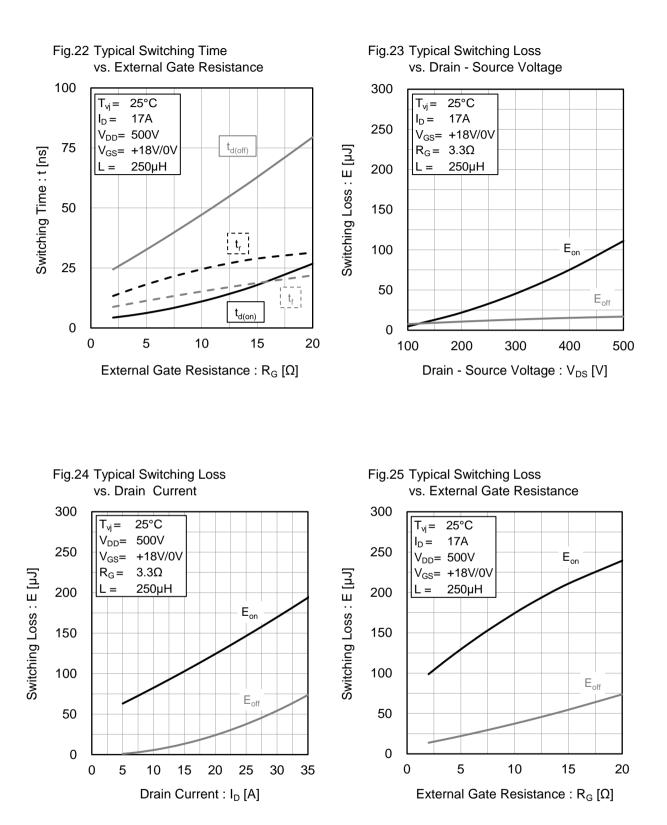
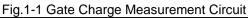


Fig.21 Dynamic Input Characteristics





Measurement circuits and waveforms



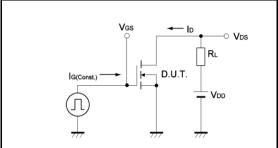


Fig.2-1 Switching Characteristics Measurement Circuit

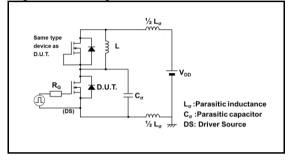


Fig.2-3 Waveforms for Switching Energy Loss

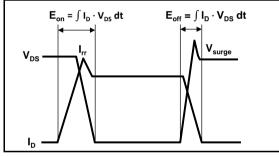


Fig.3-1 Reverse Recovery Time Measurement Circuit

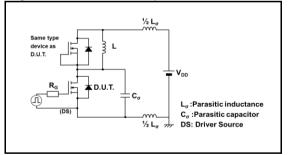


Fig.1-2 Gate Charge Waveform

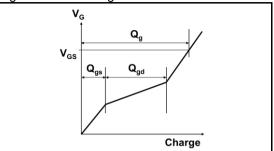


Fig.2-2 Waveforms for Switching Time

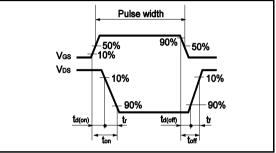
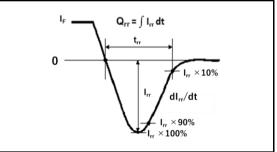
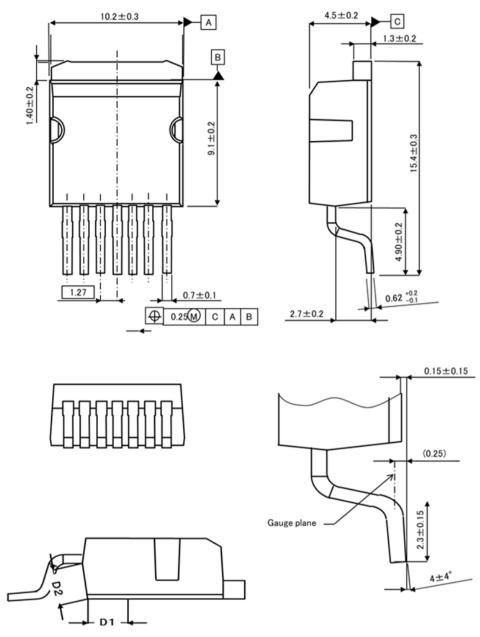


Fig.3-2 Reverse Recovery Waveform

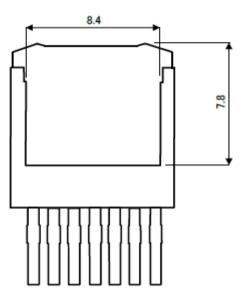


•Package Dimensions

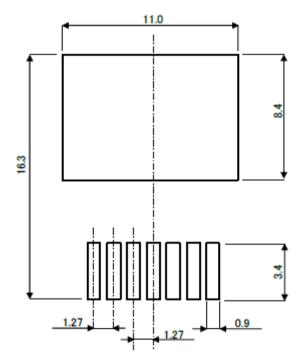


Minimum Creepage Distance = 4.7mm (D1+D2)

Unit: mm

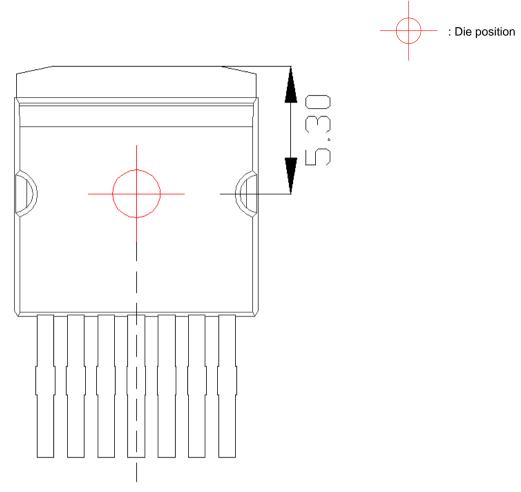


RECOMMENDED FOOTPRINT DIMENSIONS



Unit: mm

Die Bonding Layout

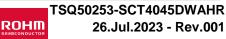


•Front view of the packaging.

•Dimensions are design values.

·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm



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