RGTV00TS65GC13

650V 50A Field Stop Trench IGBT

Datasheet

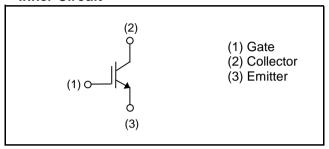
V _{CES}	650V
I _{C(100°C)}	50A
V _{CE(sat) (Typ.)}	1.5V
P_D	276W

●Outline TO-247GE (1)(2)(3)

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching & Low Switching Loss
- 3) Short Circuit Withstand Time 2µs
- 4) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Applications

Solar Inverter

UPS

Welding

ΙH

PFC

●Packaging Specifications

Tackaging Specifications					
	Packaging	Tube			
	Reel Size (mm)	-			
Typo	Tape Width (mm)	-			
Type	Basic Ordering Unit (pcs)	600			
	Packing Code	C13			
	Marking	RGTV00TS65			

● **Absolute Maximum Ratings** (at T_C = 25°C unless otherwise specified)

	, , ,		,	
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	T _C = 25°C	Ic	95	А
	T _C = 100°C	Ic	50	А
Pulsed Collector Current		I _{CP} ^{*1}	200	А
Davies Disabation	T _C = 25°C	P _D	276	W
Power Dissipation	T _C = 100°C	P _D	138	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C
		•		·

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.54	°C/W

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Lloit
r ai ai nietei	Symbol		Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 10 \mu A, V_{GE} = 0 V$	650	-	-	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	1	1	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, \ V_{CE} = 0V$		-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 34.3 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 50A$, $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C	, ,	1.5 1.85	1.9	V

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Darameter	Cymah al	Conditions		l limit		
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2890	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	116	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	48	-	
Total Gate Charge	Q_g	V _{CE} = 400V	-	104	-	
Gate - Emitter Charge	Q_ge	I _C = 50A	-	21	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	37	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 50A, V_{CC} = 400V$	-	41	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	20	-	20
Turn - off Delay Time	$t_{d(off)}$	T _j = 25°C	-	142	-	ns
Fall Time	t _f	Inductive Load	-	38	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	1.17	-	m l
Turn - off Switching Loss	E_{off}	reverse recovery	-	0.94	-	mJ
Turn - on Delay Time	$t_{d(on)}$	$I_C = 50A, V_{CC} = 400V$	-	39	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	23	-	no
Turn - off Delay Time	$t_{d(off)}$	T _j = 175°C	-	167	-	ns
Fall Time	t _f	Inductive Load	-	80	-	
Turn - on Switching Loss	E_{on}	*E _{on} includes diode	1	1.25	ı	mJ
Turn - off Switching Loss	E_{off}	reverse recovery	-	1.28	-	IIIJ
		$I_C = 200A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FULL SQUARE			-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				
		$V_{CC} \le 360V$				
Short Circuit Withstand Time	t_{sc}	V _{GE} = 15V	2	-	-	μs
		T _j = 25°C				

Fig.1 Power Dissipation vs. Case Temperature

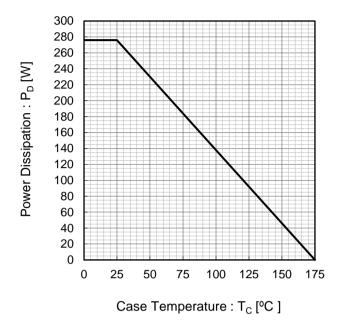
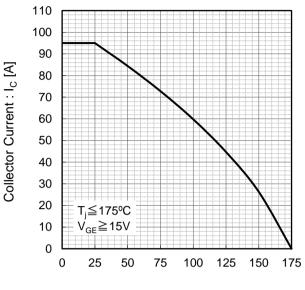
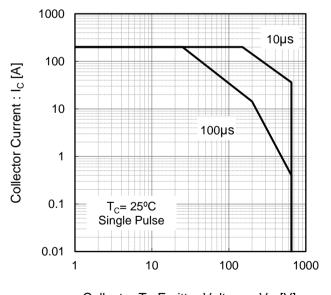


Fig.2 Collector Current vs. Case Temperature



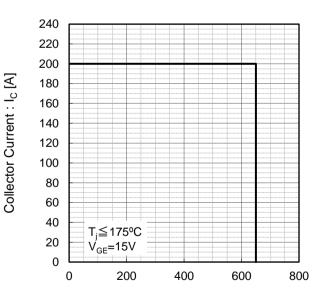
Case Temperature : T_C [°C]

Fig.3 Forward Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.5 Typical Output Characteristics

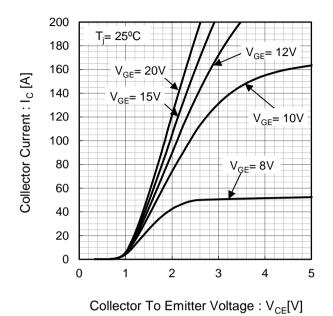
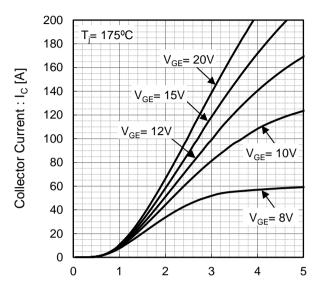


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V_{CE}[V]

Fig.7 Typical Transfer Characteristics

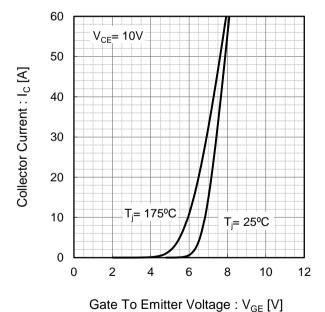
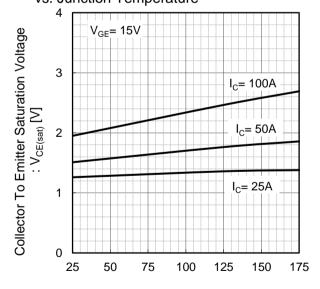


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Junction Temperature : T_i [°C]

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

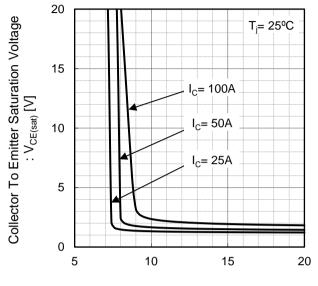
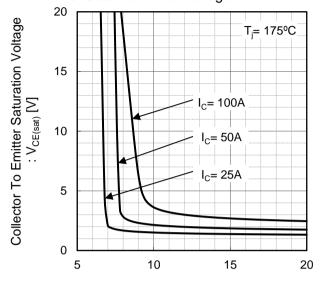


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage : V_{GE} [V]

Gate To Emitter Voltage : V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current

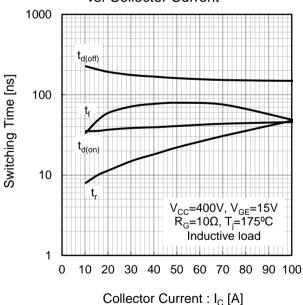
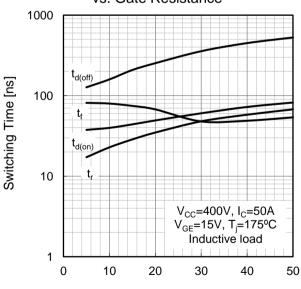


Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current

10 E_{off} 0.1 $V_{CC}=400V, V_{GE}=15V$ $R_{G}=10\Omega, T_{j}=175^{\circ}C$ Inductive load

0.10

Collector Current: I_{C} [A]

Fig.14 Typical Switching Energy Losses vs. Gate Resistance 10 Switching Energy Losses [mJ] E_{off} 1 E_{on} 0.1 V_{CC} =400V, I_{C} =50A V_{GE} =15V, T_{j} =175°C Inductive load 0.01 10 20 30 50 Gate Resistance : $R_G[\Omega]$

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz $V_{GE}=0V$ T_i=25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CF}[V]

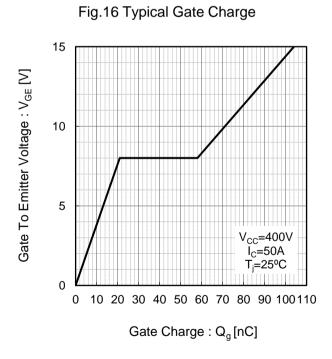
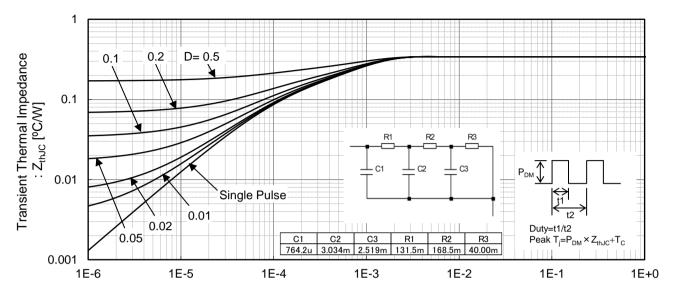


Fig.17 Typical IGBT Transient Thermal Impedance



Pulse Width: t1[s]

•Inductive Load Switching Circuit and Waveform

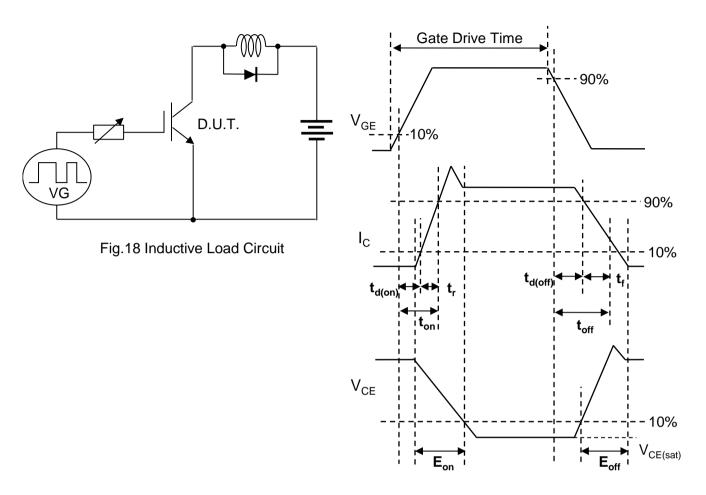


Fig.19 Inductive Load Waveform

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