

## INTRODUCTION

The SLogic Combo 8 is a versatile development tool that integrates the functionalities of a logic analyzer, CKLink debugger, DAP-Link debugger, and UART module (USB2UART). It allows for arbitrary switching between these functions via buttons. This 4-in-1 tool offers a maximum sampling rate of 80 MHz, supports up to 8 channels, and is compatible with multiple platforms. Despite its compact form factor, it provides a wide range of over 130 analysis protocols.

As a logic analyzer, it offers a maximum sampling frequency of 80 MHz, enabling the analysis of a broad range of protocols in conjunction with a host computer. When used as a UART module, it supports up to 4 serial ports with a maximum baud rate of 20 Mbps, making it suitable for various applications that rely on serial communication. As a CKLink or DAPLink, it aids in debugging RISC-V and ARM-based chips, with an additional serial port available for log observation.

### Powerful 4-in-1 Debugger



### Powerful Features



4 IN 1  
multi-function



Maximum sampling  
rate 80MHz



Maximum  
8 channels



Ultra-small size



Multi-platform  
support



130+ analysis  
protocols

**Support for analysing 130+ protocols**

### Basic protocol

spi	i2c	uart	i2s	modbus	numbers_and_state	sbus_futaba	mdio
onewire_link	i2cfilter	jtag	ook_vis	parallel	swd	lfast	microwire
ook	hdcp	usb_power_delivery	dmx512	usb_request	usb_signalling	can	usb_packet
cjtag	pjdl	onewire_network	ieee488	arm_tpiu			

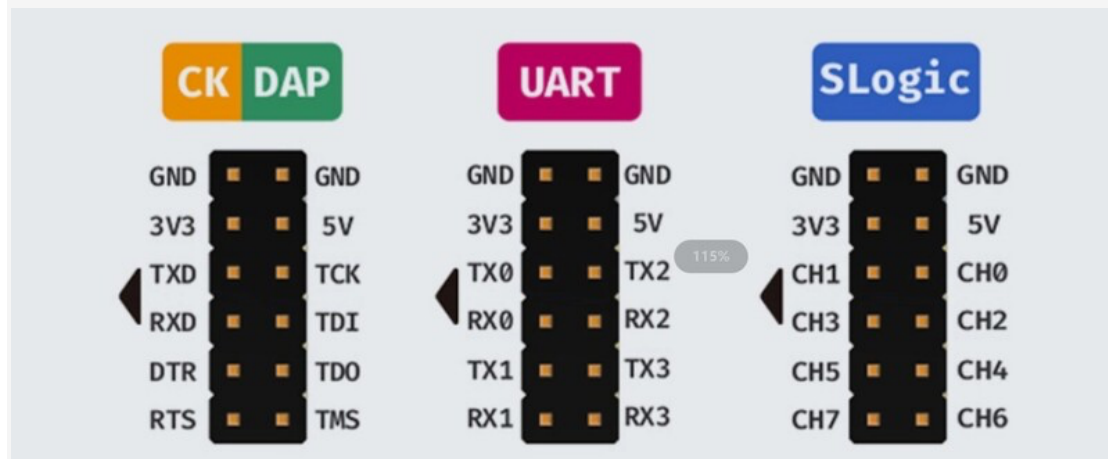
### High-level protocol

st7735	sda2506	guess_bitrate	ir_irmp	rtc8564	lin	tca6408a	ir_rc5
cc1101	mcs48	xfp	adns5020	ad79x0	avr_pdi	lpc	midi
tlc5620	enc28j60	rgb_led_spi	flexray	swim	rfm12	edid	qi
t55xx	sipi	spiflash	nes_gamepad	counter	sdq	ade77xx	pan1321
arm_etmv3	avr_isp	ltc26x7	sle44xx	aud	st25r39xx_spi	amulet_ascii	tdm_audio
mrf24j40	timing	i2cdemux	z80	cec	ir_rc6	atsha204a	jtag_stm32
ook_oregon	adf435x	nrf24l01	nrf905	eprom24xx	dsi	nunchuk	pjon
sae_j1850_vpw	ds243x	ssi32	cfp	ltc242x	ad5626	stepper_motor	em4305
ir_nec	dali	jitter	lm75	ps2	pca9571	ir_sirc	mxc6225xu
rgb_led_ws281x	maple_bus	arm_itm	miller	pwm	sdcard_spi	rc_encode	adxl345
ds28ea00	em4100	spdif	am230x	caliper	mlx90614	x2444m	seven_segment
jtag_ejtag	wiegand	morse	max7219	ac97	eprom93xx	ds1307	ds2408
graycode	dcf77	sdcard_sd	signature	xy2-100			

## Function Annotation

Indicator Light	Function	Description
●	SLogic	Up to 80M sample rate and up to 8 sampling channels. Can be used to capture and debug digital signals.
●	DAPLink + UART	One DAPLink and one serial port for monitor logs while debugging.
●	CKLink + UART	One CKLink and one serial port for monitor logs while debugging.
●	UART x 4	Supports 4 serial ports, two baud rates up to 20Mbps, and two baud rates up to 1Mbps.

## Panel Information



## APPLICATIONS

- Protocol Analysis
- Serial Communication
- Chip Debugging
- Education and Training
- Hardware Development and Testing
- Research and Development

## SPECIFICATION

## **SLogic Function Parameters**

- Maximum Channel Number: 8CH
- Maximum Sampling Rate: 80M
- Transmission Bandwidth: 320Mb/s
- Sampling Mode: Stream
- Typical Configuration: 80M@4CH 40M@8CH
- Signal Input Range: 0~3.6V
- High and Low Level Thresholds:  $V_{IN} > 2V$   $V_{IL} < 0.8V$

In Windows environment, the SLogic Combo 8 has maximum transmission bandwidth of 160 Mb/s. The typical configuration includes 80M@2CH and 40M@4CH.

## **CKLink Function Parameters**

- JTAG Clock Rate: 16M
- Typical Dump Speed: 1200KB/s
- Debug Serial Port: Up to 20Mbps

## **DAPLink Function Parameters**

- Debug Serial Port: Up to 20Mbps
- Typical Burning Speed: 110KB/s

## **UART Function Parameters**

- Maximum Baud Rate:  $2 \times 20\text{Mbps} + 2 \times 1\text{Mbps}$
- Total Bandwidth: 42Mbps

## **DOCUMENTS**

- [Basic Operations](#)
- [FAQ](#)
- [Using as CKLink](#)
- [Using as DAPLink](#)
- [Using as Serial Module](#)
- [Using as a Logic Analyzer](#)

## **SHIPPING LIST**

- Slogic Analyzer/ DAPLink /CKLink Debugger /UART Tool ×1
- Chip clip ×10
- USB Type-Cp ×1
- Female-to-female DuPont wirep ×1