RF9G120BKFRA

Nch 40V 12A Power MOSFET

Datasheet

V _{DSS}	40V
R _{DS(on)} (Max.)	17.5mΩ
I _D	±12A
P _D	23W

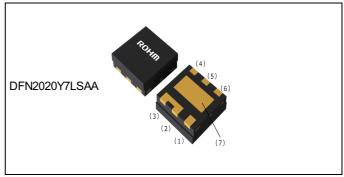
Features

- 1) AEC-Q101 qualified
- 2) Low on resistance
- 3) High power small mold package
- 4) Pb-free plating; RoHS compliant
- 5) Halogen Free
- 6) WettableFlank

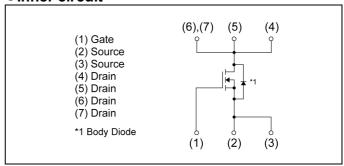
Application

ADAS/Info./Lighting/Body

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	AC

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	40	V
Continuous drain current	V _{GS} = 10V	I _D *1	±12	Α
Pulsed drain current	l _{DP} *2	±24	Α	
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *3	10	Α	
Avalanche energy, single pulse	E _{AS} *3	7.5	mJ	
Power dissipation	P _D *1	23	W	
Junction temperature	T _j	150	°C	
Operating junction and storage te	mperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Lloit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	ı	5.4	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values		Unit	
Parameter			Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 1mA$	40	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		21	-	V/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V	1	1	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	ı	±500	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 431 \mu A$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 431μA referenced to 25°C	-	-4.9	-	V/°C
Static drain - source	D *4	V _{GS} = 10V, I _D = 3A	-	13.5	17.5	mΩ
on - state resistance	R _{DS(on)} *4	$V_{GS} = 4.5V, I_D = 3A$	ı	19.6	26.0	11122
Gate resistance	R_G	f = 1MHz, open drain	-	3.6	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 5V, I _D = 3A	2.8	-	-	S

^{*1} T_c=25°C, Limited only by maximum temperature allowed.

^{*2} Pw≤10µs , Duty cycle≤1%

^{*3} L \simeq 0.1mH, V_{DD} = 20V R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Symbol	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Uniil	
Input capacitance	C _{iss}	V _{GS} = 0V	-	470	-		
Output capacitance	C _{oss}	V _{DS} = 20V	-	270	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	24	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 20V,V _{GS} = 10V	1	7	-		
Rise time	t _r *4	I _D = 10A	1	7	1	no	
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 2\Omega$	1	16	-	ns	
Fall time	t _f *4	$R_G = 1\Omega$	-	5	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate charge	O *4		V _{GS} = 10V	-	8.5	-	
Total gate charge	Q _g *4	V _{DD} ≈ 20V		-	4.8	-	5 C
Gate - Source charge	Q _{gs} *4	I _D = 6A	V _{GS} = 4.5V	-	1.5	-	nC
Gate - Drain charge	Q _{gd} *4			-	2.2	-	

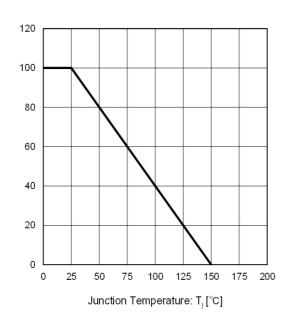
● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S *1	T = 25°C	-	-	12	Α
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	24	Α
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_{S} = 3A$	-	-	1.5	V
Reverse recovery time	t _{rr} *4	I _S = 10A, V _{GS} =0V	-	24	-	ns
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/μs	-	15	-	nC

Power Dissipation: P_D/P_{Dmax}.

• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



Drain Current : I_D [A]

Fig.2 Maximum Safe Operating Area

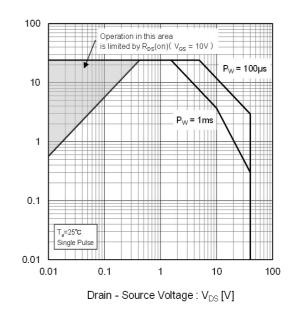


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

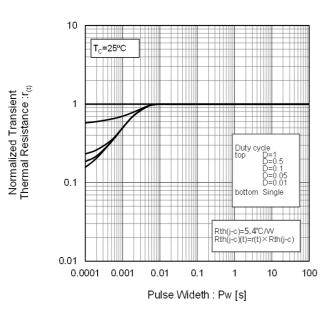
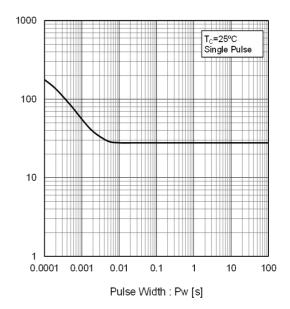


Fig.4 Single Pulse Maximum Power dissipation

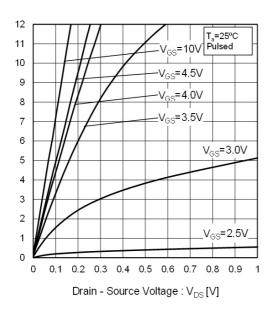


Peak Transient Power: P[W]

Drain Current : I_D [A]

• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.6 Typical Output Characteristics(II)

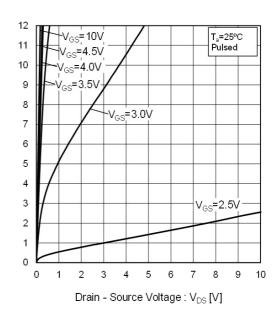
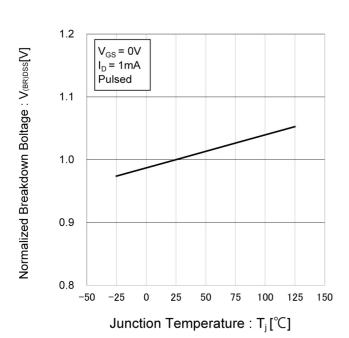


Fig.7 Breakdown Voltage vs.
Junction Temperature



Drain Current : I_D [A]

• Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

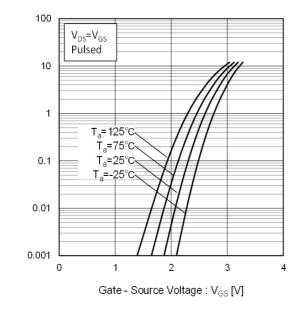
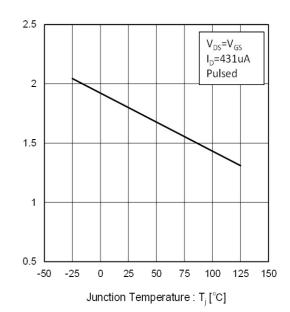
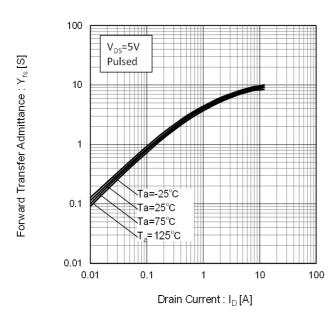


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage : $V_{GS(th)}[V]$

Fig.10 Forward Transfer Admittance vs.
Drain Current



Drain Current Dissipation: Ip/Ipmax. [%]

• Electrical characteristic curves

Fig.11 Drain Current Derating Curve

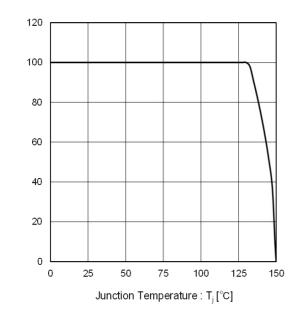


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

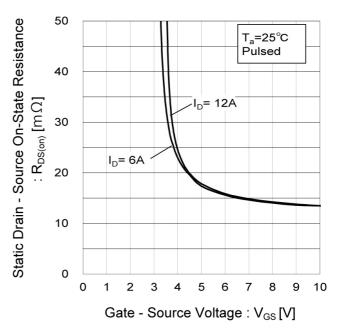
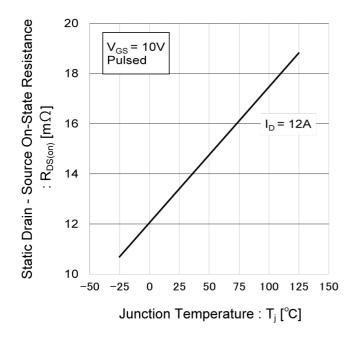


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



• Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

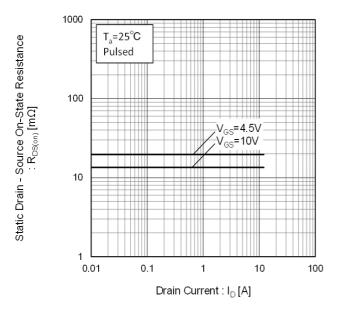
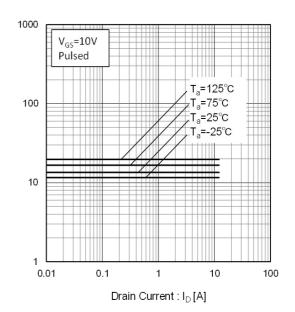
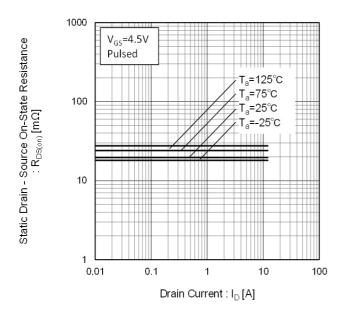


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)



Static Drain - Source On-State Resistance : $R_{DS(on)}\left[m\Omega\right]$

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



• Electrical characteristic curves

Fig.17 Typical Capacitance vs.

Drain - Source Voltage

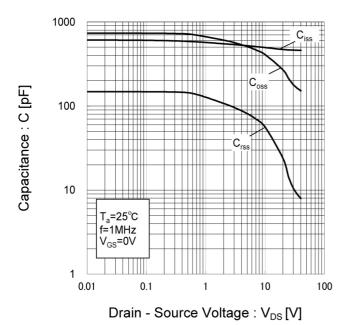


Fig.18 Switching Characteristics

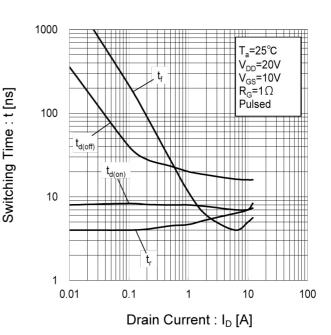


Fig.19 Dynamic Input Characteristics

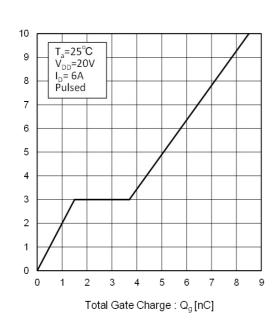
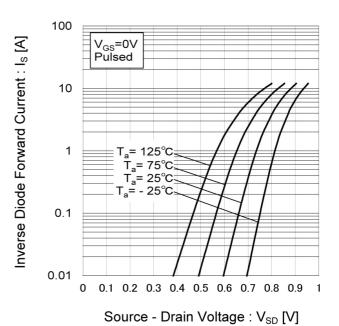


Fig.20 Source Current vs.

Source Drain Voltage



Gate - Source Voltage : V_{GS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

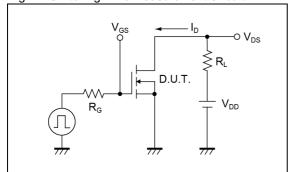


Fig.2-1 Gate Charge Measurement Circuit

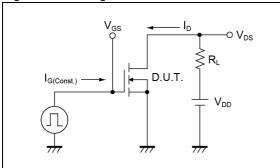


Fig.3-1 Avalanche Measurement Circuit

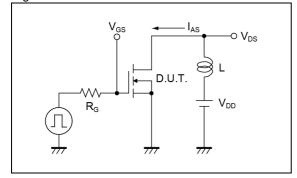


Fig.1-2 Switching Waveforms

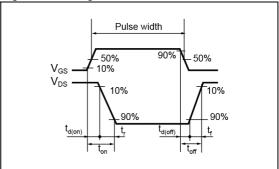


Fig.2-2 Gate Charge Waveform

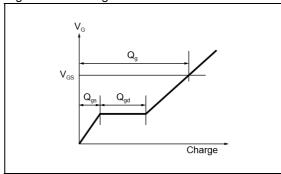
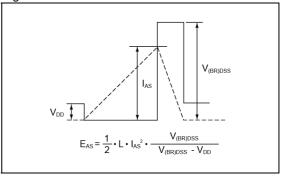
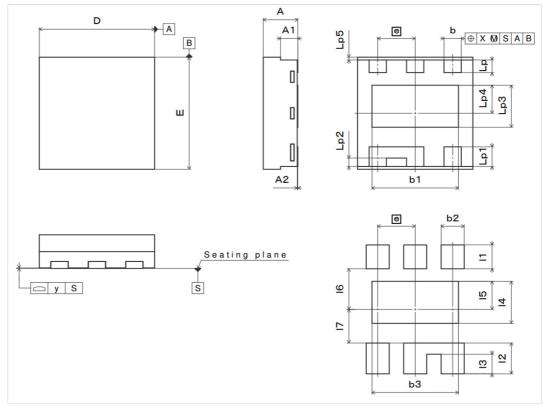


Fig.3-2 Avalanche Waveform



Dimensions

DFN2020Y7LSAA



[reference pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.20	0.30	0.008	0.012
A2	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.45	1.55	0.057	0.061
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.60	0.70	0.024	0.028
Lp	0.175	0.275	0.007	0.011
Lp1	0.30	0.40	0.012	0.016
Lp2	0.10	0.20	0.004	0.008
Lp3	0.70	0.80	0.028	0.031
Lp4	0.45	0.55	0.018	0.022
Lp5	0.01	0.09	0.000	0.004
х	-	0.10	-	0.004
у	-	0.10	-	0.004

DIM	MILIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX	
b2	0.4	40	0.0)16	
b3	1.	50	0.0)59	
I1	0.4	25	0.017		
12	0.55		0.022		
13	0.3	35	0.0)14	
14	0.75		0.0	30	
15	0.50		0.0	20	
16	0.725		0.0	29	
17	0.0	60	0.0)24	

Dimension in mm/inches



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ſ	JÁPAN	USA	EU	CHINA
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ſ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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