RF9L120BJFRA

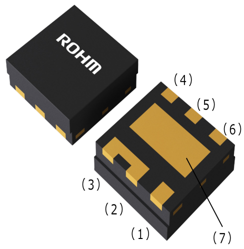
Pch -60V 12A Power MOSFET

Datasheet

V_{DSS}	-60V
$R_{DS(on)}$(Max.)	106mΩ
I_D	±12A
P_D	23W

●Outline

DFN2020Y7LSAA



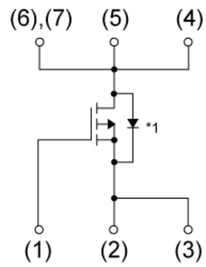
●Features

1) AEC-Q101 qualified
2) Low on - resistance
3) High power small mold package
4) Pb-free plating ; RoHS compliant
5) Halogen Free
6) WettableFlank

●Inner circuit

(1) Gate
(2) Source
(3) Source
(4) Drain
(5) Drain
(6) Drain
(7) Drain

*1 Body Diode



●Application

ADAS/Info./Lighting/Body

●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	180
	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	TD

●Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	-60	V
Continuous drain current	V_{GS} = -10V	I_D *1	±12	A
Pulsed drain current		I_{DP} *2	±24	A
Gate - Source voltage		V_{GSS}	+5/-20	V
Avalanche current, single pulse		I_{AS} *3	10	A
Avalanche energy, single pulse		E_{AS} *3	7.4	mJ
Power dissipation		P_D *1	23	W
Junction temperature		T_j	150	℃
Operating junction and storage temperature range		T_{stg}	-55 to +150	℃

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC} *1	-	-	5.4	°C/W

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = -1mA	-60	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I_D = -1mA referenced to 25°C	-	-50	-	mV/°C
Zero gate voltage drain current	I_{DSS}	V_{DS} = -48V, V_{GS} = 0V	-	-	-1	μA
Gate - Source leakage current	I_{GSS}	V_{GS} = +5/-20V, V_{DS} = 0V	-	-	±500	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -273μA	-1.0	-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I_D = -273μA referenced to 25°C	-	3.7	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}$ *4	V_{GS} = -10V, I_D = -3A	-	83	106	mΩ
		V_{GS} = -4.5V, I_D = -3A	-	92	118	
Gate resistance	R_G	f = 1MHz, open drain	-	13.8	-	Ω
Forward Transfer Admittance	$\|Y_{fs}\|$ *4	V_{DS} = -5V, I_D = -3A	3.9	-	-	S

*1 T_c=25°C , Limited only by maximum junction temperature T_j=150°C.

*2 Pw ≤10μs , Duty cycle ≤1%

*3 L=0.1mH, V_{DD}=-30V, R_G=25Ω, Starting T_j=25°C, See Fig.3-1,3-2

*4 Pulsed

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	V_{GS} = 0V	-	710	-	pF
Output capacitance	C_{oss}	V_{DS} = -30V	-	55	-	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	45	-	
Turn - on delay time	$t_{d(on)}$ *4	V_{DD} ≃ -30V, V_{GS} = -10V	-	8.7	-	ns
Rise time	t_r *4	I_D = -10A	-	13.0	-	
Turn - off delay time	$t_{d(off)}$ *4	R_L ≃ 5Ω	-	52.0	-	
Fall time	t_f *4	R_G = 1Ω	-	18.0	-	

●Gate charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions		Values			Unit
				Min.	Typ.	Max.	
Total gate charge	Q_g	V_{DD} ≃ -30V I_D = -10A	V_{GS} = -10V	-	15.7	-	nC
			V_{GS} = -4.5V	-	7.2	-	
Gate - Source charge	Q_{gs}			-	2.5	-	
Gate - Drain charge	Q_{gd}			-	3.2	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I_S	T_a = 25℃	-	-	-12	A
Pulse forward current	I_{SP} *2		-	-	-24	A
Forward voltage	V_{SD}	V_{GS} = 0V, I_S = -3A	-	-	-1.2	V
Reverse recovery time	t_{rr}	I_S = -10A, V_{GS}=0V di/dt = -100A/μs	-	23	-	ns
Reverse recovery charge	Q_{rr}		-	20	-	μC

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

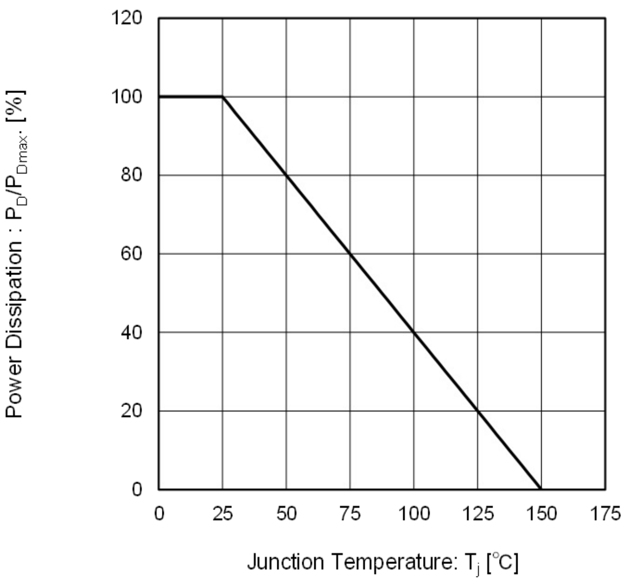


Fig.2 Maximum Safe Operating Area

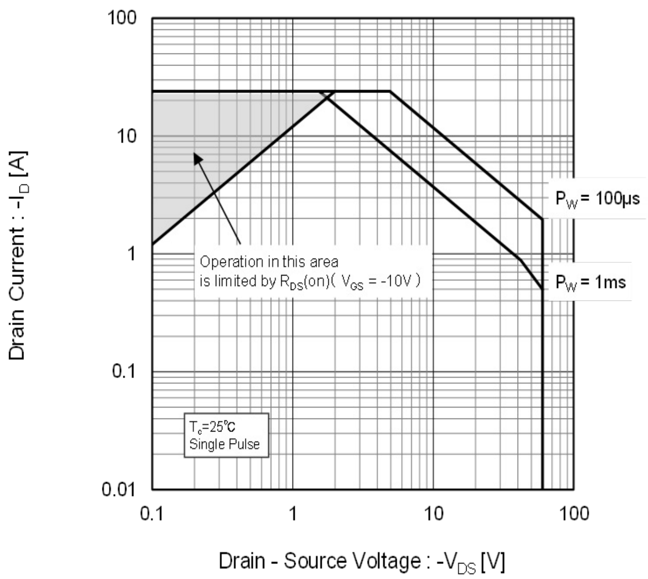


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

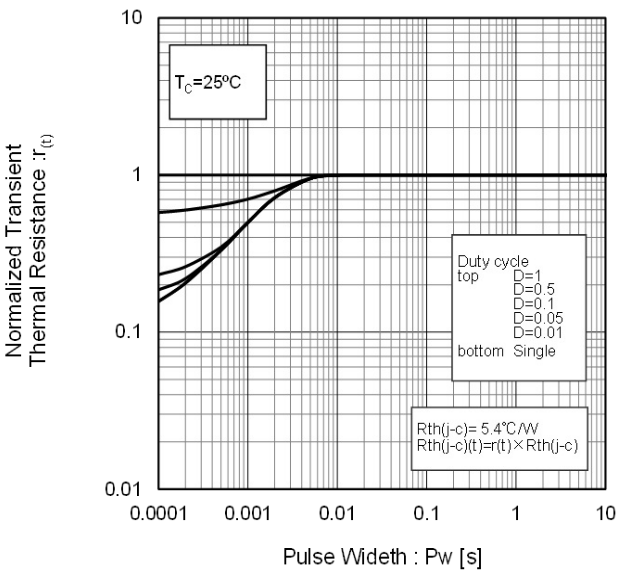
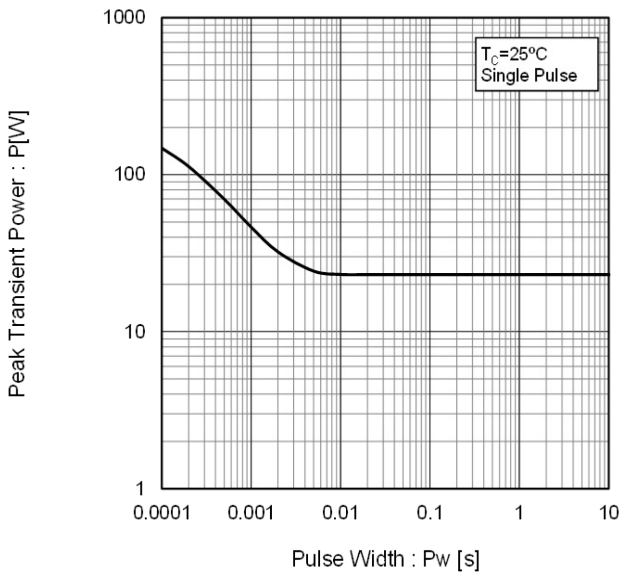


Fig.4 Single Pulse Maximum Power dissipation



●Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

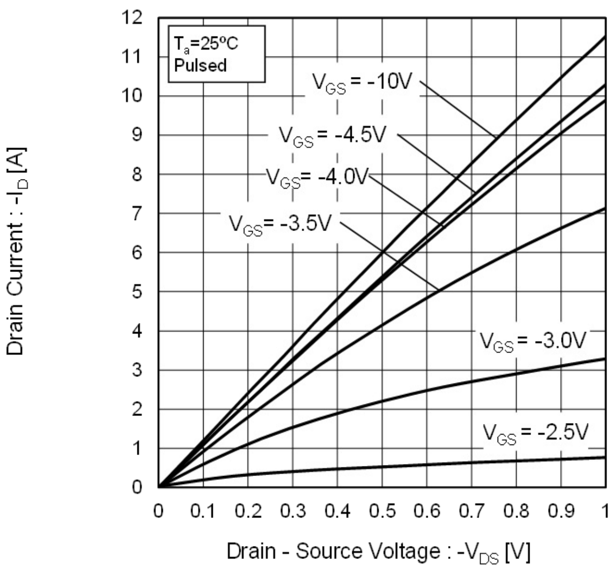


Fig.6 Typical Output Characteristics(II)

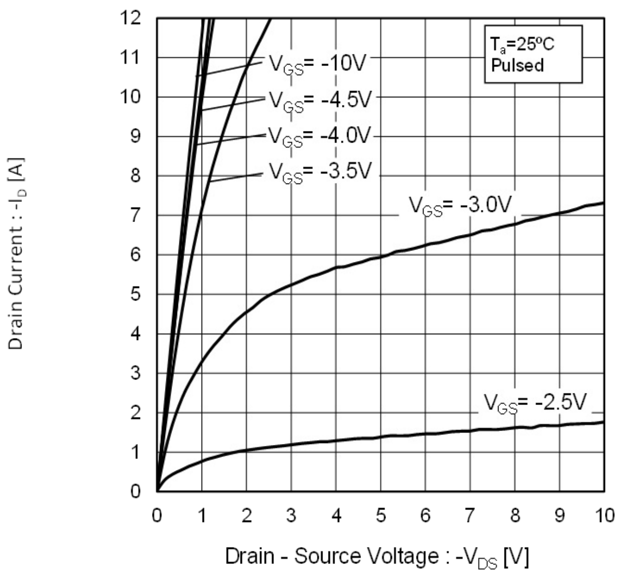
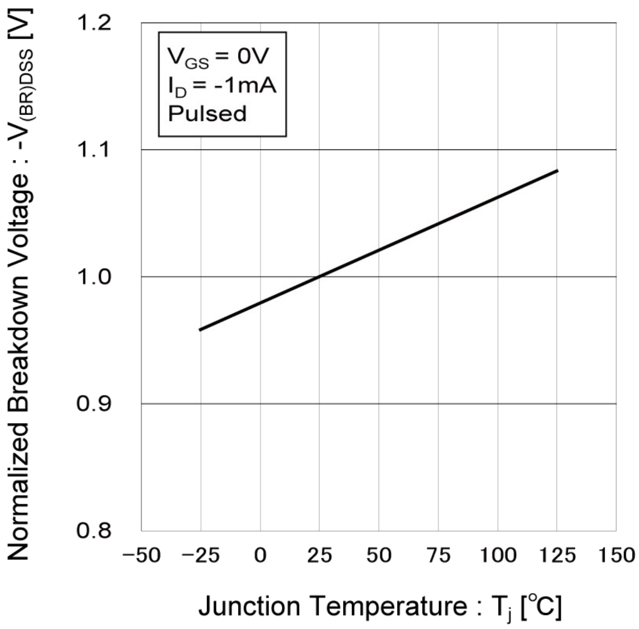


Fig.7 Breakdown Voltage vs. Junction Temperature



●Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

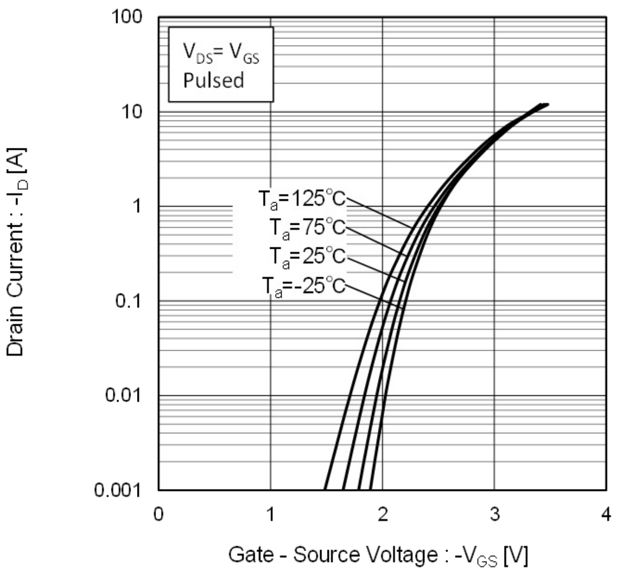


Fig.9 Gate Threshold Voltage vs. Junction Temperature

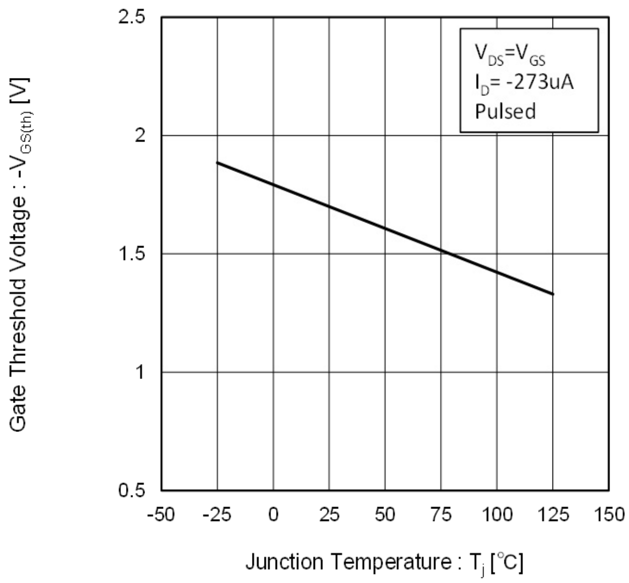
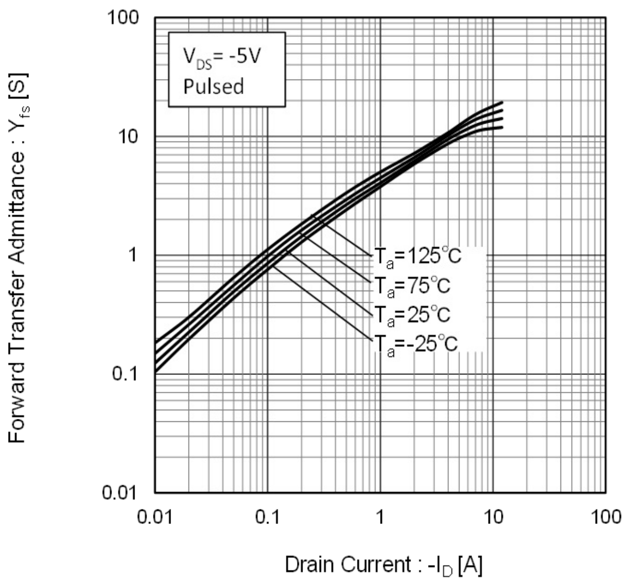


Fig.10 Forward Transfer Admittance vs. Drain Current



●Electrical characteristic curves

Fig.11 Drain Current Derating Curve

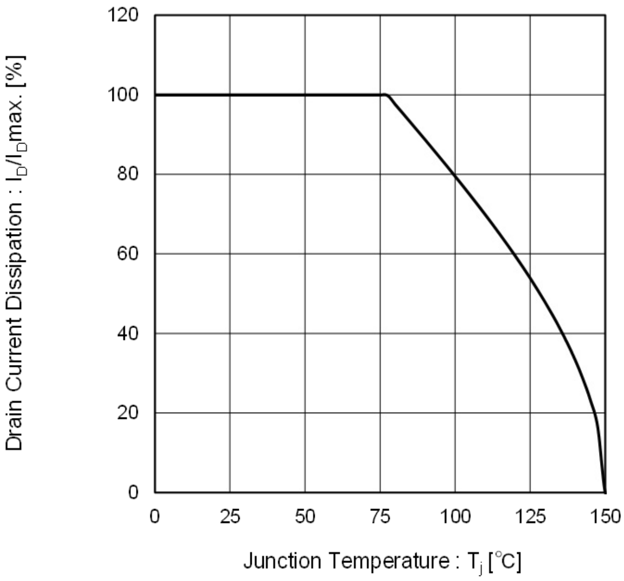


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

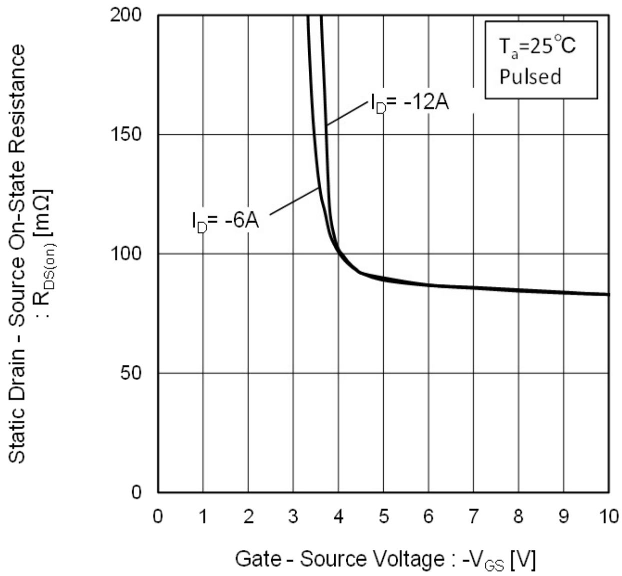
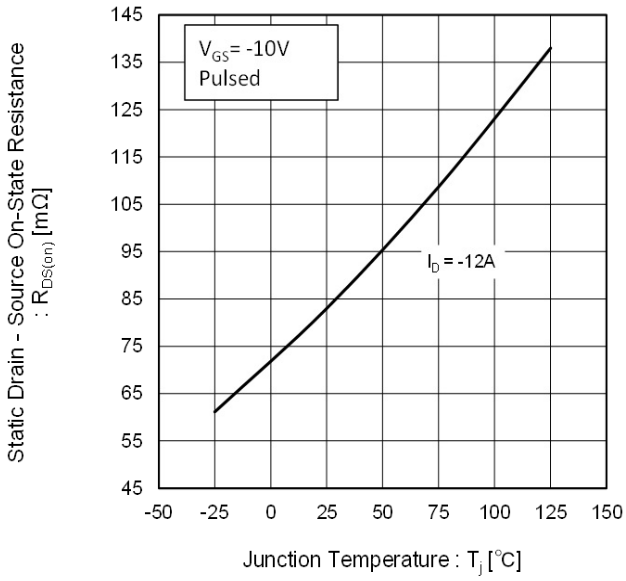


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



●Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

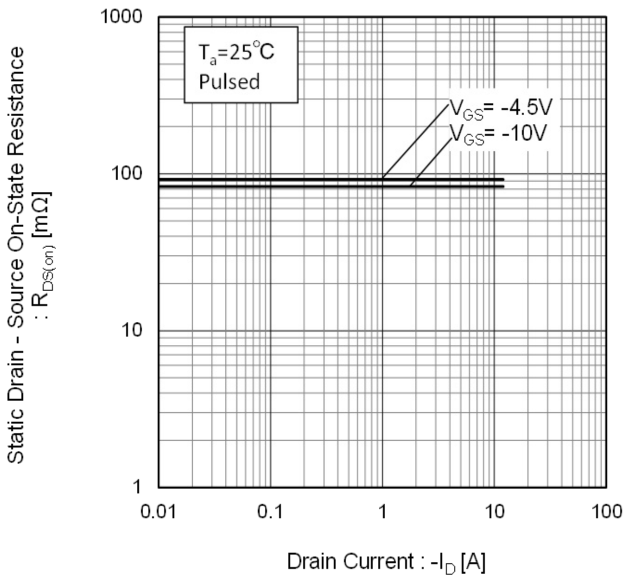


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

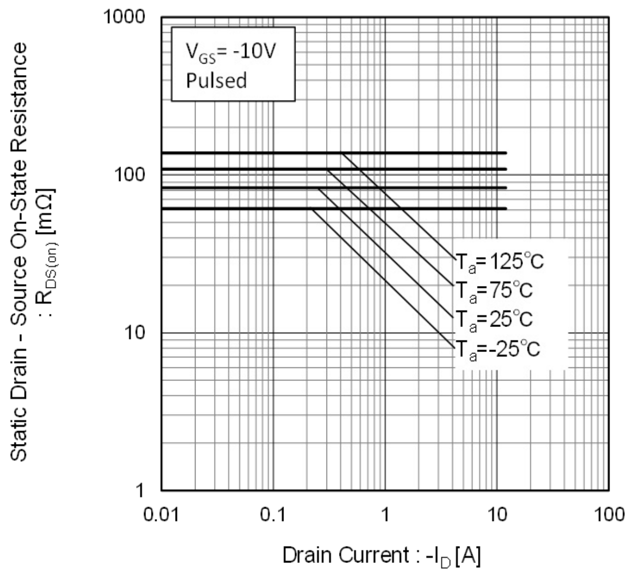
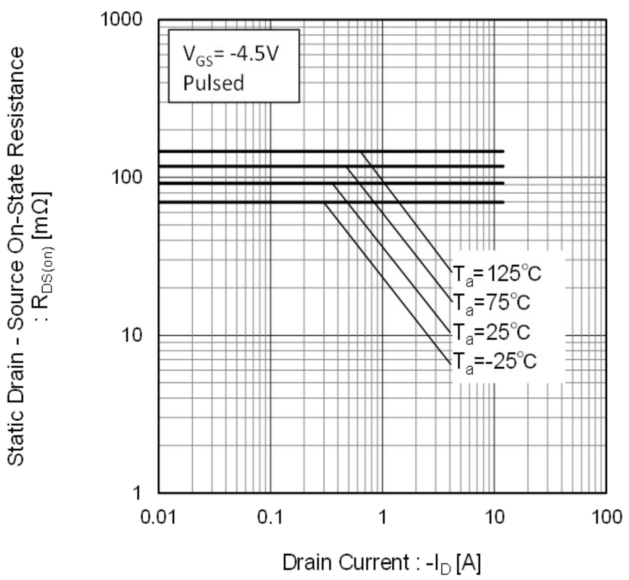


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



●Electrical characteristic curves

Fig.17 Typical Capacitance vs. Drain - Source Voltage

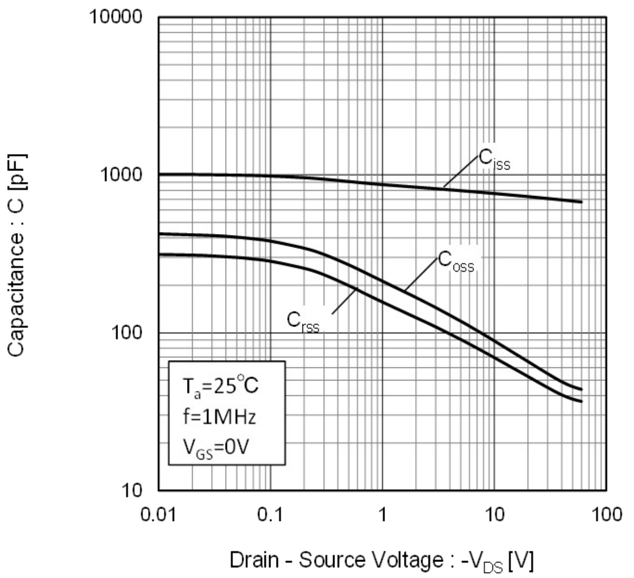


Fig.18 Switching Characteristics

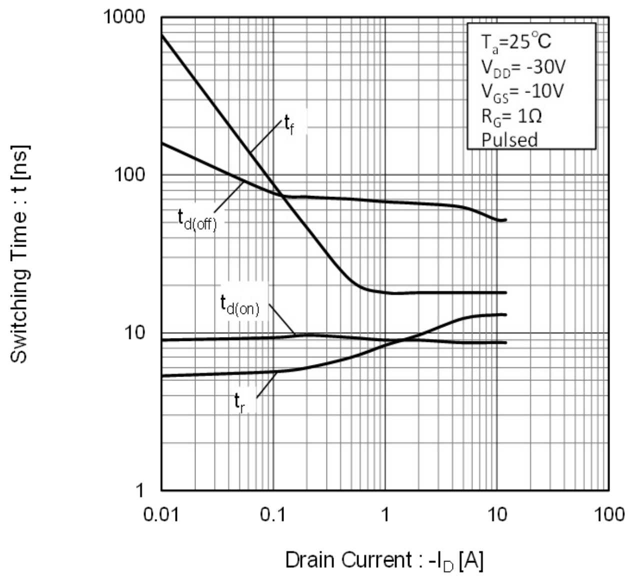


Fig.19 Dynamic Input Characteristics

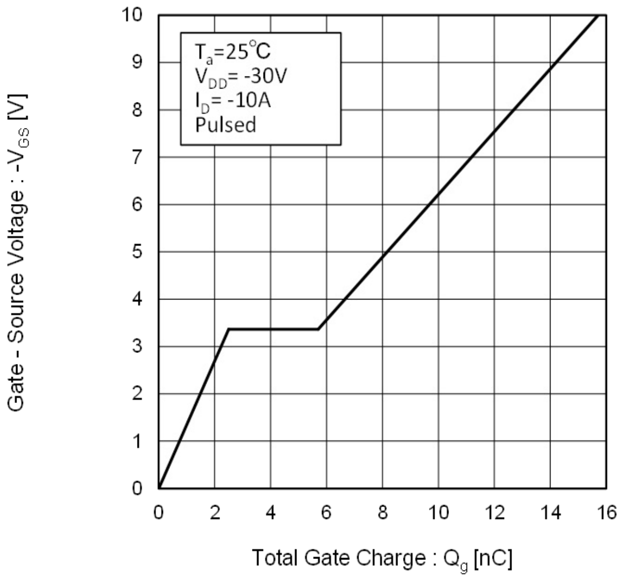
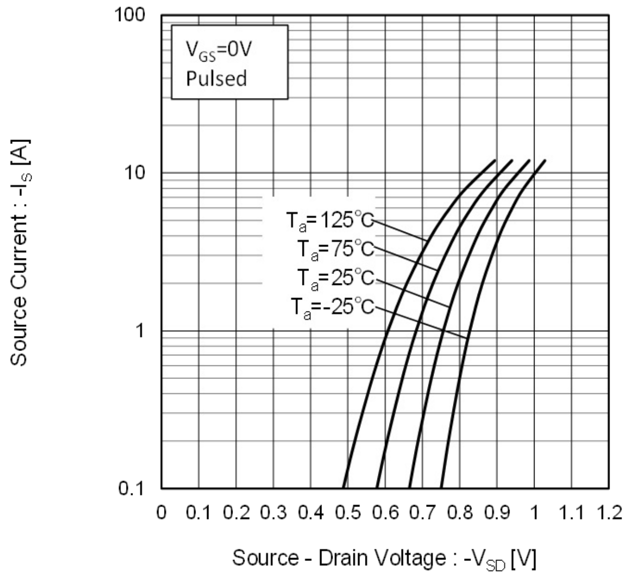


Fig.20 Source Current vs. Source Drain Voltage



●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

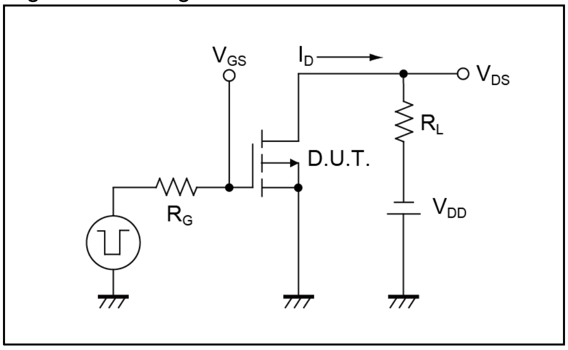


Fig.1-2 Switching Waveforms

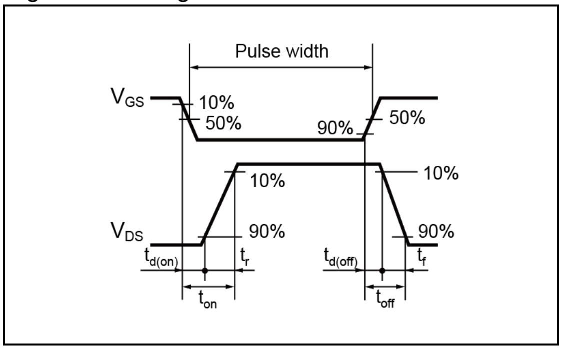


Fig.2-1 Gate Charge Measurement Circuit

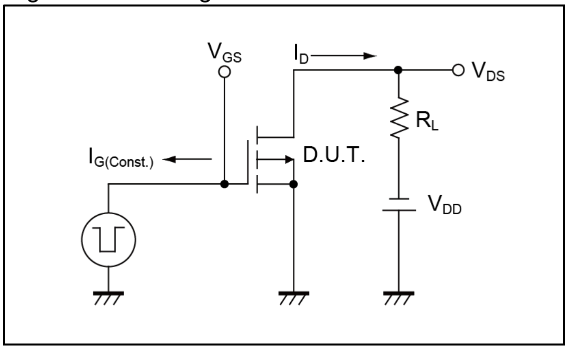


Fig.2-2 Gate Charge Waveform

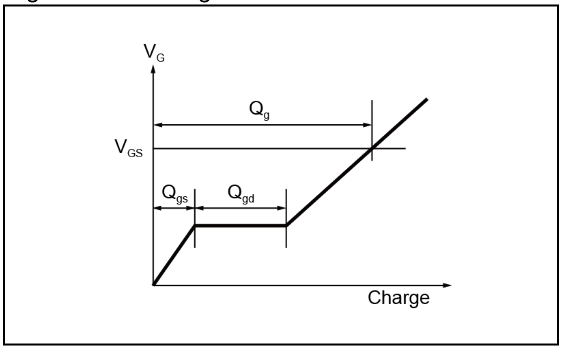


Fig.3-1 Avalanche Measurement Circuit

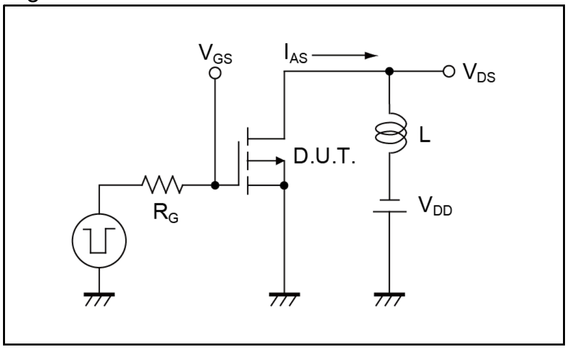
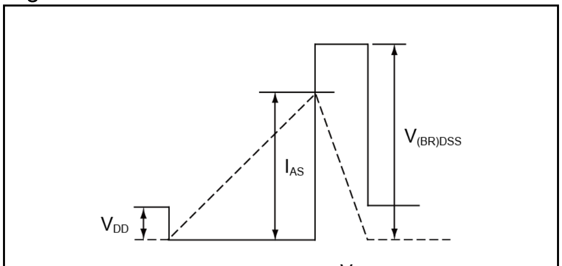


Fig.3-2 Avalanche Waveform



$$E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^{2} \cdot \frac{V_{(BR)DSS}}{V_{(BR)DSS} - V_{DD}}$$

●Dimensions

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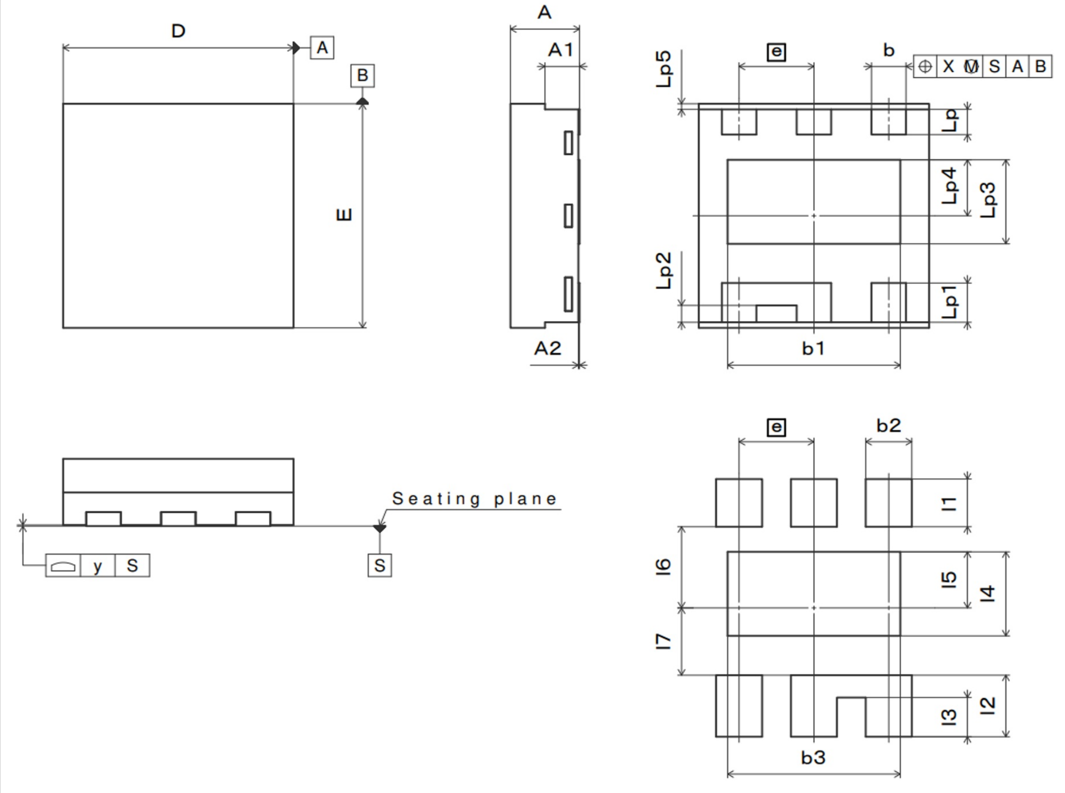

[reference pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.55	0.65	0.022	0.026
A1	0.20	0.30	0.008	0.012
A2	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.45	1.55	0.057	0.061
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
e	0.60	0.70	0.024	0.028
Lp	0.175	0.275	0.007	0.011
Lp1	0.30	0.40	0.012	0.016
Lp2	0.10	0.20	0.004	0.008
Lp3	0.70	0.80	0.028	0.031
Lp4	0.45	0.55	0.018	0.022
Lp5	0.01	0.09	0.000	0.004
x	-	0.10	-	0.004
y	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	0.40		0.016	
b3	1.50		0.059	
l1	0.425		0.017	
l2	0.55		0.022	
l3	0.35		0.014	
l4	0.75		0.030	
l5	0.50		0.020	
l6	0.725		0.029	
l7	0.60		0.024	

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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