



# RF9L120BKFRA

Nch 60V 12A Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	30mΩ
Ι <sub>D</sub>	±12A
P <sub>D</sub>	23W

# Features

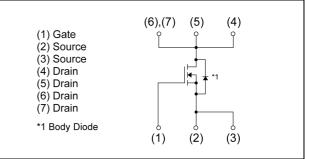
- 1) AEC-Q101 qualified
- 2) Low on resistance
- 3) High power small mold package
- 4) Pb-free plating ; RoHS compliant
- 5) Halogen Free
- 6) WettableFlank

# Application

ADAS/INfo./Lighting/Body

●Outline	
DFN2020Y7LSAA	(4) (5) (3) (2) (1) (7)

#### Inner circuit



### Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	AD

# • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit		
Drain - Source voltage		V <sub>DSS</sub>	60	V	
Continuous drain current V <sub>GS</sub> = 10V		۱ <sub>D</sub> *1	±12	А	
Pulsed drain current	<sup>*2</sup>	±24	А		
Gate - Source voltage		V <sub>GSS</sub>	±20	V	
Avalanche current, single pulse		I <sub>AS</sub> *3	9.0	А	
Avalanche energy, single pulse		$E_{AS}^{*3}$	6.4	mJ	
Power dissipation		P <sub>D</sub> <sup>*1</sup>	23	W	
Junction temperature	Tj	150	°C		
Operating junction and storage temp	T <sub>stg</sub>	-55 to +150	°C		

#### •Thermal resistance

Parameter	Symbol	Values			Unit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*1}$	-	-	5.4	°C/W

# •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Currente e l	Conditions	Values			1.1:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	34	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS} = 60V, V_{GS} = 0V$		-	-	1	μA	
Gate - Source leakage current	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±500	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 19 \mu A$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 19μA referenced to 25°C	-	-5.0	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	23	30		
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A	-	31 42		- mΩ	
Gate resistance	$R_G$ f = 1MHz, open drain		-	3.4	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 3A	3.3	-	-	S	

\*1 T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

\*2 Pw≤10µs , Duty cycle≤1%

\*3 L  $\simeq$  0.1mH, V\_{DD} = 30V R\_G = 25 $\Omega$ , Starting T\_j = 25°C Fig.3-1,3-2

\*4 Pulsed



# • Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumphal	Conditions	Values			Lincit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	440	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	105	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	10	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 30V, V_{GS}$ = 10V	-	9.0	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 10A	-	6.0	-		
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L \simeq 3\Omega$	-	24	-	ns	
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 1Ω	-	4.7	-		

# • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

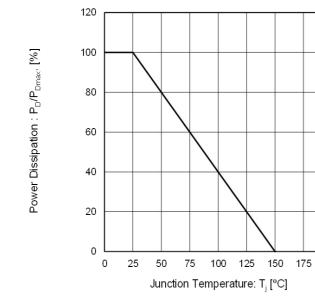
Deremeter	Sumbol	Conditions		Values			l lait	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Total gata abarga	O *4		V <sub>GS</sub> = 10V	-	7.3	-		
Total gate charge	Q <sub>g</sub> *4		$V_{DD} \simeq 30V$		-	4.2	-	nC
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 10A	V <sub>GS</sub> = 4.5V	-	1.5	-	nc	
Gate - Drain charge	Q <sub>gd</sub> *4			-	1.7	-		

# •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Deremeter	Symbol		Values			Unit
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub> *1	T <sub>a</sub> = 25℃	-	-	12	А
Pulse forward current	I <sub>SP</sub> *2	$T_a = 25 C$	-	-	24	А
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 3A	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 10A, V <sub>GS</sub> =0V	-	26	-	ns
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/µs	-	21	-	nC



#### • Electrical characteristic curves



#### Fig.1 Power Dissipation Derating Curve

Drain Current : I<sub>D</sub> [A]

200

# Fig.2 Maximum Safe Operating Area

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

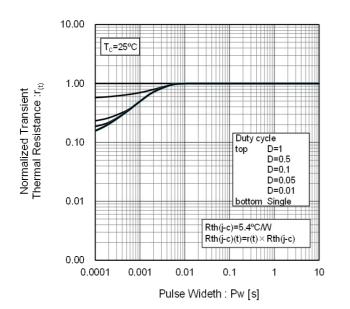
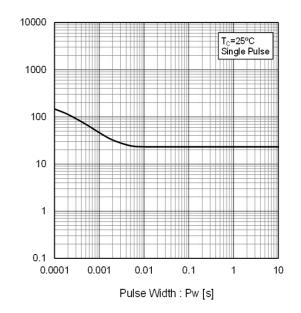


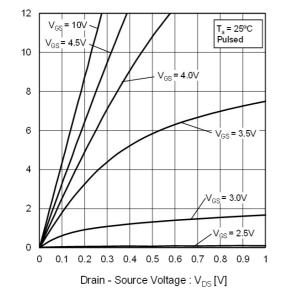
Fig.4 Single Pulse Maximum Power dissipation





Peak Transient Power : P[W]

Normalized Breakdown Voltage : V<sub>(BR)DSS</sub>



Drain Current : I<sub>D</sub> [A]

Fig.5 Typical Output Characteristics(I)

#### Fig.6 Typical Output Characteristics(II)

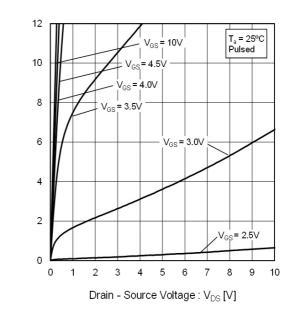
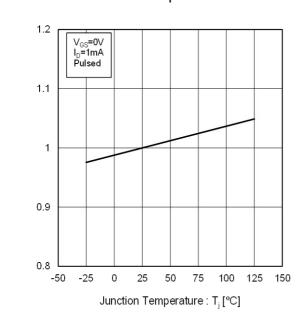


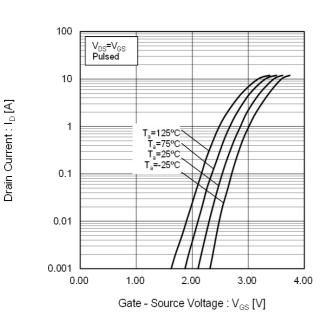
Fig.7 Breakdown Voltage vs. Junction Temperature



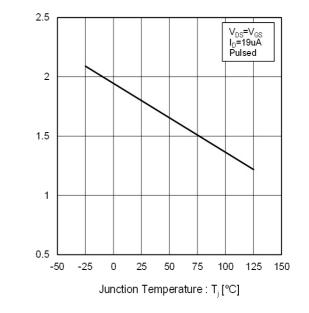


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Fig.8 Typical Transfer Characteristics

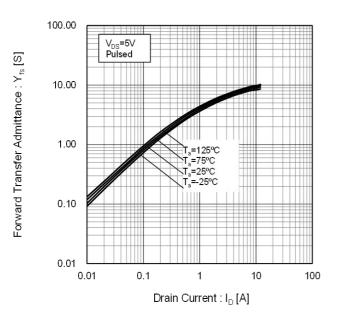


### Fig.9 Gate Threshold Voltage vs. Junction Temperature



Gate Threshold Voltage :  $V_{GS(th)}$  [V]

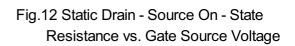
# Fig.10 Forward Transfer Admittance vs. Drain Current











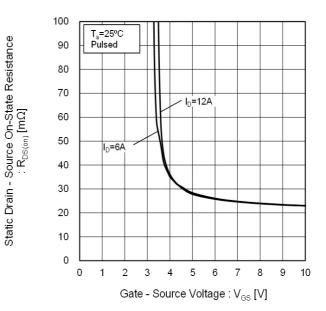
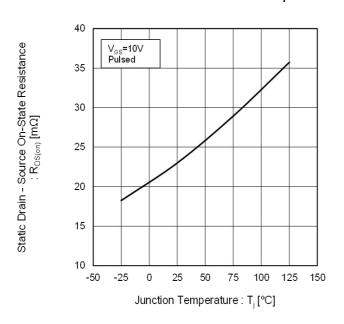


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Junction Temperature : T<sub>i</sub> [°C]





### • Electrical characteristic curves

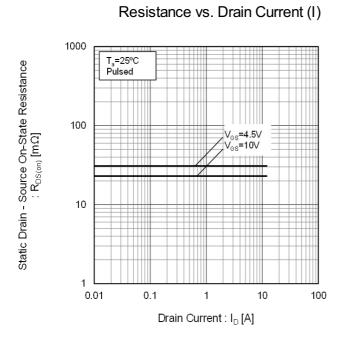
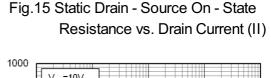


Fig.14 Static Drain - Source On - State



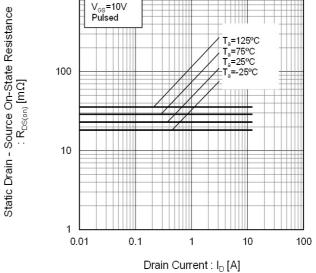
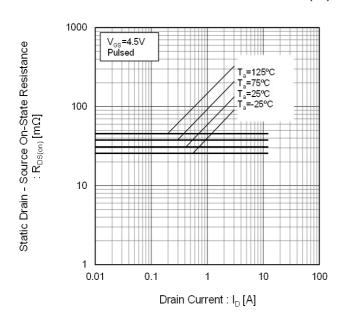
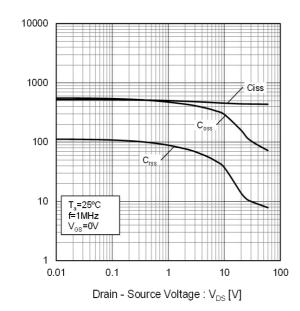


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

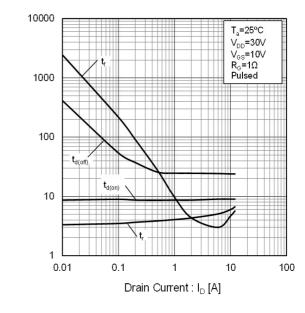




#### • Electrical characteristic curves



# Fig.17 Typical Capacitance vs. Drain - Source Voltage



Switching Time : t [ns]

# Fig.18 Switching Characteristics

Fig.19 Dynamic Input Characteristics

Gate - Source Voltage :  $V_{GS}$  [V]

Capacitance : C [pF]

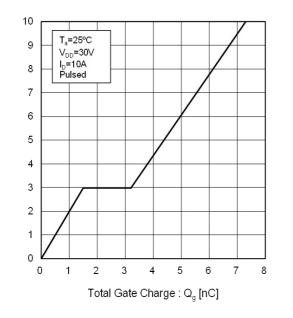
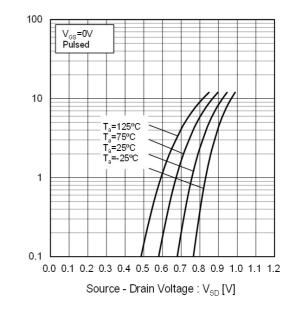


Fig.20 Source Current vs. Source Drain Voltage





Source Current : I<sub>s</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

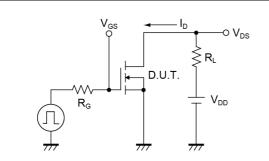


Fig.2-1 Gate Charge Measurement Circuit

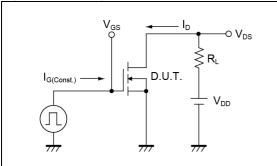


Fig.3-1 Avalanche Measurement Circuit

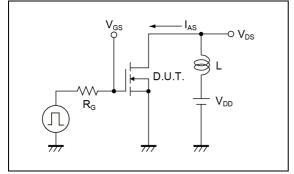


Fig.1-2 Switching Waveforms

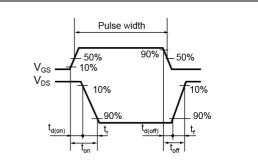
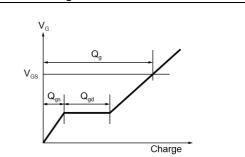
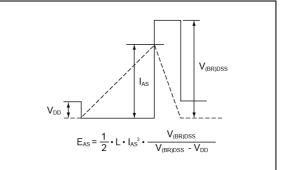


Fig.2-2 Gate Charge Waveform



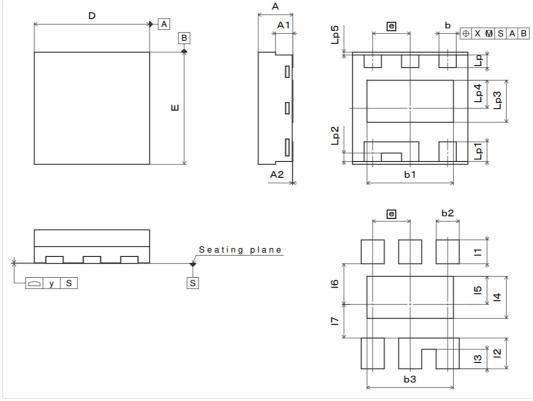
#### Fig.3-2 Avalanche Waveform





#### Dimensions

# DFN2020Y7LSAA



#### [reference pattern of soldering pads]

	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	0.55	0.65	0.022	0.026
A1	0.20	0.30	0.008	0.012
A2	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.45	1.55	0.057	0.061
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.60	0.70	0.024	0.028
Lp	0.175	0.275	0.007	0.011
Lp1	0.30	0.40	0.012	0.016
Lp2	0.10	0.20	0.004	0.008
Lp3	0.70	0.80	0.028	0.031
Lp4	0.45	0.55	0.018	0.022
Lp5	0.01	0.09	0.000	0.004
x	-	0.10	-	0.004
у	-	0.10	-	0.004
DIM	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
b2		40		)16
b3	1.	50		)59
1		25	0.017	
12		55	0.022	
13		0.35		)14
l4		75	0.030	
15	0.	50	0.020	
16	0.7	25	0.0	)29
17	0.	60	0.0	)24

Dimension in mm/inches



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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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