

Nch 60V 27A Power MOSFET

V _{DSS}	60V
R _{DS(on)} (Max.)	14.6mΩ
I _D	±27A
P _D	69W

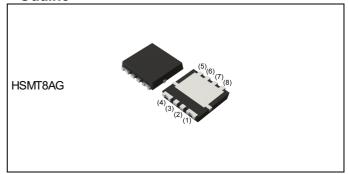
- 1) Small high-powered package reduces mounting area by 64% at a maximum
- 2) Realization of high mounting reliability by original terminal and plating treatment
- 3) AEC-Q101 Qualified

Application

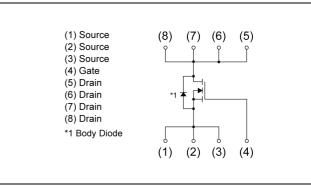
Features

ADAS/Info./Lighting/Body

Outline



Inner circuit



Packaging specifications

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	Packing	Embossed Tape					
	Reel size (mm)	330					
Туре	Tape width (mm)	12					
	Quantity (pcs)	3000					
	Taping code	TCB					
	Marking	L270BK					

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	60	V
Continuous drain current	V _{GS} = 10V	I _D *1	±27	Α
Pulsed drain current	I _{DP} *2	±54	Α	
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *3	15	Α	
Avalanche energy, single pulse	E _{AS} *3	17	mJ	
Power dissipation		P _D *1	69	W
Junction temperature	T _j	150	°C	
Operating junction and storage temper	rature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Lloit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	1.8	°C/W

● Electrical characteristics (T_a = 25°C)

Darameter	Symbol Conditions		Values			Lleit
Parameter	Symbol	OI CONDITIONS		Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		34	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	1	1	1	μΑ
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	ı	±500	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 44\mu A$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = 44μA referenced to 25°C	-	-5.0	-	mV/°C
Static drain - source	D *4	V _{GS} = 10V, I _D = 27A	-	11.3	14.6	m0
on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 13A	-	14.8	21.0	mΩ
Gate resistance	R_G	f = 1MHz, open drain	-	2.2	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 5V, I _D = 10A	6.8	-	-	S

^{*1} T_c=25°C, Limited only by maximum temperature allowed.

^{*2} Pw≤10µs , Duty cycle≤1%

^{*3} L \simeq 0.1mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

Dorameter	Symbol	Conditions	Values			Lleit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	990	-	
Output capacitance	C _{oss}	V _{DS} = 30V	-	235	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	17	1	
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	15	1	
Rise time	t _r *4	I _D = 10A	-	10	1	no
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 3\Omega$	-	36	-	ns
Fall time	t _f *4	$R_G = 1\Omega$	-	8	-	

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Daramatar	Cymala al	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate charge	O *4		V _{GS} = 10V	-	15.0	-	
Total gate charge	Q_g^{*4}	V _{DD} ≈ 30V I _D = 10A		-	8.1	-	»C
Gate - Source charge	Q _{gs} *4		V _{GS} = 4.5V	-	2.8	-	nC
Gate - Drain charge	Q _{gd} *4			-	2.9	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T = 25°C	1	-	27	Α
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	54	Α
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_{S} = 27A$	-	-	1.5	V
Reverse recovery time	t _{rr} *4	I _S = 10A, V _{GS} =0V	-	31	-	ns
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/µs	-	29	-	nC

Power Dissipation: P_D/P_{Dmax}. [%]

• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

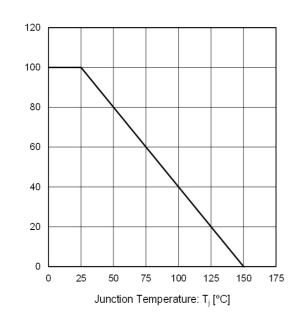
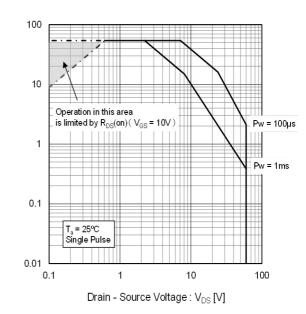


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

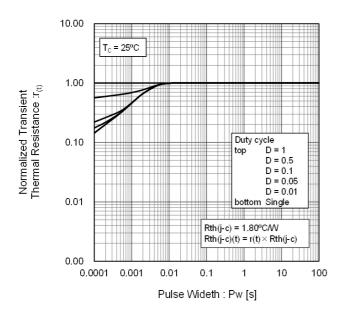
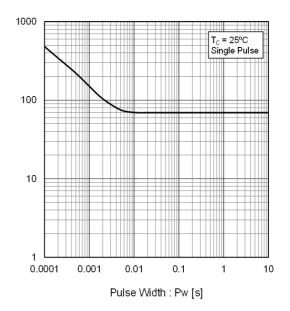
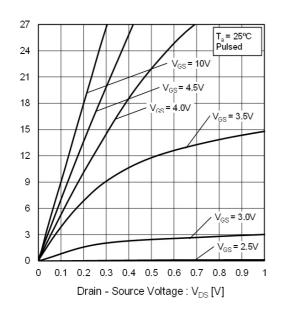


Fig.4 Single Pulse Maximum Power dissipation



Peak Transient Power: P[W]

Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.6 Typical Output Characteristics(II)

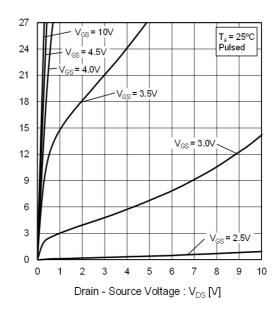
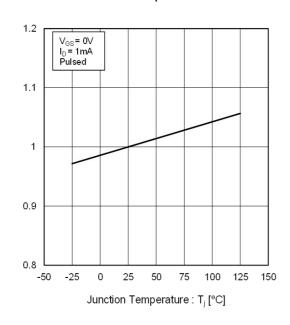


Fig.7 Breakdown Voltage vs.
Junction Temperature



Drain Current : I_D [A]

Fig.8 Typical Transfer Characteristics

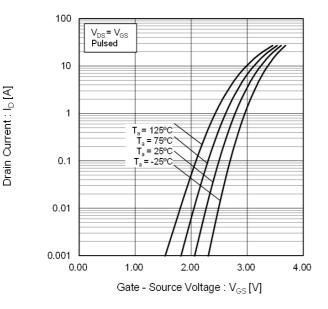


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

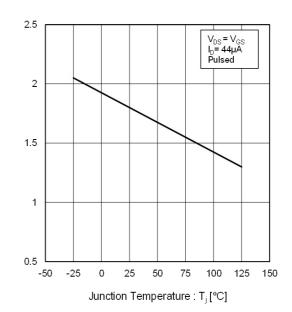
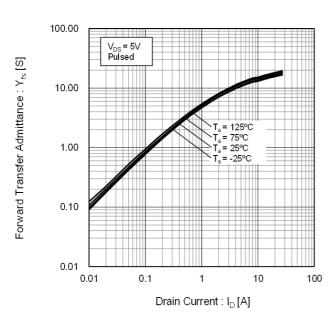


Fig.10 Forward Transfer Admittance vs.
Drain Current



Gate Threshold Voltage : $V_{GS(th)}$ [V]

Fig.11 Drain Current Derating Curve

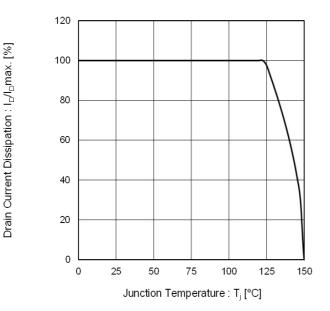
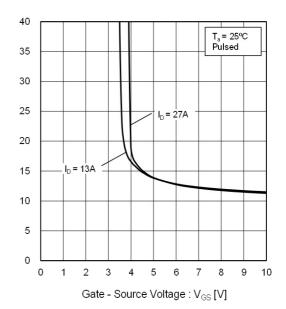


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance : $R_{\text{DS}(\text{on})} \left[\text{m} \Omega \right]$

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

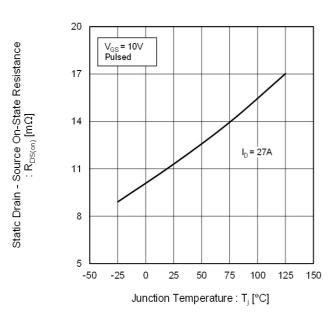


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

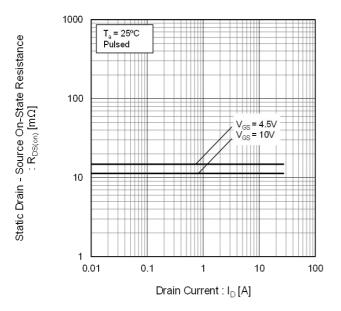
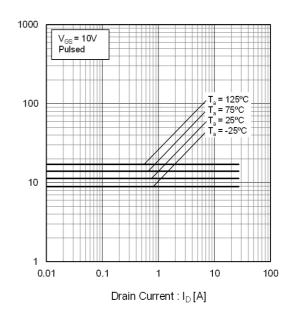


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)



Static Drain - Source On-State Resistance : $R_{DS(on)}\left[m\Omega\right]$

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

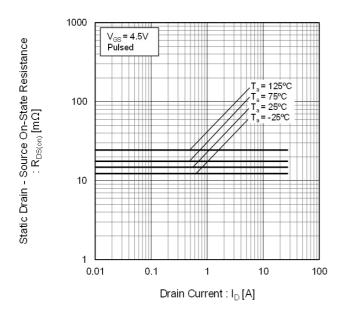


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

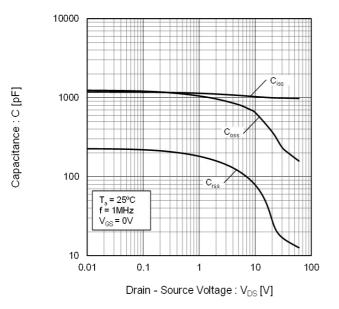
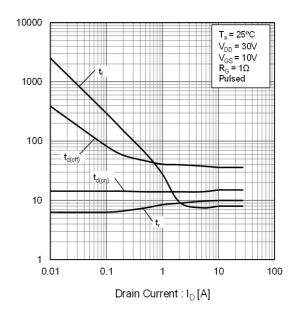


Fig.18 Switching Characteristics



Switching Time : t [ns]

Fig.19 Dynamic Input Characteristics

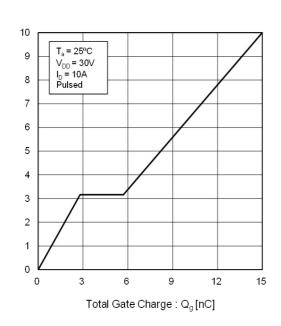
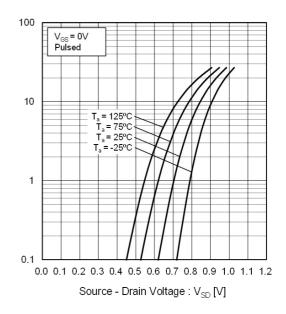


Fig.20 Source Current vs.

Source Drain Voltage



Gate - Source Voltage : V_{GS} [V]

Source Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

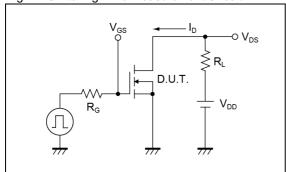


Fig.2-1 Gate Charge Measurement Circuit

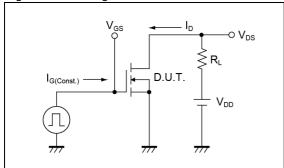


Fig.3-1 Avalanche Measurement Circuit

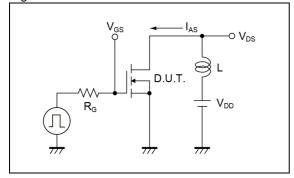


Fig.1-2 Switching Waveforms

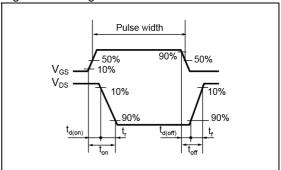


Fig.2-2 Gate Charge Waveform

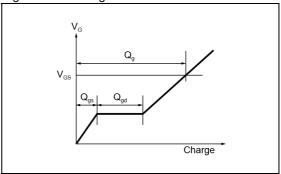
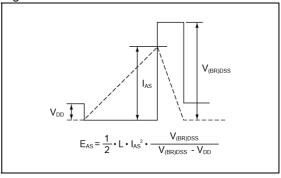


Fig.3-2 Avalanche Waveform



Dimensions

Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	MILIMETERS		HES	
DIM	MIN	MAX	MIN	MAX	
Α	0.70	0.90	0.028	0.035	
A1	0.00	0.05	0.000	0.002	
Ð	0.27	0.37	0.011	0.015	
b1	0.69	0.79	0.027	0.031	
b2	2.50	2.70	0.098	0.106	
C	0.10	0.30	0.004	0.012	
D	3.10	3.30	0.122	0.130	
Е	2.90	3.10	0.114	0.122	
е	0.65		0.0	0.026	
e1	0.	78	0.031		
e2	0.	57	0.022		
HD	3.20	3.40	0.126	0.134	
H	3.20	3.40	0.126	0.134	
┙	0.07	0.25	0.003	0.010	
L1	0.07	0.25	0.003	0.010	
Lp	0.20	0.40	0.008	0.016	
Lp1	0.25	0.45	0.010	0.018	
Lp2	2.10	2.50	0.083	0.098	
Х	1	0.10	-	0.004	
у	ı	0.10	-	0.004	

DIM	MILIME	ETERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
b3	-	0.47	-	0.019	
b4	-	0.89	-	0.035	
b5	-	2.70	-	0.106	
l1	-	0.50	-	0.020	
12	14	0.55	-	0.022	
13	-	2.40	-	0.094	
14	-	3.40	-	0.134	

Dimension in mm/inches



Notice

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ſ	JÁPAN	USA	EU	CHINA
Ī	CLASSⅢ	CL ACCIII	CLASS II b	СГУССШ
ſ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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