


MC42004A6W-BNMLW-V2	4 x 20	4mm Character Height	LCD Module
Specification			
Version: 1		Date: 31/10/2016	
Revision			

Display Features			
Character Count	4 x 20		
Appearance	White on Black		
Logic Voltage	5V		
Interface	Parallel		
Font Set	English / Japanese		
Display Mode	Transmissive		
Character Height	4.03mm		
LC Type	BSTN		
Module Size	77.00 x 47.00 x 14.50 mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COB	Box Quantity	Weight / Display
LED Backlight	White	45 pcs	28.88 grams



RoHS
compliant

* - For full design functionality, please use this specification in conjunction with the ST7066U specification. (Provided Separately)

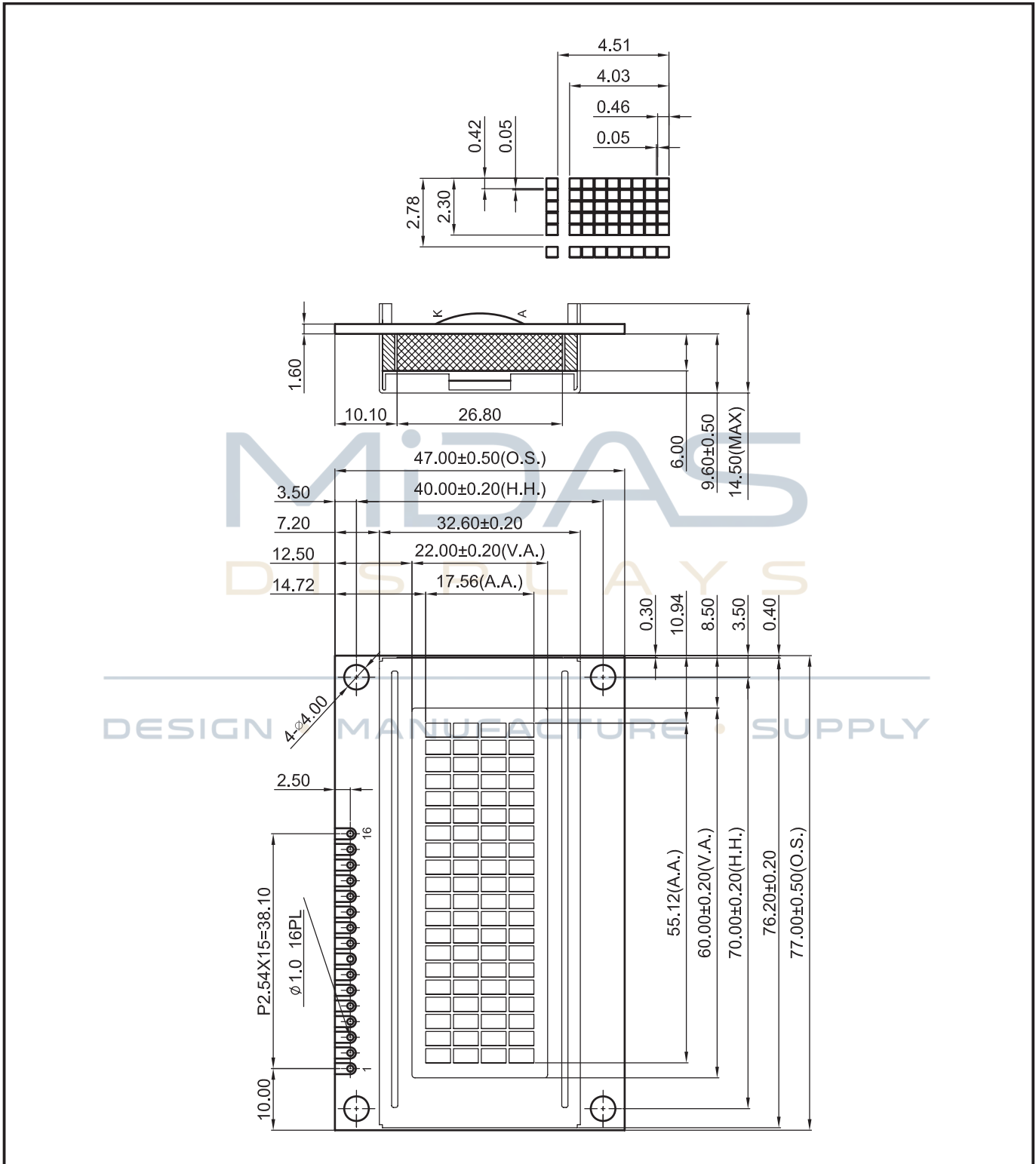
Display Accessories	
Part Number	Description
MCCMDB-16SIL	LCD Interconnect board, can be driven from either a PC or a single Board computer with a USB output.
MCCBL1A16SLIP-16DILS-150	16 Way, Single in-line to Dual In-line connector Cable.
MCCBL1A16SLIP-16SILS-150	16 Way, Single in-line to Single In-line connector Cable.

Optional Variants		
Fonts	Appearances	Voltage
English / European English / Russian	Black on Yellow/Green Black on White Black on Red Black on Amber	3V



Mechanical Specifications

Module Size	77.00 x 47.00 x 14.50 (With Backlight)				W x H x D mm
Viewing Area	60.00 x 22.00	W x H mm	Hole-to-Hole	70.00 x 40.00	W x H mm
Character Size	2.30 x 4.03	W x H mm	Character Pitch	0.48 x 0.48	W x H mm
Dot Size	0.42 x 0.46	W x H mm	Dot Pitch	0.05 x 0.05	W x H mm



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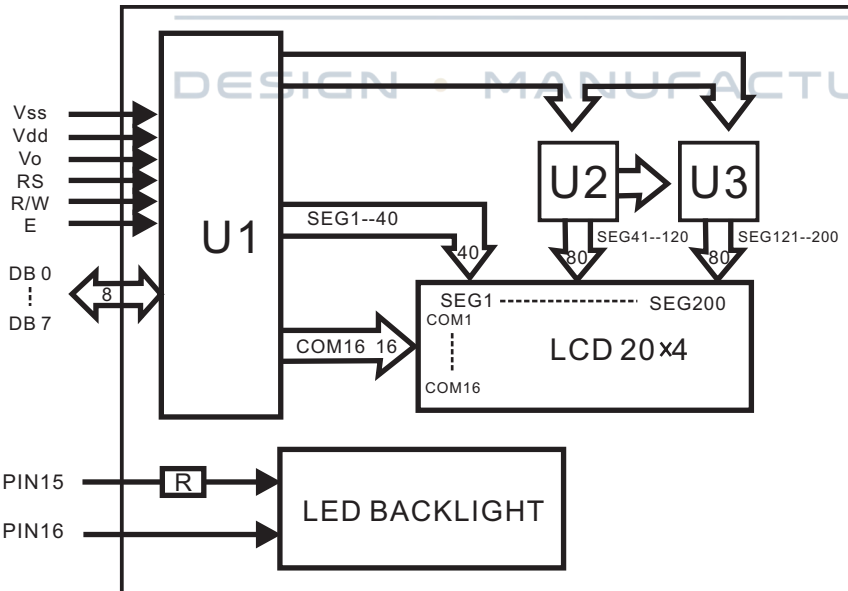


Pin Layout

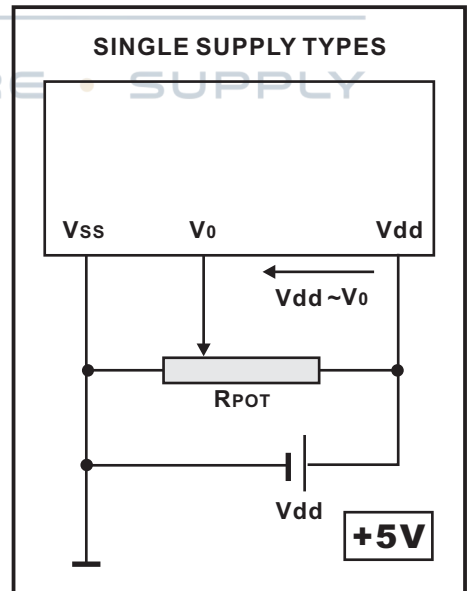
PI	SYMBOL	DESCRIPTION	REMARKS
1	Vss	GND	
2	Vdd	Power supply for LCM	5.0V
3	V0	Contrast Adjust	
4	RS	Register Select Signal	
5	R/W	Data Read /Write	
6	E	Enable Signal	
7	DB0	Data bus line	
8	DB1	Data bus line	
9	DB2	Data bus line	
10	DB3	Data bus line	
11	DB4	Data bus line	
12	DB5	Data bus line	
13	DB6	Data bus line	
14	DB7	Data bus line	
15	LED+	Power supply for BKL	5.0V
16	LED-	Power supply for BKL	

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Block Diagram



Power Supply Diagram



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Font Map

Upper 4bit Lower 4bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)															
LLLH	(2)															
LLHL	(3)															
LLHH	(4)															
LHLL	(5)															
LHLH	(6)															
LHHL	(7)															
LHHH	(8)															
HLLL	(1)															
HLLH	(2)															
HLHL	(3)															
HLHH	(4)															
HHLL	(5)															
HHLH	(6)															
HHHL	(7)															
HHHH	(8)															

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Absolute Maximum Ratings

Item	Symbol	Condition	Min	Typ	Max	Unit
Power Supply (LOGIC)	Vdd	25°C	-0.3	---	7.0	V
Power Supply (LCD)	V0	25°C	Vdd -13.5	---	Vdd +0.3	V
Input Voltage	Vin	25°C	-0.3	---	Vdd +0.3	V
Operating Temperature	Vopr	---	-20	---	70	C
Storage Temperature	Vstg	---	-30	---	80	C

Electronic Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Input Voltage	Vlcm = Vdd	---	---	5.0	---	V
Supply Current	Idd	Vdd=5V	---	1.5	---	mA
Driving Voltage for LCD Panel	Vlcd = (Vdd - V0)	-20°C	4.25	---	4.70	V
		0°C	4.25	---	4.75	
		25°C	4.25	4.50	4.80	
		50°C	4.20	---	4.70	
		70°C	4.20	---	4.65	

LCD Characteristics

For STN/FSTN LCD Panel Types						
Item	Symbol	Condition	Min	Typ	Max	Unit
Viewing Angle	$\Phi 2 - \Phi 1$	K = 4	40°	---	---	Deg
	Θ		60°			
Contrast Ratio	K	---	6	10	---	---
Response Time (Rise)	TR	---	---	150	250	ms
Response Time (Fall)	TF	---	---	150	250	ms

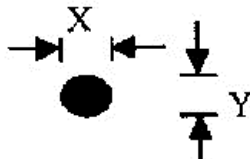
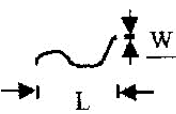
LED Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
LED Forward Voltage	Vf	25°C If=15mA	---	3.0	---	V
LED Forward Current*	If	25°C	---	30	---	mA
LED Reverse Current	Ir	25°C Vr=5.0V	---	---	30	μA
LED Colour Range	X Coordinate	25°C If=15mA	0.26	---	0.30	---
	Y Coordinate		0.27	---	0.31	---
LED Brightness (Without LCD)	Lv	25°C If=15mA	---	330	---	cd/m ²
LED Brightness Uniformity	Lvmin/Lvmax	25°C If=15mA	70	---	---	Ratio
LED Life Time	---	25°C If=15mA	20K	---	---	Hours

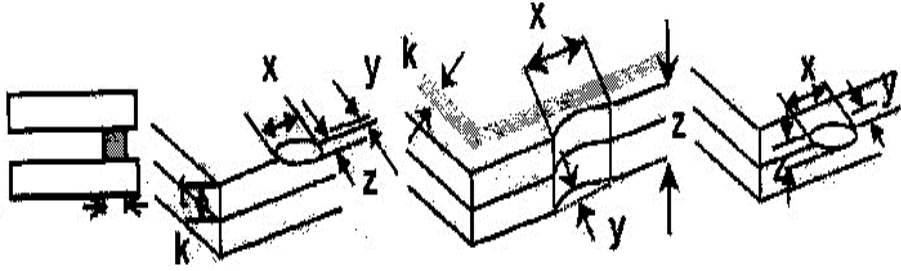

Attention: It is constant current, not constant voltage, which should be applied when driving the LED backlight, please ensure you adhere to this rule.

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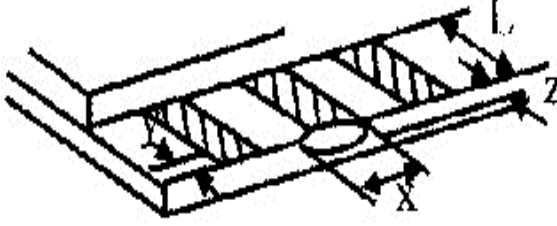
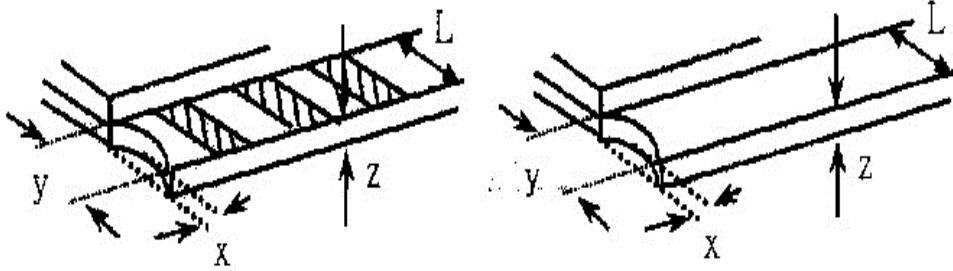
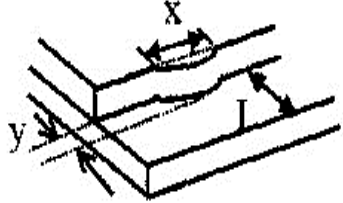
Inspection specification

NO	Item	Criterion	AQL													
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65													
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5													
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$  <table border="1" data-bbox="805 918 1332 1176"> <thead> <tr> <th>SIZE</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>2</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \Phi$</td> <td>0</td> </tr> </tbody> </table>	SIZE	Acceptable Q TY	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi$	0	2.5			
		SIZE	Acceptable Q TY													
$\Phi \leq 0.10$	Accept no dense															
$0.10 < \Phi \leq 0.20$	2															
$0.20 < \Phi \leq 0.25$	1															
$0.25 < \Phi$	0															
3.2 Line type : (As following drawing)  <table border="1" data-bbox="694 1310 1332 1556"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.02 < W \leq 0.03$</td> <td rowspan="2">2</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.03 < W \leq 0.05$</td> </tr> <tr> <td>---</td> <td>$0.05 < W$</td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable Q TY	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	---	$0.05 < W$	As round type	2.5	
Length	Width	Acceptable Q TY														
---	$W \leq 0.02$	Accept no dense														
$L \leq 3.0$	$0.02 < W \leq 0.03$	2														
$L \leq 2.5$	$0.03 < W \leq 0.05$															
---	$0.05 < W$	As round type														
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	<table border="1" data-bbox="821 1680 1332 1982"> <thead> <tr> <th>Size Φ</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.20$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.50$</td> <td>3</td> </tr> <tr> <td>$0.50 < \Phi \leq 1.00$</td> <td>2</td> </tr> <tr> <td>$1.00 < \Phi$</td> <td>0</td> </tr> <tr> <td>Total Q TY</td> <td>3</td> </tr> </tbody> </table>	Size Φ	Acceptable Q TY	$\Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$1.00 < \Phi$	0	Total Q TY	3	2.5
Size Φ	Acceptable Q TY															
$\Phi \leq 0.20$	Accept no dense															
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$1.00 < \Phi$	0															
Total Q TY	3															

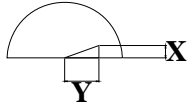


NO	Item	Criterion	AQL																		
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination																			
06	Chipped glass	<p>Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length:</p> <p>6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="443 958 1353 1169"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is total length of each chip.</p> <p>6.1.2 Corner crack:</p>  <table border="1" data-bbox="443 1601 1353 1812"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is the total length of each chip.</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	2.5
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
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$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			



NO	Item	Criterion	AQL																
06	Glass crack	<p>Symbols :</p> <p>x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="363 840 1284 940"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>6.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="434 1321 1260 1438"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged.</p> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table border="1" data-bbox="774 1742 1332 1841"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$x \leq a$</td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$x \leq a$	2.5
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$																	
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$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$																	
y: width	x: length																		
$y \leq 1/3L$	$x \leq a$																		



NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB · COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  $X * Y \leq 2\text{mm}^2$	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65



NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65
		12.12 Visual defect outside of VA is not considered to be rejection.	

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Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) T_{ãæ} have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) T_{ãæ} have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, T_{ãæ} have the right to modify the version.)

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DISPLAYS

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