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MCOB21605GX-EYP	2 x 16	Euro/Jap/Cyrillic	OLED Module
Specification			
Version: 1		Date: 04/05/2013	
Revision			
1	02/05/2013	First issue	

Display Features			
Character Count	2 x 16		
Appearance	Yellow on Black		
Logic Voltage	5V		
Interface	Parallel		
Font Set	English / European / Cyrillic		
Character Height	5.57 mm		
Module Size	80.00 x 36.00 x 9.10 mm		
Operating Temperature	-40°C ~ +85°C	Box Quantity	Weight / Display
Construction	COB	---	---

RoHS compliant

Display Accessories	
Part Number	**Description**

Optional Variants	
Appearance	**Voltage**

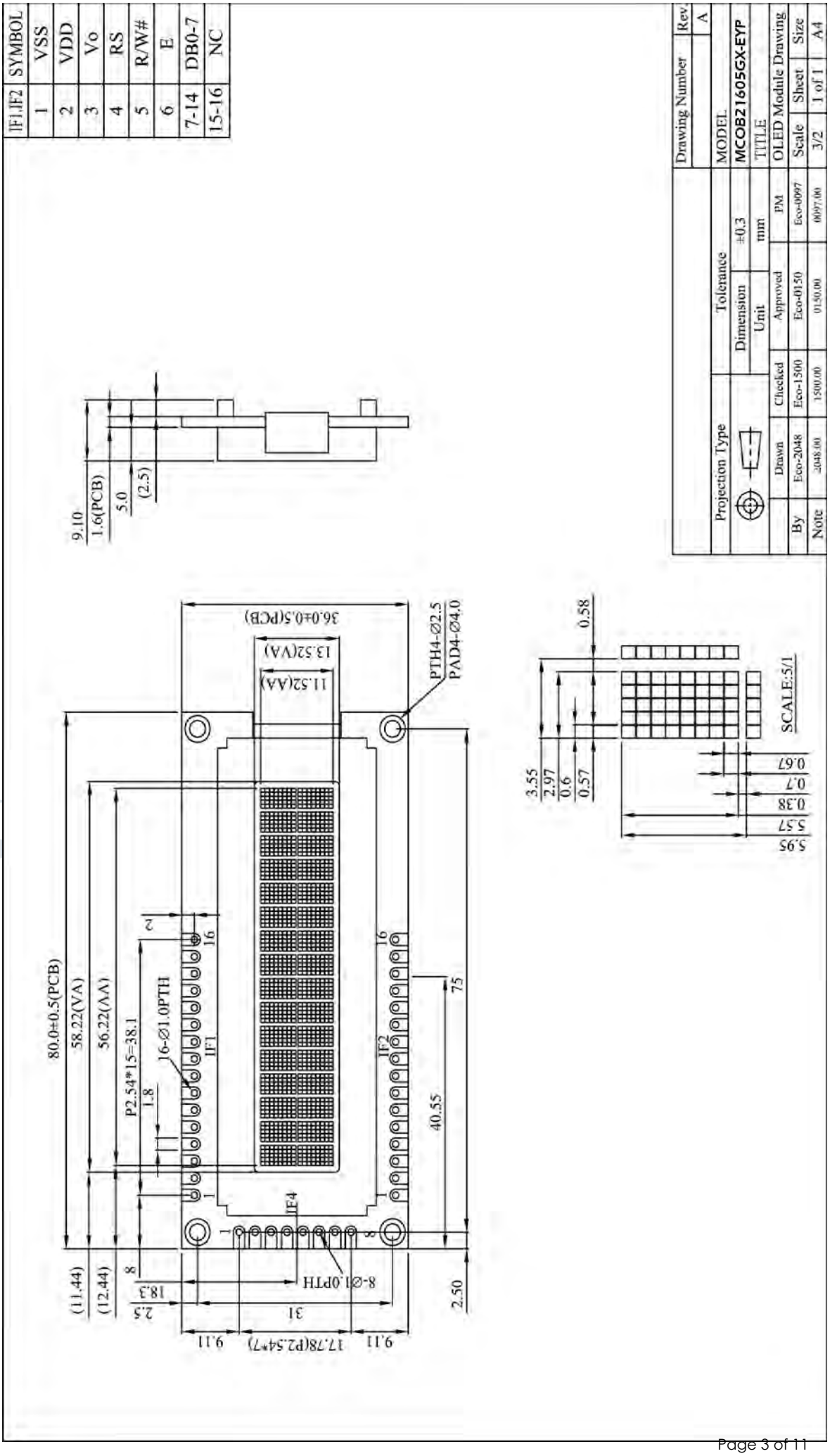
Functions and Features

- 2 lines x 16 characters
- Built-in controller
- Parallel or serial MPU interface (Default 6800 MPU parallel)
- +2.8V ~ +5.3V Power Supply
- viewing angle “Free”
- Wide Temperature -40℃ ~ +80℃ (Operating)
- Sunlight Readable Technology
- RoHS compliant

Mechanical Specification

Item	Description	
Product No.	MCOB21605GX-EYP	
Viewing Area	58.22(W)×13.52(H)	mm
Module Size	80.0(W)×36.0(H)×9.1 (D)	mm
Dot Size	0.57(W)×0.67(H)	mm
Dot Pitch	0.60(W)×0.70(H)	mm
Display Format	16 characters (W)×2 lines (H)	
Duty Ratio	1/16	Duty
Controller	SSD1311 or Equivalent	
Interface	6800 (Default) 8Bit 8080 (Option) SPI (Option) I2C (Option)	

Mechanical Drawing



Pin Description

Parallel Interface (default):

Pin No.	Symbol	External Connection	Description
1	VSS	Power Supply	Ground
2	VDD	Power Supply	Supply Voltage for OLED and logic
3	Vo	-	Contrast Adjustment
4	RS(D/C#)	MPU	Register select signal. H: DATA, L: Command
5	R/W# (WR#)	MPU	6800-interface: Read/Write select signal, R/W=1: Read R/W: =0: Write 8080-interface: Active LOW Write signal.
6	E or /RD	MPU	6800-interface: Operation enable signal. Falling edge triggered. 8080-interface: Active LOW Read signal.
7-14	DB0~DB7	MPU	8-bit Bi-directional data bus lines
15-16	NC	-	No Connect

DC Characteristics

Item	Symbol	Condition	Min.	Type	Max.	Unit
Power Supply for Logic	VDD	(Wide Voltage I/O Application)	2.8	5.0	5.3	Volt
Input Voltage for I/O Pins	Vi	(Wide Voltage I/O Application)	2.8	5.0	5.3	Volt
Input Voltage	VIL	L level	0	-	0.2 VDD	Volt
Input Voltage	VIH	H level	0.8 VDD	-	VDD	Volt
Output Voltage	VOL	L level	0	-	0.1 VDD	
Output Voltage	VOH	H level	0.9 VDD	-	VDD	
Power Supply Current for OLED	IDD	Note	-	30		mA
Sleep Mode Current for VDD	IDD,SLEEP			1	10	μA

Note:

VDD = 5.0V, 25% Display Area Turn on.100 cd/㎡
When random texts pattern is running, averagely, about 1/4 of pixels will be on.

Optical Characteristics

Item	Symbol	Min.	Typ	Max.	Unit
Viewing angle range			Free		Degree
Dark Room Contrast	Cr		>10,000:1		
Brightness	Lbr		125		cd/㎡
Peak Emission Wavelength	C.I.E 1931	X=0.45 Y=0.46	X=0.50 Y=0.49	X=0.54 Y=0.53	

Electrical Absolute Ratings

Item	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply for Logic	V_{DD}	-0.3	5.0	5.5	Volt	1,2
Input Voltage for I/O Pins	V_I	-0.3	5.0	5.5	Volt	1,2
Life Time (100 cd/㎡)		---	100,000	---	Hours	3

Note 1: All the above voltages are on the basis of “VSS = 0V”.

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur.

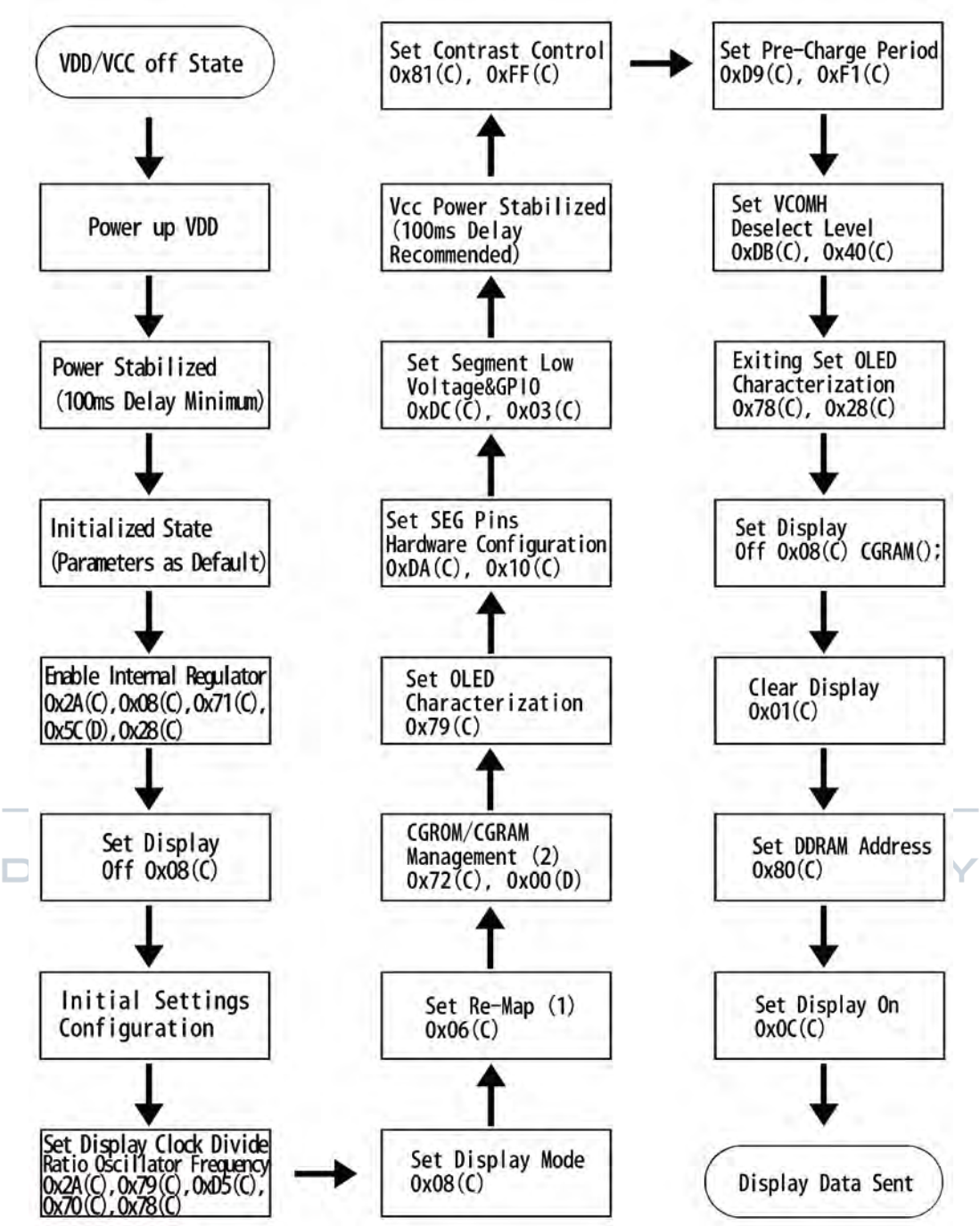
Note 3: Ta = 25°C, 25% Checkerboard.
Software configuration follows Section ACTUAL APPLICATION EXAMPLE Initialization. End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

POWER SUPPLY

Adjust Brightness by Software & Hardware(VR) Adjust Brightness by Software(Only)

Application

Power up Sequence



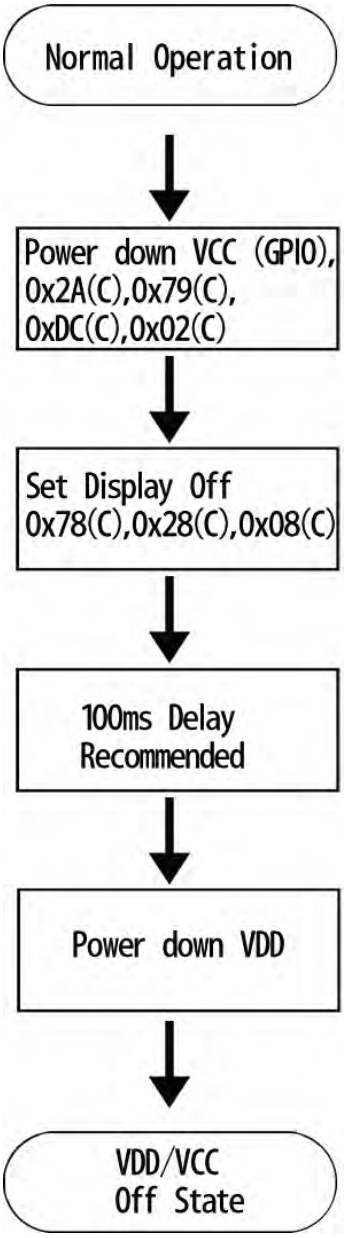
(1) This command could be programmable or defined by pin configuration.

(2) This command could be programmable or defined by pin configuration.

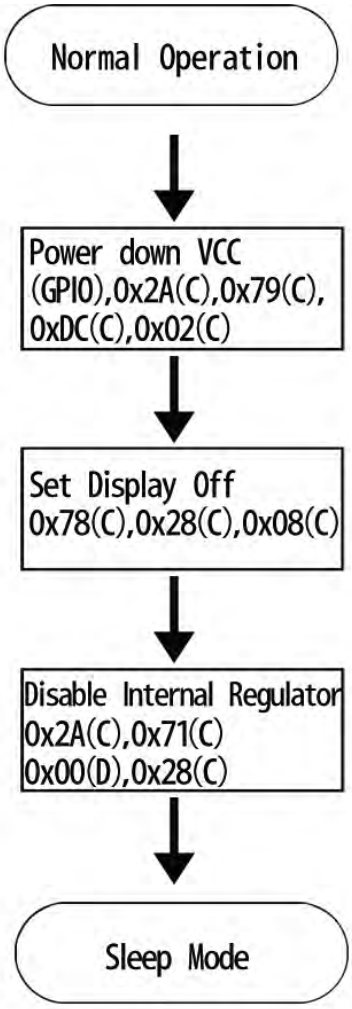
※（C）: Write Command　　※（D）: Write Data

If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.

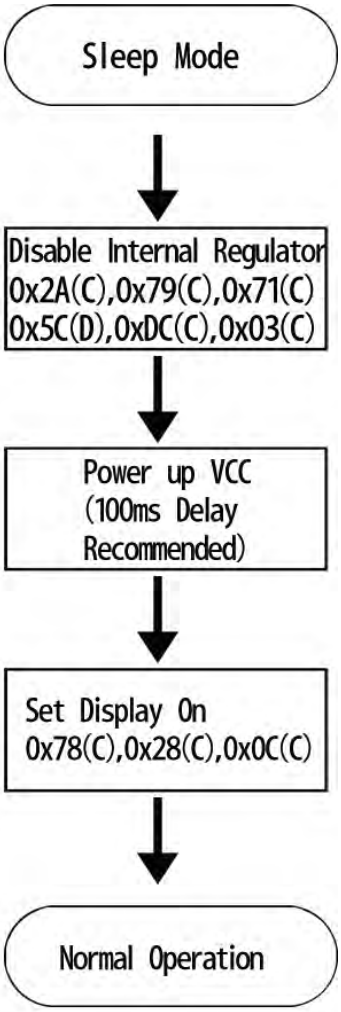
Power down Sequence



Entering Sleep Mode

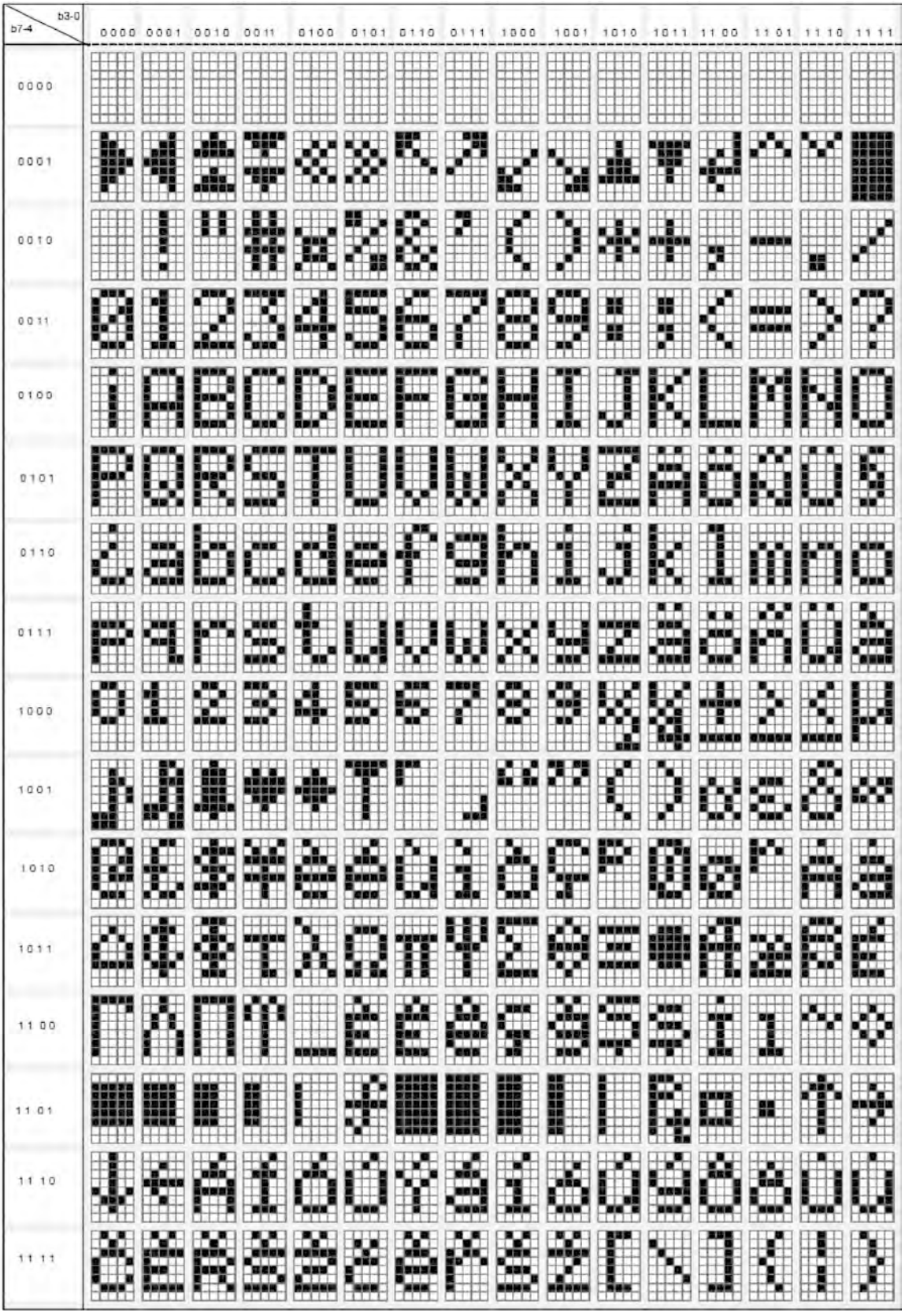


Exiting Sleep Mode

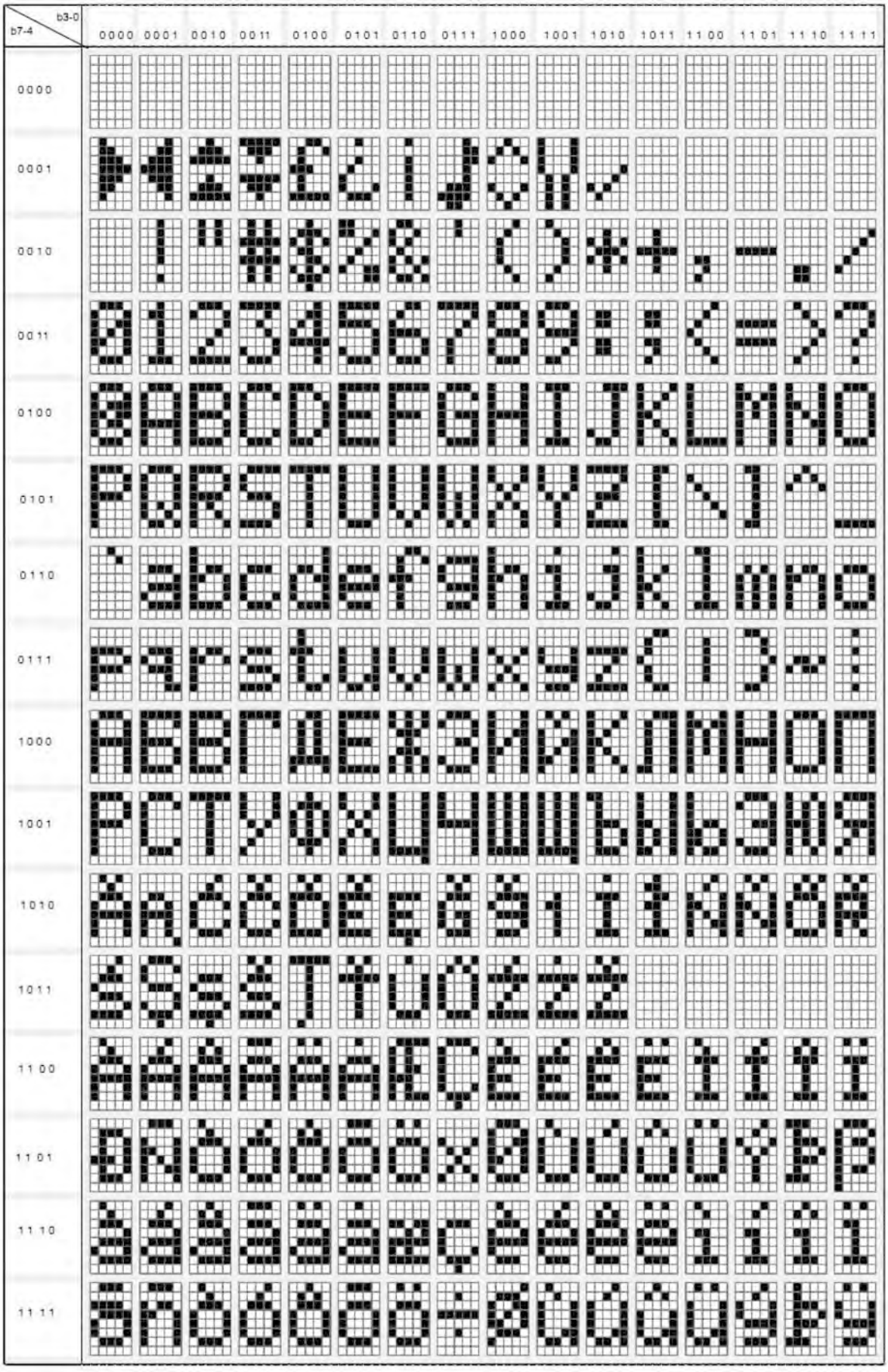


SSD1311 CGROM CHARACTER CODE

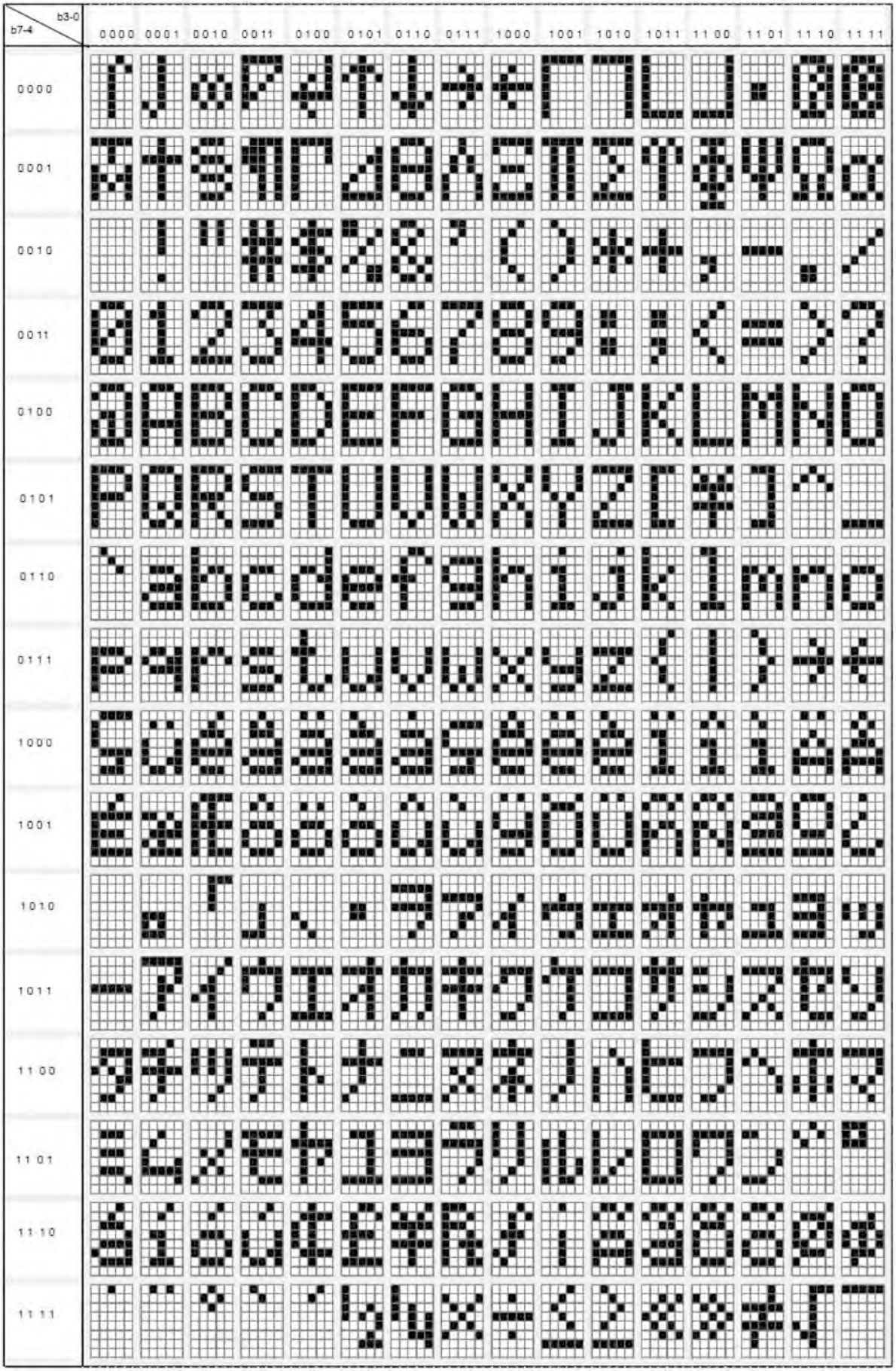
ROM A



ROM B


b3-0
b7-4
0000 0001 0010 0011 0100 0101 0110 0111 1000 1001 1010 1011 1100 1101 1110 1111
0000
0001
0010
0011
0100
0101
0110
0111
1000
1001
1010
1011
1100
1101
1110
1111

ROM C


b7-4
b3-0
0000 0001 0010 0011 0100 0101 0110 0111 1000 1001 1010 1011 1100 1101 1110 1111
0000
0001
0010
0011
0100
0101
0110
0111
1000
1001
1010
1011
1100
1101
1110
1111