MDCOG128064CA6W-BNMLW	128 x 64		LCD Module
Specification			
Version: 1		Date: 03/09/2020	
Revision			
1	30/08/2020	First Issue	

Display Features		RoHS compliant	
Resolution	128 x 64		
Appearance	White on Blue		
Logic Voltage	3.0V		
Interface	Parallel / SPI		
Font Set	N/A		
Display Mode	Transmissive		
LC Type	Blue STN		
Module Size	89.70 x 49.80 x 6.00mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COG	Box Quantity	Weight / Display
LED Backlight	White	---	---

* - For full design functionality, please use this specification in conjunction with the ST7565P specification. (Provided Separately)

Display Accessories	
Part Number	**Description**
MCIB-12	UNO 32 Breakout Board with SD Card and LED BKL driver.
MPBV-7	30-Way FFC to Cable and Wires 0.5mm Pitch.

Optional Variants	
Appearances	**Voltage**

General Specification

The Features of the Module is description as follow:

- Module dimension: 89.7 x 49.8 x 6.0 mm

- View area: 66.8 x 35.5 mm

- Active area: 63.98 x 31.98 mm

- Number of Dots: 128 x 64

- Dot size: 0.48 x 0.48 mm

- Dot pitch: 0.50x 0.50 mm

- LCD type: STN Negative, Blue Transmissive

- Duty: 1/65duty , 1/9 Bias

- View direction: 6 o'clock

- Backlight Type: LED White

- IC: ST7565P

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Interface Pin Function

Pin No.	Symbol	Description
1	/CS1	This is the chip select signal. When /CS1 = "L" and CS2 = "H", then the chip select becomes active, and data/command I/O is enabled.
2	/RES	When /RES is set to "L", the register settings are initialized (cleared). The reset operation is performed by the /RES signal level.
3	A0	This is connect to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command. A0 = "H": Indicates that D0 to D7 are display data. A0 = "L": Indicates that D0 to D7 are control data.
4	/WR	• When connected to 8080 series MPU, this pin is treated as the "/WR" signal of the 8080 MPU and is LOW-active. The signals on the data bus are latched at the rising edge of the /WR signal. • When connected to 6800 series MPU, this pin is treated as the "R/W" signal of the 6800 MPU and decides the access type : When R/W = "H": Read.
5	/RD	• When connected to 8080 series MPU, this pin is treated as the "/RD" signal of the 8080 MPU and is LOW-active. The data bus is in an output status when this signal is "L". • When connected to 6800 series MPU, this pin is treated as the "E" signal of the 6800 MPU and is HIGH-active. This is the enable clock input terminal of the 6800 Series MPU.
6	D0	
7	D1	This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus. When the serial interface (SPI-4) is selected (P/S = "L") : D7 : serial data input (SI) ; D6 : the serial clock input (SCL). D0 to D5 should be connected to VDD or floating. When the chip select is not active, D0 to D7 are set to high impedance.
8	D2	
9	D3	
10	D4	
11	D5	
12	D6	
13	D7	
14	VDD	Power supply Power supply
15	GND	Ground
16	VOUT	DC/DC voltage converter. Connect a capacitor between this terminal and VSS or VDD

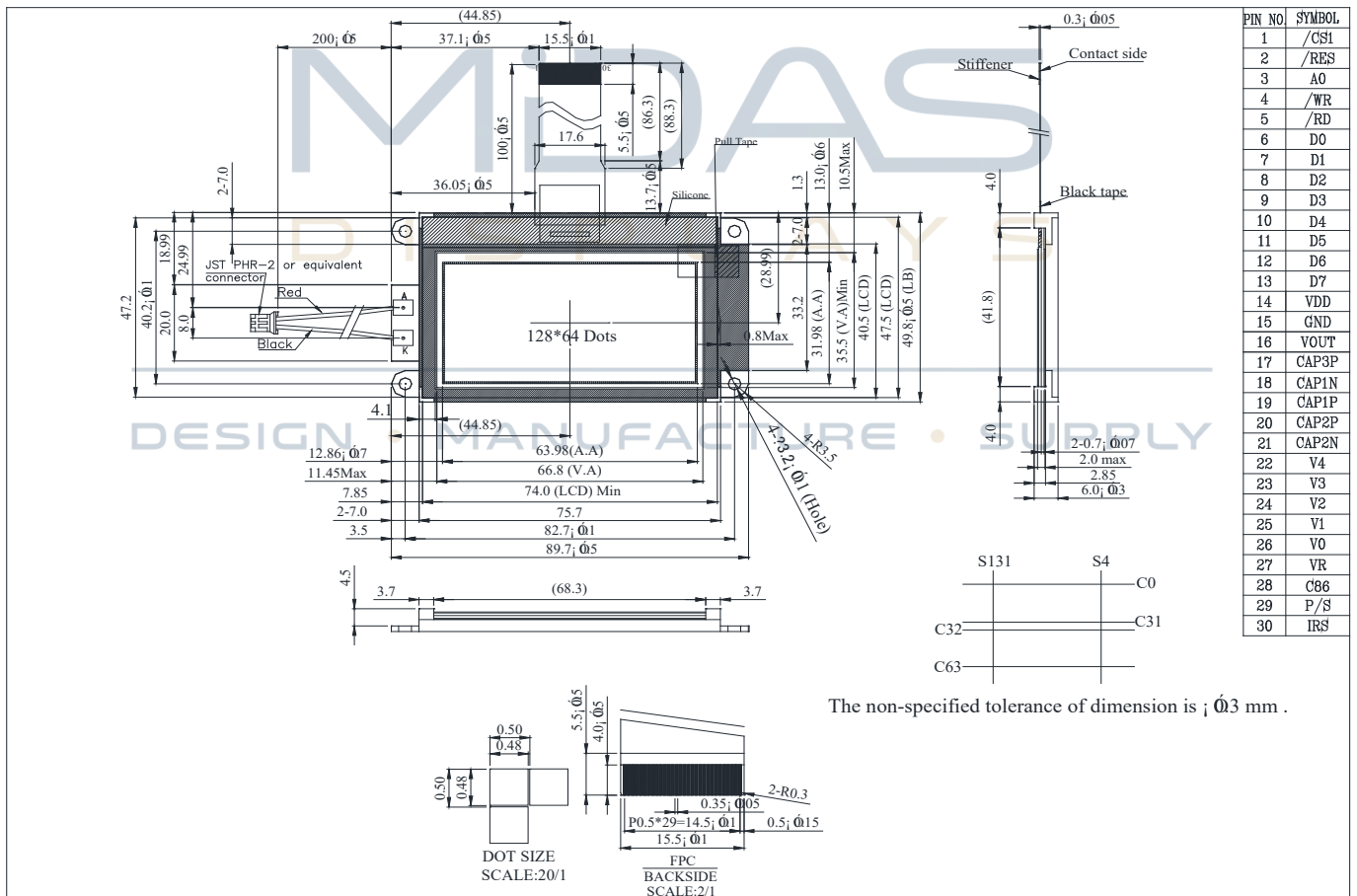| 17	CAP3+	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
18	CAP1-	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.
19	CAP1+	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
20	CAP2+	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.
21	CAP2-	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal.
22	V4	This is a multi-level power supply for the liquid crystal drive. The voltage Supply applied is determined by the liquid crystal cell, and is changed through the use of a resistive voltage divided or through changing the impedance using an op. amp. Voltage levels are determined based on Vss, and must maintain the relative magnitudes shown below. $V0 \geqq V1 \geqq V2 \geqq V3 \geqq V4 \geqq Vss$ When the power supply turns ON, the internal power supply circuits produce the V1 to V4 voltages shown below. The voltage settings are selected using the LCD bias set command.
23	V3	
24	V2	
25	V1	
26	V0	
27	VR	Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider. IRS = "L" : the V0 voltage regulator internal resistors are not used. IRS = "H" : the V0 voltage regulator internal resistors are used.
28	C86	This is the MPU interface selection pin. C86 = "H": 6800 Series MPU interface. C86 = "L": 8080 Series MPU interface.
29	P/S	This pin configures the interface to be parallel mode or serial mode. P/S = "H": Parallel data input/output. P/S = "L": Serial data input. The following applies depending on the P/S status: 	P/S	Data/Command	Data	Read/Write	Serial Clock	|---|---|---|---|---|	"H"	A0	D0 to D7	/RD, /WR	X		"L"	A0	SI (D7)	Write only	SCL (D6)	 When P/S = "L", D0 to D5 must be fixed to "H". /RD (E) and /WR (R/W) are fixed to either "H" or "L". The serial access mode does NOT support read operation.

30	IRS	This terminal selects the resistors for the V0 voltage level adjustment. IRS = "H": Use the internal resistors IRS = "L": Do not use the internal resistors. The V0 voltage level is regulated by an external resistive voltage divider attached to the VR

Contour Drawing

PIN NO	SYMBOL
1	/CS1
2	/RES
3	A0
4	/WR
5	/RD
6	D0
7	D1
8	D2
9	D3
10	D4
11	D5
12	D6
13	D7
14	VDD
15	GND
16	VOUT
17	CAP3P
18	CAP1N
19	CAP1P
20	CAP2P
21	CAP2N
22	V4
23	V3
24	V2
25	V1
26	V0
27	VR
28	C86
29	P/S
30	IRS

The non-specified tolerance of dimension is ¡Ó3 mm .

128*64 Dots

DOT SIZE
SCALE:20/1

FPC
BACKSIDE
SCALE:2/1

Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	θ	CR≧2	0	─	20	ψ= 180°
	θ	CR≧2	0	─	40	ψ= 0°
	θ	CR≧2	0	─	30	ψ= 90°
	θ	CR≧2	0	─	30	ψ= 270°
Contrast Ratio	CR	─	─	3	─	─
Response Time	T rise	─	─	200	300	ms
	T fall	─	─	250	350	ms

Definition of Operation Voltage (Vop)   **Definition of Response Time (Tr , Tf)**
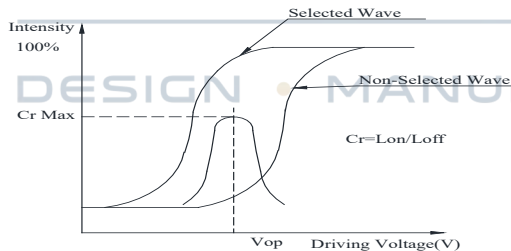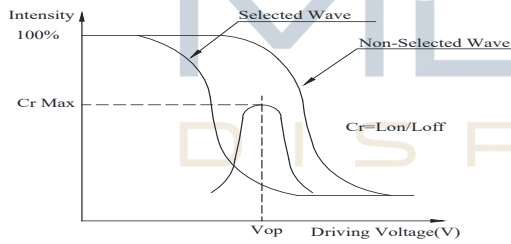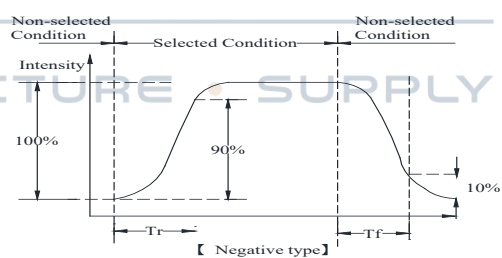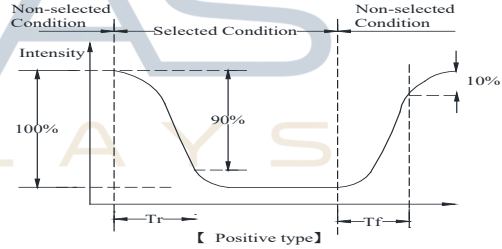
Conditions :

Operating Voltage : Vop   Viewing Angle(θ，φ) : 0°， 0°

Frame Frequency : 64 HZ Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle(CR≧2)
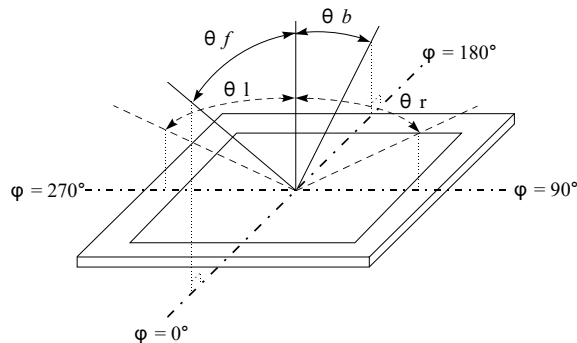
Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Power Supply Voltage	VDD	-0.3	—	3.6	V
Power supply voltage (VDD standard)	V0, VOUT	-0.3	—	14.5	V
Power supply voltage (VDD standard)	V1, V2, V3, V4	-0.3	—	V0+0.3	V

Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	2.8	3.0	3.2	V
Supply Voltage For LCD *Note	V_{OP}	Ta=-20℃	—	—	—	V
		Ta=25℃	9.3	9.5	9.7	V
		Ta=70℃	—	—	—	V
Input High Volt.	V_{IH}	—	0.8 V_{DD}	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	V_{SS}	—	0.2 V_{DD}	V
Output High Volt.	V_{OH}	—	0.8 V_{DD}	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	V_{SS}	—	0.2 V_{DD}	V
Supply Current	I_{DD}	V_{DD}=3.0V	—	—	2.0	mA

Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance

Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	15	40	60	mA	V=5.0V(Note 1)
Supply Voltage	V	—	5.0	—	V	—
Reverse Voltage	VR	—	—	5	V	—
Colour	X	0.26	—	0.32	—	—
Coordinate	Y	0.26	—	0.32	—	—
Luminance (Without LCD)	IV	800	1000	—	cd/m^2	ILED=40mA
LED Life Time (For Reference only)	—	—	50K	—	Hr.	ILED=40mA 25℃,50-60%RH, (Note 2)
Color	White					

Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).

Note 1: Supply current minimum value is only for reference since LED brightness efficiency keeps enhancing. Current consumption becomes less and less to achieve the same luminance.

Note 2:50K hours is only an estimate for reference.
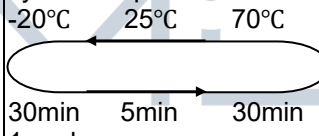
LED B\L Drive Method
1.Drive from A , K
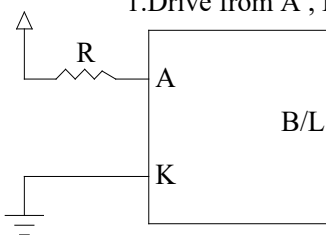
Reliability

Content of Reliability Test (Wide temperature, -20℃~70℃)

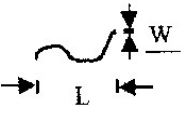| Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	____
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity storage	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20℃ 25℃ 70℃ 30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	____
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	____
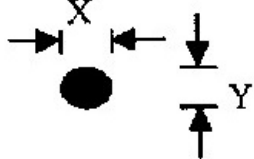
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal
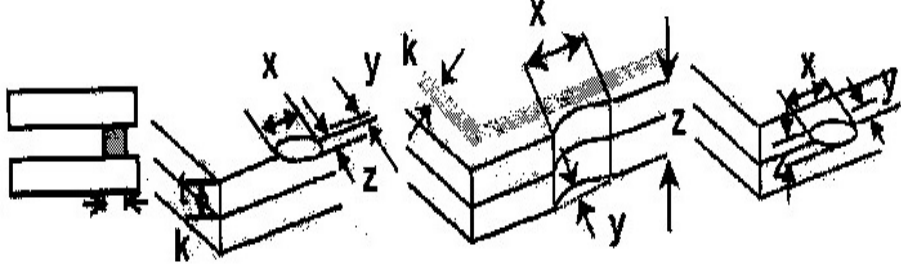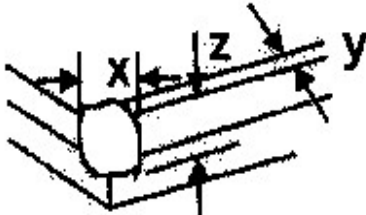   **Temperature and humidity after remove from the test chamber.**

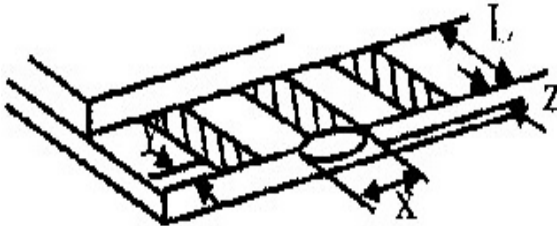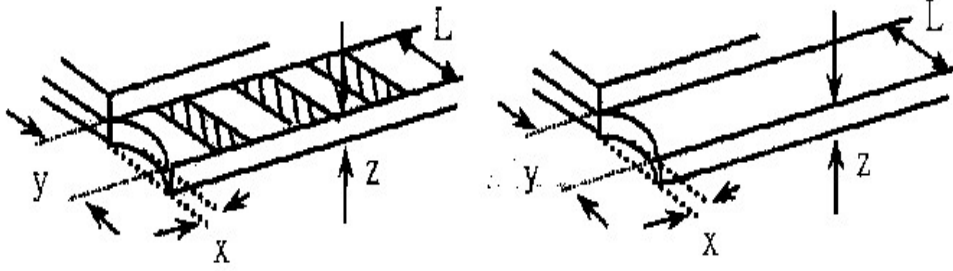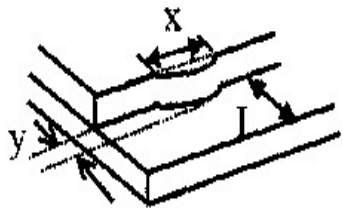**Note3: The packing have to including into the vibration testing.**
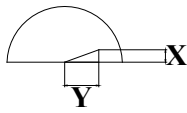
Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65
02	Black or white spots on LCD (display only)	2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5

03 — LCD black spots, white spots, contamination (non-display)

3.1 Round type : As following drawing
$\Phi=(x + y) / 2$

SIZE	Acceptable Q TY
Φ≦0.10	Accept no dense
0.10＜Φ≦0.20	2
0.20＜Φ≦0.25	1
0.25＜Φ	0

AQL 2.5

3.2 Line type : (As following drawing)

Length	Width	Acceptable Q TY
---	W≦0.02	Accept no dense
L≦3.0	0.02＜W≦0.03	2
L≦2.5	0.03＜W≦0.05	2
---	0.05＜W	As round type

AQL 2.5

04 — Polarizer bubbles

If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.

Size Φ	Acceptable Q TY
Φ≦0.20	Accept no dense
0.20＜Φ≦0.50	3
0.50＜Φ≦1.00	2
1.00＜Φ	0
Total Q TY	3

AQL 2.5

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	

NO	Item	Criterion	AQL
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels:	2.5

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is the total length of each chip.

NO	Item	Criterion	AQL
06	Glass crack	Symbols : x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad : (see table below) 6.2.2 Non-conductive portion: (see table below) ⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙If the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack. 	2.5

6.2.1 Chip on electrode pad :

y: Chip width	x: Chip length	z: Chip thickness
$y \leqq 0.5mm$	$x \leqq 1/8a$	$0 < z \leqq t$

6.2.2 Non-conductive portion:

y: Chip width	x: Chip length	z: Chip thickness
$y \leqq L$	$x \leqq 1/8a$	$0 < z \leqq t$

6.2.3 Substrate protuberance and internal crack.

y: width	x: length
$y \leqq 1/3L$	$x \leqq a$

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit.	0.65
		8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.	2.5
		8.3 Backlight doesn't light or color wrong.	0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.	2.5
		9.2 Bezel must comply with job specifications.	0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination.	2.5
		10.2 COB seal surface may not have pinholes through to the IC.	2.5
		10.3 The height of the COB should not exceed the height indicated in the assembly diagram.	0.65
		10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.	2.5
		10.5 No oxidation or contamination PCB terminals.	2.5
		10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.	0.65
		10.7 The jumper on the PCB should conform to the product characteristic chart.	0.65
		10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.	2.5
		10.9 The Scraping testing standard for Copper Coating of PCB $X * Y <= 2mm2$	2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB.	2.5
		11.2 No cold solder joints, missing solder connections, oxidation or icicle.	2.5
		11.3 No residue or solder balls on PCB.	2.5
		11.4 No short circuits in components on PCB.	0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65
		12.12 Visual defect outside of VA is not considered to be rejection.	

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Precautions in use of LCD Modules

(1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3) Don't disassemble the LCM.

(4) Don't operate it above the absolute maximum rating.

(5) Don't drop, bend or twist LCM.

(6) Soldering: only to the I/O terminals.

(7) Storage: please storage in anti-static electricity container and clean environment.

(8) Midas have the right to change the passive components, including R3, R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)

(9) Midas have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Midas have the right to modify the version.)

(10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

(11) Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.

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Material List of Components for RoHs

1. Midas Displays hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A：The Harmful Material List

Material	Cd	Pb	Hg	Cr6+	PBB	PBDE	DEHP	BBP	DBP	DIBP
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.										

2.Process for RoHS requirement：(only for RoHS inspection)

(1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.：

   Reflow：250℃,30 seconds Max.；

   Connector soldering wave or hand soldering：320℃, 10 seconds max.

(3) Temp. curve of reflow, max. Temp.：235±5℃；

   Recommended customer's soldering temp. of connector：280℃, 3 seconds.

Recommendable Storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.