

Sauls Wharf House Crittens Road Great Yarmouth Norfolk NR31 0AG Telephone +44 (0)1493 602602 Email:sales@midasdisplays.com Email:tech@midasdisplays.com www.midasdisplays.com

MDT0240CIH-RGB	800 x 480	RGB Interface	TFT Module				
Specification							
Version: 1 Date: 25/01/2022							
	Revision						
1 2	3/01/2022	First issue					

Display F	eatures		
Display Size	2.40"		
Resolution	800 x 480		
Orientation	Landscape		
Appearance	RGB		1
Logic Voltage	3.3V		oHS ompliant
Interface	RGB		$\odot$
Brightness	1100 cd/m <sup>2</sup>		mnliant
Touchscreen	SPLA	1500	mphant
Module Size	58.00 x 41.30 x 2.58mm		
Operating Temperature	-20°C ~ +70°C		
Pinout	40 way FFC	Box Quantity	Weight / Display
Pitch	0.5mm		

\* - For full design functionality, please use this specification in conjunction with the HX8283-A specification.(Provided Separately)

Display Accessories				
Part Number	Description			
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.			
MDIB-11	The MDIB-11 is an HDMI to RGB converter. Ideal for connecting a range of Midas TFT displays to a Single Board Computer such as the Raspberry Pi.			

Optional Variants					
Appearances	Voltage				

## \* Description

This is a color active matrix LTPS LCD using Low Temperature Poly-silicon TFT's (Thin Film Transistors) as an active switching devices. This module is composed of a Transmissive type LTPS-LCD Panel, driver circuit, back-light unit. The resolution of a 2.4 " LTPS-LCD contains 800X480 pixels, and can display up to 16.7M colors.

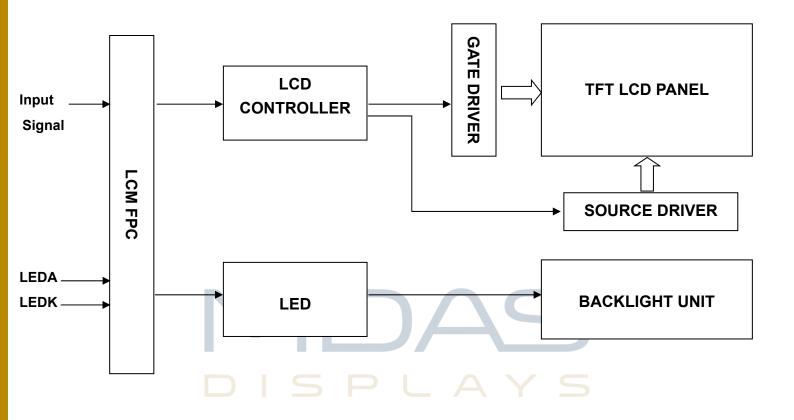
## \* Features

General Information	Specification	I luit	Nata
Items	Main Panel	Unit	Note
Display area(AA)	52.32(H)*31.392(V) (2.4 inch)	mm	
Driver element	TFT active matrix	-	
Display colors	16.7M	colors	
Number of pixels	800(RGB)*480	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.0218(H)*0.0654(V)	mm	
Viewing angle	Free	o'clock	
Controller IC	HX8283-A	5	
LCM Interface	18/24BIT RGB	-	
Display mode	Transmissive /Normally Black	-	
Operating temperature	-20~+70	$^{\circ}$ C	
Storage temperature	MANU <sub>30</sub> A <sub>80</sub> TURE	SUPPL	Y

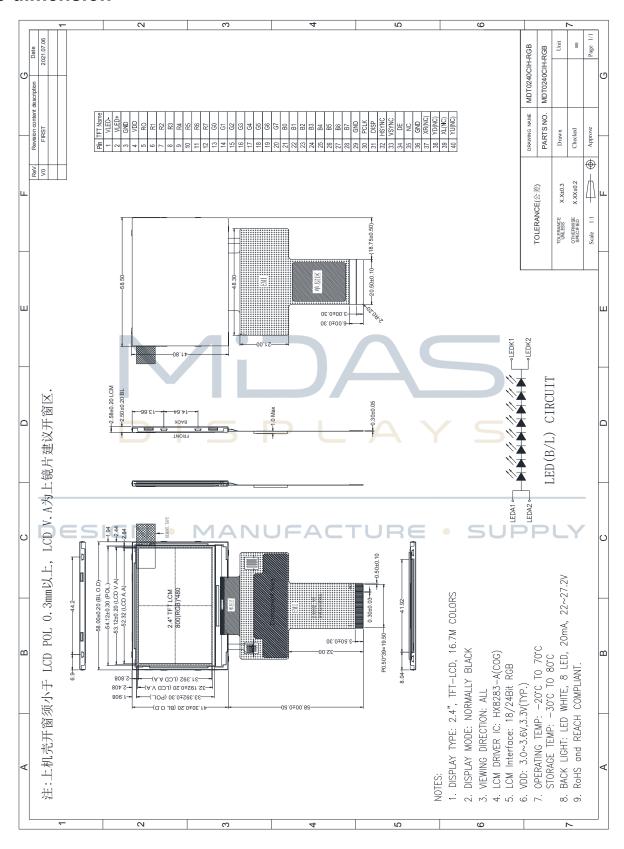
## \* Mechanical Information

	Item	Min.	Тур.	Max.	Unit	Note
NA L L-	Horizontal(H)	-	58.00	-	mm	
Module	Vertical(V)	-	41.30	-	mm	
size	Depth(D)	-	2.58			
Weight		-	15	-	g	

## **Block Diagram**



## **Outline dimension**



# **Input terminal Pin Assignment**

•			
NO.	SYMBOL	DISCRIPTION	I/O
1	VLED-	Cathode pin OF backlight	Р
2	VLED+	Anode pin of backlight	Р
3	GND	Ground.	Р
4	VDD	Supply voltage(3.3V).	Р
5	R0	Red data input.	I/O
6	R1	Red data input.	I/O
7	R2	Red data input.	I/O
8	R3	Red data input.	I/O
9	R4	Red data input.	I/O
10	R5	Red data input.	I/O
11	R6	Red data input.	I/O
12	R7	Red data input.	I/O
13	G0	Green data input.	I/O
14	G1	Green data input.	I/O
15	G2	Green data input.	I/O
16	G3	Green data input.	I/O
17	G4	Green data input.	I/O
18	G5	Green data input.	I/O
19	G6	Green data input.	I/O
20	G7	Green data input.	I/O
21	В0	Blue data input.	I/O
22	B1	Blue data input.	I/O
23	B2	Blue data input.	I/O
24	В3	Blue data input.	I/O
25	B4	Blue data input.	I/O

B5	Blue data input.	I/O			
B6	Blue data input.	I/O			
В7	lue data input.				
GND	Ground.	Р			
PCLK	Dot clock signal for RGB interface operation.	I			
DISP	Standby setting for testing, it should be connected to VDD in normal operation mode. If connected to GND, the IC is in standby mode.	I			
HSYNC	Line synchronizing signal for RGB interface operation.				
VSYNC	Frame synchronizing signal for RGB interface operation.	I			
DE	Data enable signal for RGB interface operation.	I			
NC	NC				
GND	Ground.	Р			
XR(NC)	Touch panel Right Glass Terminal	A/D			
YD(NC)	Touch panel Bottom Film Terminal	A/D			
XL(NC)	Touch panel LIFT Glass Terminal	A/D			
YU(NC)	Touch panel Top Film Terminal	A/D			
	B6 B7 GND PCLK DISP HSYNC VSYNC DE NC GND XR(NC) YD(NC) XL(NC)	B6 Blue data input.  B7 Blue data input.  GND Ground.  PCLK Dot clock signal for RGB interface operation.  Standby setting for testing, it should be connected to VDD in normal operation mode. If connected to GND, the IC is in standby mode.  HSYNC Line synchronizing signal for RGB interface operation.  VSYNC Frame synchronizing signal for RGB interface operation.  DE Data enable signal for RGB interface operation.  NC NC  GND Ground.  XR(NC) Touch panel Right Glass Terminal  YD(NC) Touch panel Bottom Film Terminal  XL(NC) Touch panel LIFT Glass Terminal			

## **LCD Optical Characteristics**

### 1. Optical specification

Item		Symbol	Condition	Min.	Тур.	Max.	Unit.	Note
Contrast R	Contrast Ratio		Θ=0	800	1000			(1)(2)
Doonana tima	Rising	T_ T_	Normal viewing		40	45	meac	(1)(3)
Response time	Falling	T <sub>R+</sub> T <sub>F</sub>	angle		40	45	msec	
Color Gan	nut	S(%)		40	45		%	
	\ \	Wx			0.3208			CA-
	White	WY			0.3581			310
	Б.	Rx			0.5876	+0.04		test
Color Filter	Red	Ry		-0.04	0.3523			
Chromacicity	0	Gx	•		0.3504			
	Green	Gy	1 1/		0.5637			
	Dive	Вх			0.1578			
	Blue	By	L D	A	0.1253			
	Hor	ΘL		70	80			(1)(4)
Viewies es els	Hor.	ΘR	OD: 40	70	80			
Viewing angle	)/an	ΘU	CR>10	70	80			
	S Ver. N	ΘD	ANUFACI	U <sub>70</sub> E	80	PPL	Y	
Option View D	irection			Free				

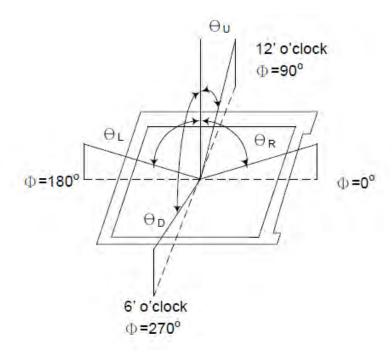
### **Measuring Condition**

15min. warm-up time.

#### **Measuring Equipment**

FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

Note (1): Definition of Viewing Angle:



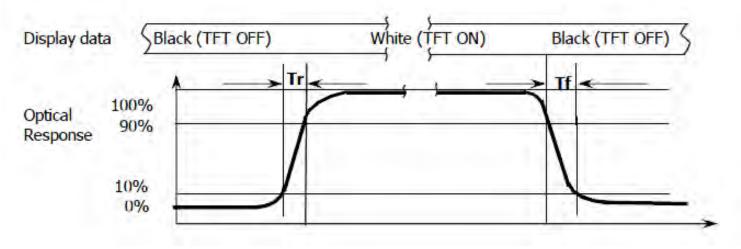
Note (2): Definition of Contrast Ratio(CR) :measured at the center point of panel

CR = 

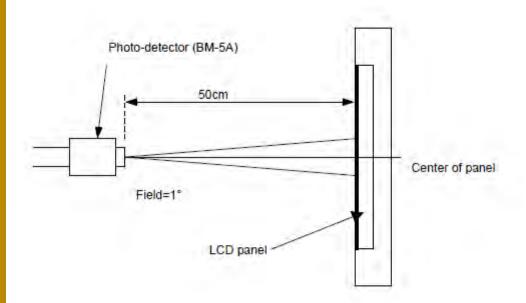
Luminance with all pixels white

Luminance with all pixels black

Note (3): Response Time



Note (4): Definition of optical measurement setup





### **Electrical Characteristics**

#### 1. Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	VCI	-0.3	4.0	V	Note1
Operating temperature	Тор	-20	+70	°C	
Storage temperature	T <sub>ST</sub>	-30	+80	°C	

NOTE1: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

#### 2. DC Electrical Characteristics

Characteristics	Symbol	Min.	Тур.	Max.	Unit	Note
Digital Supply Voltage	VCI	3.0	3.3	3.6	V	
Normal mode Current consumption	IDD 1ANU	FACT	28 TURE	50 SU	mA PPLY	-
Lovel input veltage	V <sub>IH</sub>	0.7* <sub>VCI</sub>		VCI	V	
Level input voltage	VıL	GND		0.3* <sub>VCI</sub>	V	
	Vон	0.8*vci		VCI	V	
Level output voltage	Vol	GND		GND+0.4	V	

#### 3. LED Backlight Characteristics

The back-light system is edge-lighting type with 8 chips LED

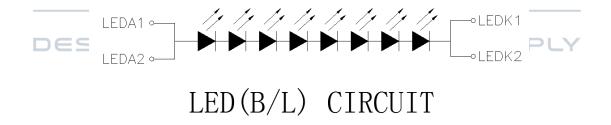
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Forward Current	lF		20		mA	
Forward Voltage	VF		24		V	
LCM Luminance	LV	1000	1100		cd/m2	Note3
LED life time	Hr		50000		Hour	Note1,2
Uniformity	Avg	80			%	Note3

Note1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition:

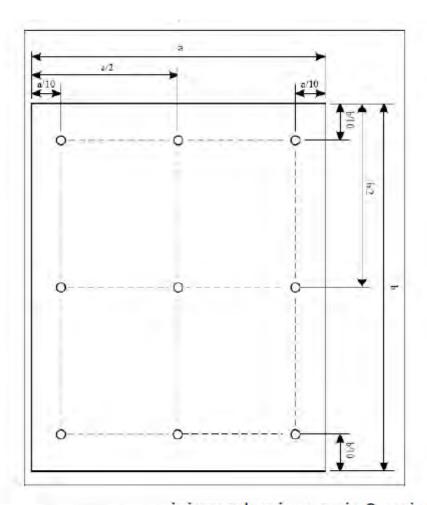
Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25°C and IL=20mA. The LED lifetime could be decreased if operating IL is larger than 20mA. The constant current driving method is suggested.



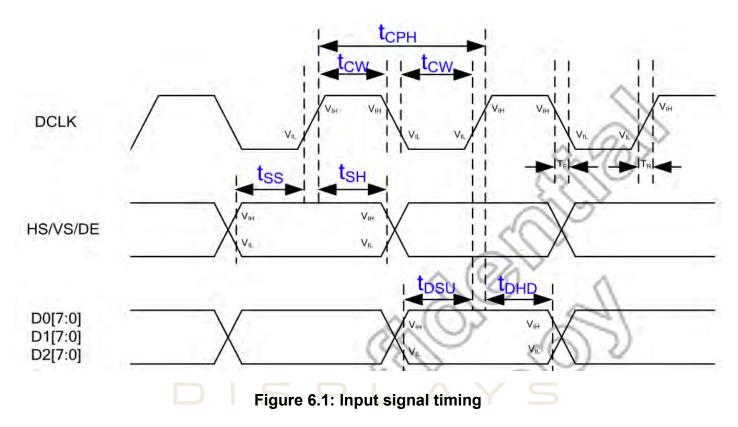
Note (3) Luminance Uniformity of these 9 points is defined as below:



Uniformity =  $\frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$ 

## **AC Characteristics**

#### 1. RGB mode AC electrical characteristics



### Input data/Sync. Parameters

(VCI =3.0V to 3.6V, GND=0V, T A =-20~70℃) UFACTURE • SUPPLY

Parameter	Symphol	,	Spec.	Spec.	
Parameter	Symbol	Min.	Тур.	Max.	Unit
DCLK period	T <sub>CPH</sub>	16.67	-	555.5	ns
DCLK clock high/low width	Tcw	6	-	1	ns
Data setup time	Tosu	5	-	1	ns
Data hold time	T <sub>DHD</sub>	5	-	1	ns
VS/HS/DE setup time	Tss	5	-	-	ns
VS/HS/DE hold time	T <sub>SH</sub>	5	-	-	ns
Input signal rising time	O) T <sub>R</sub>	-	-	10	ns
Input signal falling time	T <sub>F</sub>	-	-	10	ns

Table 6.1: Input data/Sync. Parameters

#### 2. Parallel RGB with DE mode

It just needs DE signal only, when DE only mode enable.

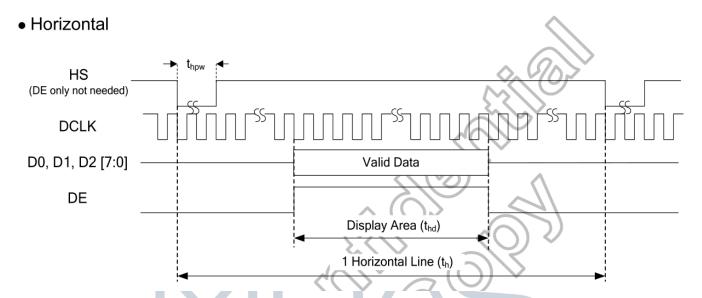


Figure 6.3.1: Horizontal input timing at DE only mode

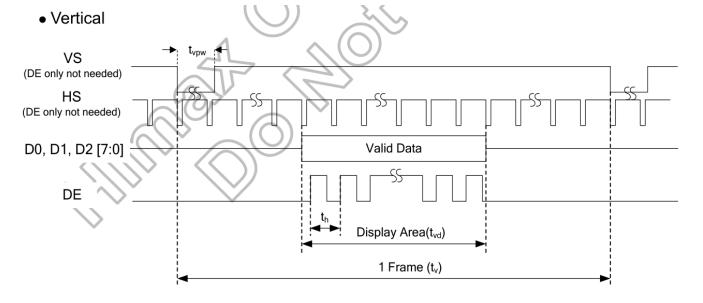
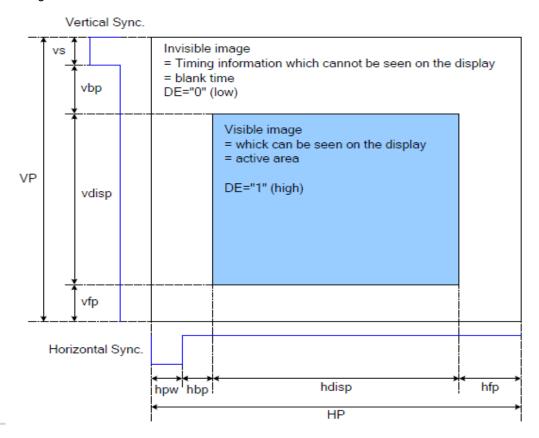


Figure 6.3.2: Vertical input timing at DE only mode

#### 3. Timing Table

The display operation via the RGB interface is synchronized with the VSYNC, HSYNC, and DOTCLK signals. The data can be written only within the specified area with low power consumption by using window address function. The back porch and front porch are used to set the RGB interface timing.



Please refer to the following table for the setting limitation of RGB interface signals.

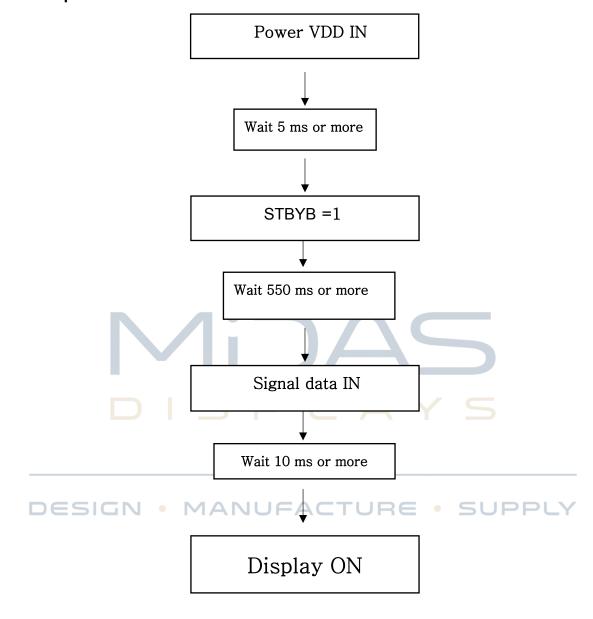
Parameter	Symbol	Min.	Тур.	Max.	Unit
				max.	
DCLK frequency	FCLK	-	(27)		MHz
Horizontal display area	Hdisp	-	800		Clock
Horizontal Sync. Width	hpw	1	8	254	Clock
Horizontal Sync. Back Porch	hbp	1	10	255	Clock
Horizontal Sync. Front Porch	hfp	1	50		Clock
Vertical display area	Vdisp	ı	480	1	Line
Vertical Sync. Width	vs	1	4	1	Line
Vertical Sync. Back Porch	vbp	1	20	-	Line
Vertical Sync. Front Porch	vfp	1	16		Line
Frame-Rate			60		Hz

Note:

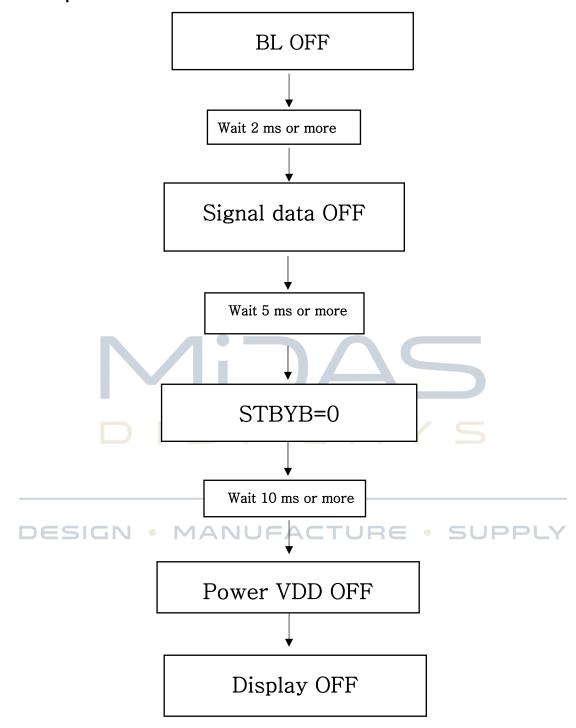
<sup>1.</sup> Typical value are related to the setting frame rate is 60Hz.

## **LCM Power Sequence**

### 1. Power-on Sequence



### 2. Display OFF Sequence



## **LCD Module Out-Going Quality Level**

#### 1. VISUAL & FUNCTION INSPECTION STANDARD

#### 1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

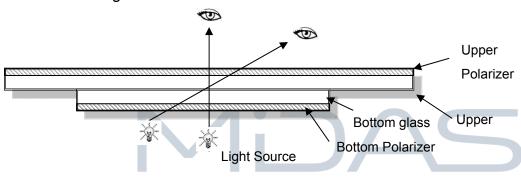
Temperature : 25±5 ℃

Humidity: 65%±10%RH

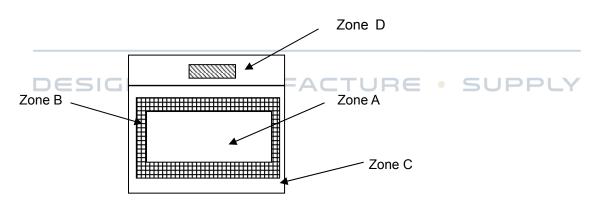
Viewing Angle: Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



#### 1.2 Definition



Zone A: Effective Viewing Area(Character or Digit can be seen)

Zone B: Viewing Area except Zone A

Zone C: Outside (Zone A+Zone B) which can not be seen after assembly by customer

Zone D: IC Bonding Area

Note:As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

### 1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class  $\,\, \mathbb{I} \,$  AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , LCM: Liquid Crystal Module,

No	Items to be inspected	Criteria	Classification of defects
		1) No display, Open or miss line	
1	Functional defects	2) Display abnormally, Short	
		Backlight no lighting, abnormal lighting. etc	
			Major
2	Missing	Missing components and etc	
2	Outline dimension	Overall outline dimension beyond the drawing	
3	Outline dimension	is not allowed,deformation and etc	
4	Color tone	Color unevenness, refer to limited sample	
		Light dot,Dim spot,(Note1)	
5	Spot/Line defect	Polarizer Air Bubble,	
		Polarizer accidented spot and etc.	Minor
6	Soldering appearance	Good soldering , Peeling off is not allowed and etc.	JPPLY
7	LCD/Polarizer	Black/White spot/line, scratch, crack, etc.	

**Note1:** a) Light dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.

b) Dim dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.

## 1.4 Criteria (Visual)

Number	Items	Criteria(mm)	
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of ITO,	(1) The edge of LCD broken	X Y Z	
T: Height of LCD		≤3.0mm	
DESI	(2)LCD corner broken    S   F	X Y Z ≤3.0mm ≤L ≤T	
	(3) LCD crack		
		Crack Not allowed	

	Spot defect	S light dot ( blac	k/white spot , pii	nhole, sta Accentat	<del>iin. etc.)</del> ole Otv		
		Zone Size (mm)	А	7 toooptas	В	С	
	<b>▼ Y</b>	Ф≤0.15	Ignore				
0.0	<b>—</b> ,,	0.15<Φ≤0.25	3(distance ≥ 6mm)				
2.0	X	0.25<Φ≤0.4	2(distance ≥ 6mm)		I	gnore	
	Φ-(Υ.Υ.Υ.)	Ф>0.4	0				
	Φ=(X+Y)/2	② Dim spot (light I	② Dim spot (light leakage、dent、dark spot, etc) Acceptable Qty				
		Size (mm)	А		В	С	
		Φ≤0.15	Ignore			ı	
		0.15<Φ≤0.25	3( distance ≥	3( distance ≥ 6mm)		Ignore	
		0.25<Φ≤0.4	2( distance ≥ 6mm)		'		
		Φ>0.4	0				
		③ Polarizer accidented spot					
		Zone	Acceptable Qty				
		Size (mm)	А		В	С	
		Ф≤0.2		nore			
		0.2<Φ≤0.5	2( distar	nce≧6mr	n)	Ignore	
		Ф>0.5		0			
		4 Polarizer Bubble					
	DESIGN	Zone	CTURE	Accepta	ble Qty	.Y	
		Size (mm)	Α	В		С	
		Ф≤0.2	lgn	ore			
		0.2<Φ≤0.4	3(distanc	e≧6mm)		Ignore	
		Ф>0.4		)			

3.0	LCD Pixel defect	Pixel bad points		
		Item	Zone A	Acceptable Qty
			Random	N≤2
		Bright dot	2 dots adjacent	N≤0
			3 dots adjacent	N≤0
			Random	N≤2
		Dark dot	2 dots adjacent	N≤0
			3 dots adjacent	N≤0
		Distance	<ol> <li>Minimum Distance Between Bright dots.</li> <li>Minimum Distance Between dark dots</li> <li>Minimum Distance Between dark and bright dot.</li> </ol>	5mm
		Total bright a	and dark dot	N≤4
		LCD pane	: Dots appear bright and unchanged I is displaying under black pattern. Dots appear dark and unchanged in	
	DESIGN	MANC	l is displaying under pure red, green	, blue picture.
		2 dot adja	cent 2 dot adjacent	
		2 dot adjacen	t (vertical) 2 dot adjacent (	slant)

	Line defect (LCD	Polarizer hacklight Le		Acce	eptable C	ıty
	black/white line,	Width(mm)	m)	А	В	С
4.0	scratch, stain)	Ф≤0.03	Ignore	Ignore	)	
4.0		0.03 <w≤0.04< td=""><td>L≤3.0</td><td>N≤2</td><td colspan="2">N≤2 Igno</td></w≤0.04<>	L≤3.0	N≤2	N≤2 Igno	
	W: width, L∶ length	0.04 <w≤0.05< td=""><td>L≤2.0</td><td>N≤1</td><td></td><td></td></w≤0.05<>	L≤2.0	N≤1		
	N : Count	W>0.05		Define as spo	t defect	
	Electronic Componen					older joint, m
5.0	ts SMT.	smatch, The positive and negative polarity opposite				
6.0	Display color& Brigh tness.	<ol> <li>Color: Measuring the color coordinates, The measurement stard according to the datasheet or samples.</li> <li>Brightness: Measuring the brightness of White screen, The murement standard according to the datasheet or Samples.</li> </ol>				en, The mea
7.0	LCD Mura/Waving/	Not visible through 5% ND filter in 50% gray or judge by limit sar e if necessary.				oy limit samp
	Hot spot					_

Criteria (functional items)

DESIGN .	MANUFACIURE	SUPPLY
Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed

## **Reliability Test Result**

Item	Condition	Inspection after test
High Temperature Operating	70°C,96HR	
Low Temperature Operating	-20°C, 96HR	
High Temperature Storage	80°C, 96HR	lacancetics often 2. About
Low Temperature Storage	-30°C, 96HR	Inspection after 2~4hours
High Temperature & High	±60°C 00% PH 06 hours	storage at room temperature,
Humidity Operating	100 C, 30 /0 IXI1,30 Hours.	the sample shall be free from
Thermal Shock (Non-operation)	-30°C,30 min ↔ +80°C,30 min, Change time:5min 20CYC.	defects:  1.Air bubble in the LCD;  2.Non-display;
ESD test	Air:±8KV, 5times; Contact:±6KV, 5 times;	3.Missing segments/line;  4.Glass crack;  5.Current IDD is twice higher
Vibration (Non-operation)	Frequency range:10~55Hz. Stroke:1.5mm	than initial value.
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

#### Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance  $> 10M\Omega$ ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
- 6. The color fading mura of polarizing filter should not care.

### **Cautions and Handling Precautions**

#### 1. Handling and Operating the Module

- (1) When the module is assembled, it should be attached to the system firmly.
- Do not warp or twist the module during assembly work.
- (2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
- (4) Do not allow drops of water or chemicals to remain on the display surface.
- If you have the droplets for a long time, staining and discoloration may occur.
- (5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.
- Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static; it may cause damage to the CMOS ICs.
- (9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (10) Do not disassemble the module.
- (11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (12) Pins of I/F connector shall not be touched directly with bare hands.
- (13) Do not connect, disconnect the module in the "Power ON" condition.
- (14) Power supply should always be turned on/off by the item 6.1 Power On Sequence &6.2 Power Off Sequence

#### 2. Storage and Transportation.

- (1) Do not leave the panel in high temperature, and high humidity for a long time.
- It is highly recommended to store the module with temperature from 0 to 35  $\,^\circ\mathbb{C}\,$  and relative humidity of less than 70%
- (2) Do not store the TFT-LCD module in direct sunlight.
- (3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
- (4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.
- In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
- (5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.