


MDT0500D6IH-RGB	800 x 480	RGB Interface	TFT Module
<b>Specification</b>			
Version: 1		Date: 14/03/2020	
<b>Revision</b>			
1	12/03/2020	First issue	

Display Features			
Display Size	5.0"		
Resolution	800 x 480		
Orientation	Landscape		
Appearance	RGB		
Logic Voltage	3.3V		
Interface	RGB		
Brightness	500 cd/m <sup>2</sup>		
Touchscreen	---		
Module Size	120.70 x 75.80 x 2.80mm		
Operating Temperature	-30°C ~ +80°C		
Pinout	40 way FFC		Box Quantity
Pitch	0.5mm		Weight / Display
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\* - For full design functionality, please use this specification in conjunction with the ST7262 specification.(Provided Separately)

Display Accessories	
Part Number	Description
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.
MCIB14/16	HDMI-to-LVDS interface board, with voltage generation.

Optional Variants	
Appearances	Voltage



## Summary

TFT 5.0" is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This TFT LCD has a 5.0 inch diagonally measured active display area with 800 x 480 (800 horizontal by 480 vertical pixel) resolution.

## General Specifications

- Size: 5.0 inch
- Dot Matrix: 800× 3(RGB) × 480 dots
- Module dimension: 120.7(W) ×75.8(H) ×2.8mm
- Active area: 108(W) ×64.8 (H) mm
- Dot pitch: 0.135(W) ×0.135(H) mm
- LCD type: TFT, Normally Black, Transmissive
- View Direction: 80/80/80/80
- Aspect Ratio: 16:9
- Driver IC: ST7262 or equivalent
- Interface: 24 bit R.G.B.
- Backlight Type: LED ,Normally White
- With /Without TP: Without TP
- Surface: Anti-Glare

\*Color tone slight changed by temperature and driving voltage.



# Interface

## 1. LCM PIN Definition

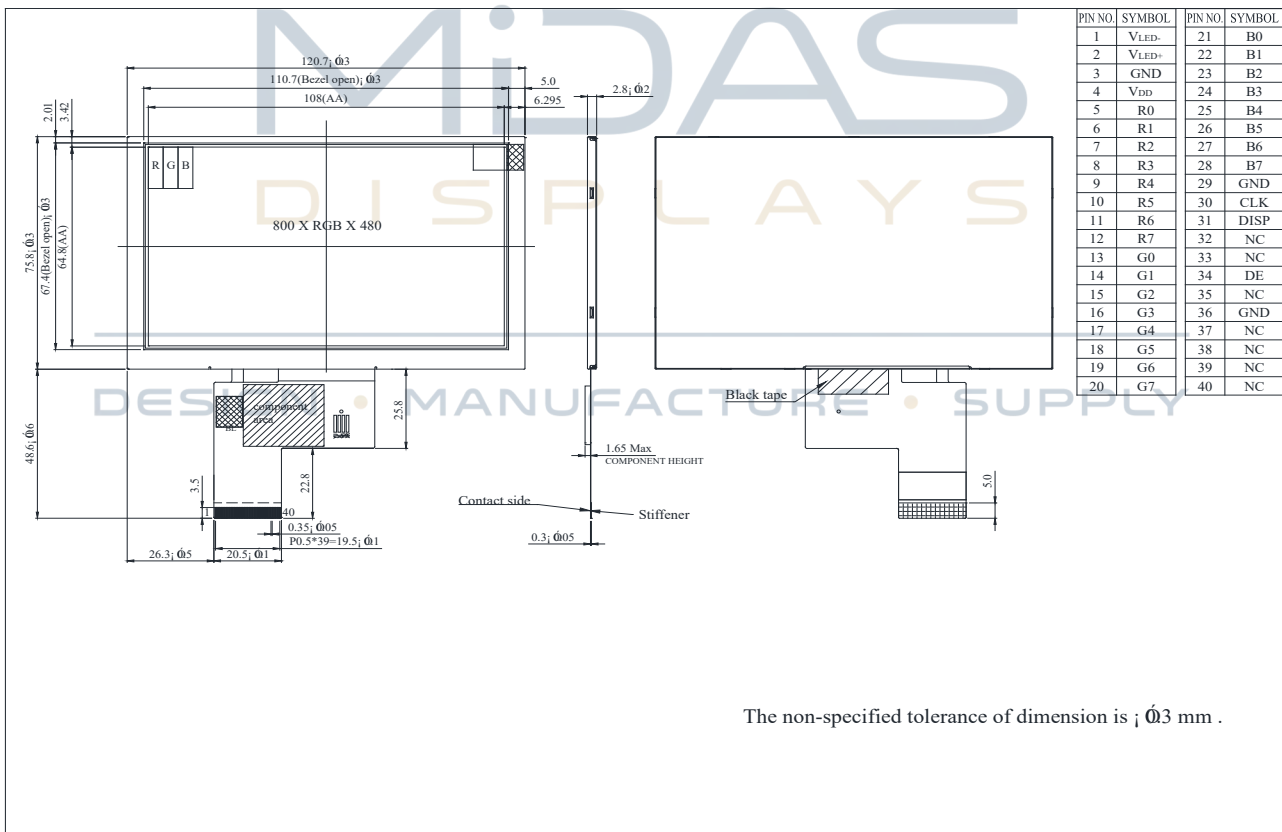
FPC Connector is used for the module electronics interface.

Pin	Symbol	Function	Remark
1	VLED-	Power for LED backlight (Cathode)	
2	VLED+	Power for LED backlight (Anode)	
3	GND	Power Ground	
4	VDD	Power voltage	
5	R0	Red data (LSB)	
6	R1	Red data	
7	R2	Red data	
8	R3	Red data	
9	R4	Red data	
10	R5	Red data	
11	R6	Red data	
12	R7	Red data(MSB)	
13	G0	Green data(LSB)	
14	G1	Green data	
15	G2	Green data	
16	G3	Green data	
17	G4	Green data	
18	G5	Green data	
19	G6	Green data	
20	G7	Green data(MSB)	
21	B0	Blue data(LSB)	
22	B1	Blue data	
23	B2	Blue data	
24	B3	Blue data	
25	B4	Blue data	
26	B5	Blue data	
27	B6	Blue data	
28	B7	Blue data(MSB)	
29	GND	Power Ground	
30	CLK	Sample clock	



31	DISP	Display on/off	
32	NC	No connection	
33	NC	No connection	
34	DE	Data input enable	
35	NC	No connection	
36	GND	Power Ground	
37	NC	No connection	
38	NC	No connection	
39	NC	No connection	
40	NC	No connection	

## Contour Drawing



## Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

- Temp.  $\leq 60^{\circ}\text{C}$ , 90% RH MAX. Temp.  $> 60^{\circ}\text{C}$ , Absolute humidity shall be less than 90% RH at  $60^{\circ}\text{C}$

## Electrical Characteristics

### 1. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	3.0	3.3	3.6	V	
Current for Driver(Black)	IDD	-	65	97.5	mA	VDD=3.3V
Input logic high voltage	VIH	0.7 VDD	-	VDD	V	Note 1
Input logic low voltage	VIL	0	-	0.3 VDD		

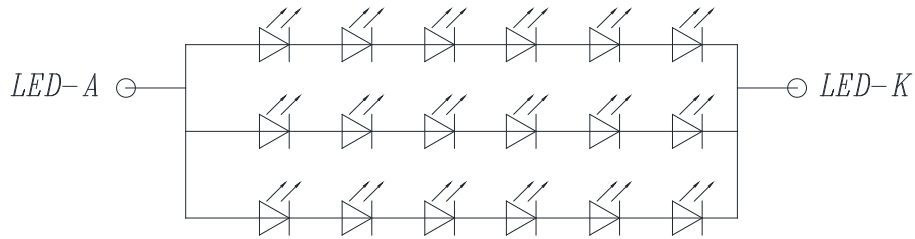
Note1: CLK,DE,R0~R7, G0~7, B0~7.

### 2. Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	VL	16.8	19.2	20.4	V	Note 1
Current for LED backlight	IL	--	60	--	mA	
LED life time	-	--	50,000	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at  $T_a=25^{\circ}\text{C}$  and  $I_L=20\text{mA/pcs}$ .

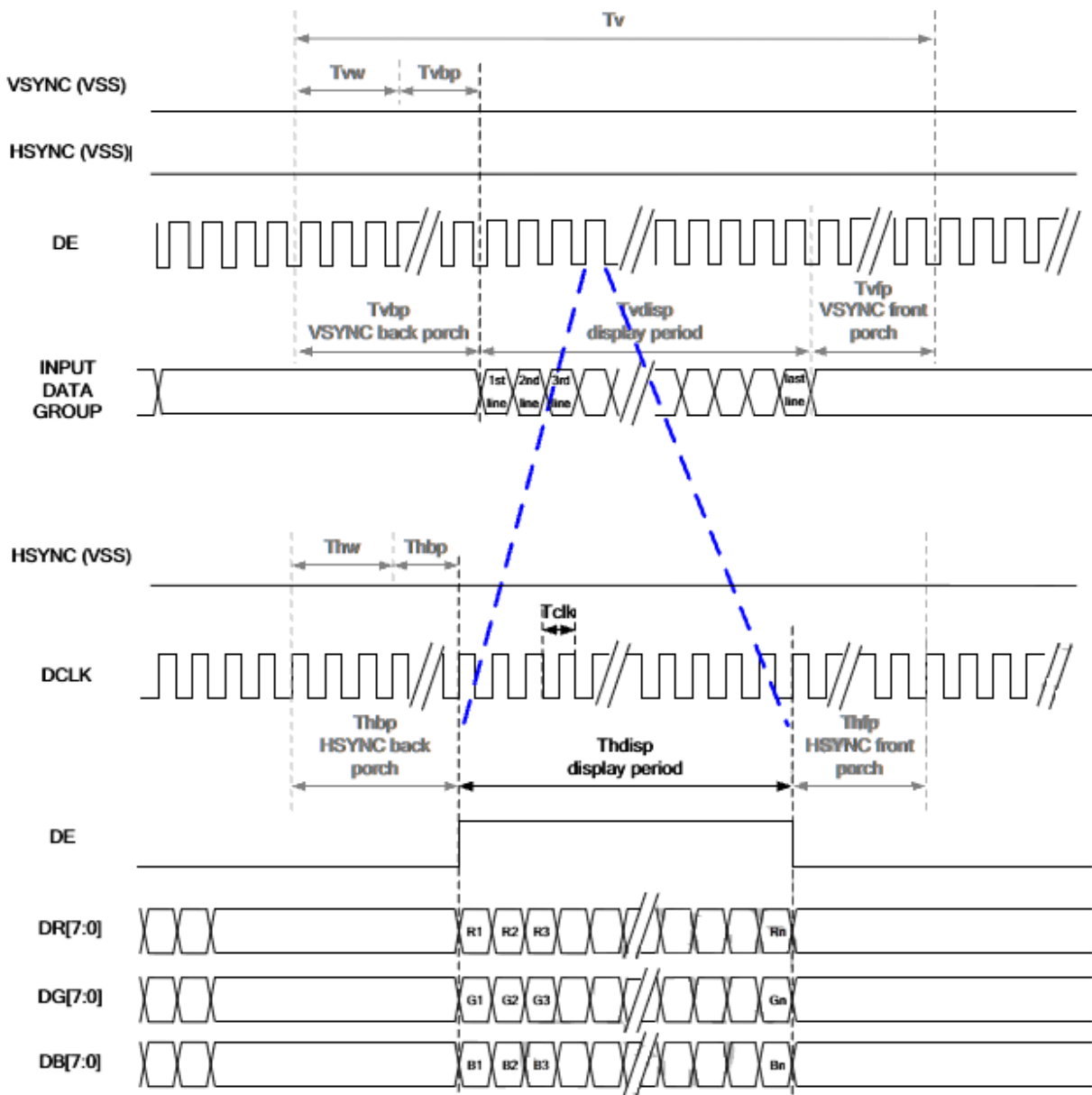
Note 2: The "LED life time" is defined as the module brightness decrease to 50% Original brightness at  $T_a=25^{\circ}\text{C}$  and  $I_L=20\text{mA/pcs}$ . The LED lifetime could be decreased if operating  $I_L$  is larger than  $25\text{mA/pcs}$ .



CIRCUIT DIAGRAM(LED 3\*6=18 DIES)

## Communication Interface

### 1. DE Mode

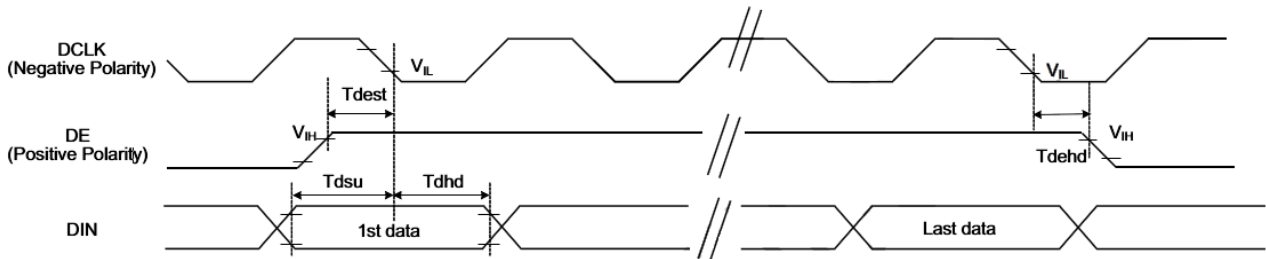


## 2. Parallel 24-bit RGB Input Timing Table

Parallel 24-bit RGB Input Timing (PVDD=PVDD1=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

Parallel 24-bit RGB Interface Timing Table						
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency	Fclk	23	25	27	MHz	
HSYNC	Period Time	Th	808	816	896	DCLK
	Display Period	Thdisp	800			DCLK
	Back Porch	Thbp	4	8	48	DCLK
	Front Porch	Thfp	4	8	48	DCLK
	Pulse Width	Thw	2	4	8	DCLK
VSYNC	Period Time	Tv	488	496	504	HSYNC
	Display Period	Tvdisp	480			HSYNC
	Back Porch	Tvbp	4	8	12	HSYNC
	Front Porch	Tvfp	4	8	12	HSYNC
	Pulse Width	Tvw	2	4	8	HSYNC

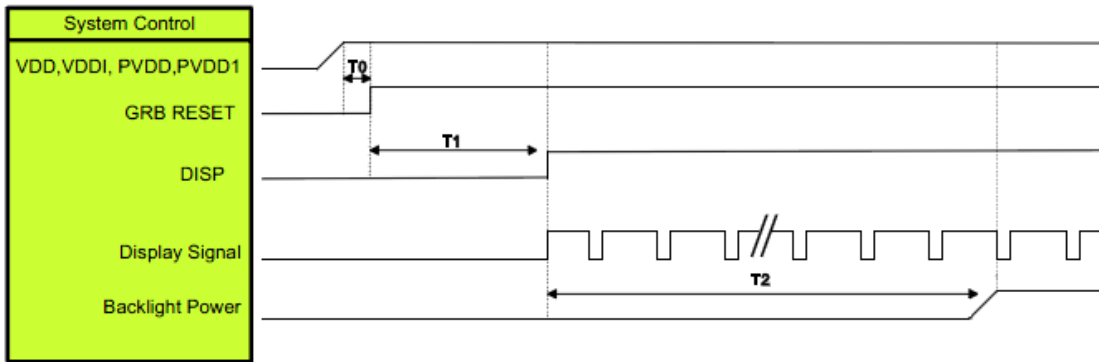
## 8.3. System Bus Timing for RGB Interface



Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	Tcw	40	50	60	%	
HSYNC Width	Thw	2	-	-	DCLK	
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	10	-	-	ns	
VSYNC Hold Time	Tvhd	10	-	-	ns	
HSYNC Setup Time	Thst	10	-	-	ns	
HSYNC Hold Time	Thhd	10	-	-	ns	
Data Setup Time	Tdsu	10	-	-	ns	
Data Hold Time	Tdhd	10	-	-	ns	
DE Setup Time	Tdest	10	-	-	ns	
DE Hold Time	Tdehd	10	-	-	ns	

# Power ON/OFF Sequence

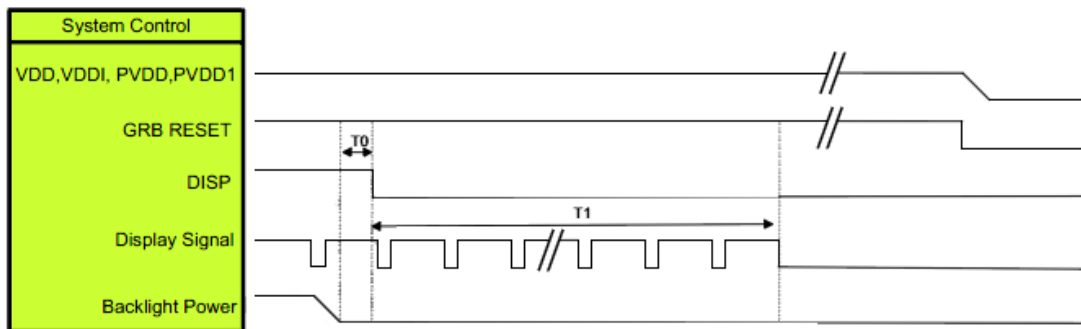
## Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

## Power Off Sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	100	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]





## Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark	
Response time	Tr+Tf	$\theta=0^\circ$ 、 $\Phi=0^\circ$	-	30	40	.ms	Note 3	
Contrast ratio	CR	At optimized viewing angle	800	1000	-	-	Note 4	
Color Chromaticity	White	Wx	$\theta=0^\circ$ 、 $\Phi=0$	0.27	0.32	0.37		Note 2,6,7
		Wy		0.295	0.345	0.395		
Viewing angle	Hor.	$\Theta_R$	$CR \geq 10$	70	80	-	Deg.	Note 1
		$\Theta_L$		70	80	-		
	Ver.	$\Phi_T$		70	80	-		
		$\Phi_B$		70	80	-		
Brightness	-	-	400	500	-	cd/m <sup>2</sup>	Center of display	
Uniformity	(U)	-	75	-	-	%	Note5	

Ta=25±2°C

Note 1: Definition of viewing angle range

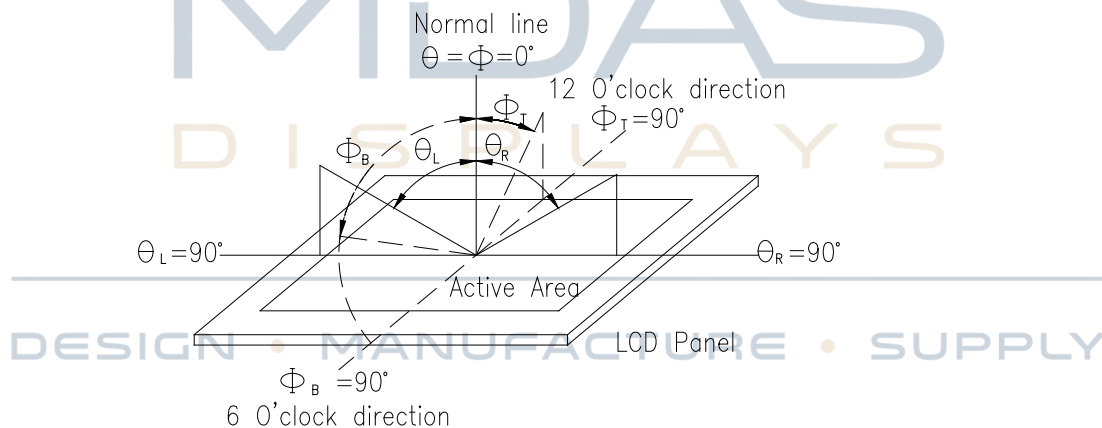


Fig. 10.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



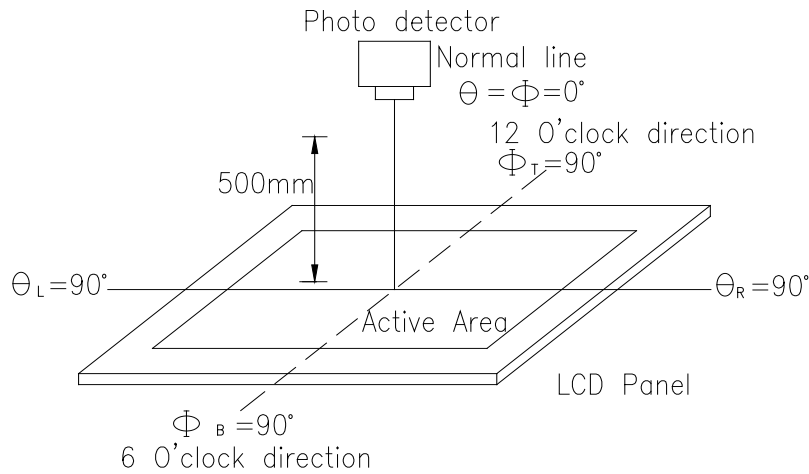
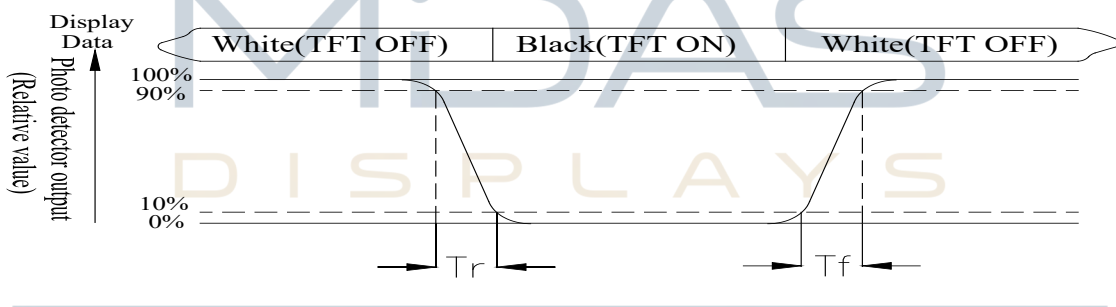


Fig. 10.2. Optical measurement system setup

**Note 3: Definition of Response time:**

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time,  $T_r$ , is the time between photo detector output intensity changed from 90% to 10%. And fall time,  $T_f$ , is the time between photo detector output intensity changed from 10% to 90%



**Note 4: Definition of contrast ratio:**

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$



Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) =  $L_{min}/L_{max} \times 100\%$

L = Active area length

W = Active area width

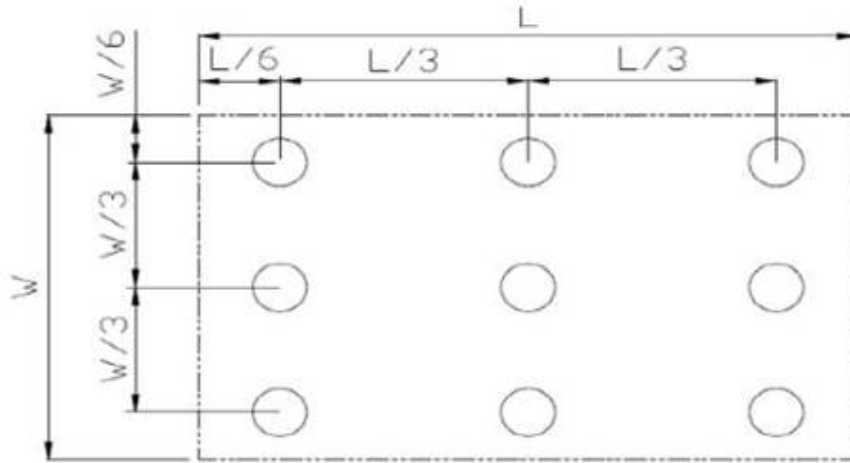


Fig10.3. . Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

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# Reliability

Content of Reliability Test (Super Wide temperature, -30°C~80°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-30°C 200hrs	1
High Temperature/ Humidity storage	The module should be allowed to stand at 60°C,90%RH max	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  <div style="text-align: center;"> <p style="margin: 0;">-30°C    25°C    80°C</p> <p style="margin: 0;">30min    5min    30min</p> <p style="margin: 0;">1 cycle</p> </div>	-30°C/80°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm  Vibration Frequency : 10~55Hz  One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

