

Sauls Wharf House Crittens Road Great Yarmouth Norfolk NR31 0AG Telephone +44 (0)1493 602602 Email:sales@midasdisplays.com Email:tech@midasdisplays.com www.midasdisplays.com

MDT0700F1IHHC-MIPI		1200 x 19	920 MIPI Interface TFT Module		
			Specification		
Version: 2 Date: 03/11/2021					
			Revision		
1 28/04/2021 First issue					
2 02/11/2021 Updated backlight connetor to ZHR-2, Update block diagra mechanical drawing.					

Display I			
Display Size	7.0"		
Resolution	1200 x 1920		
Orientation	Landscape		
Appearance	RGB		1
Logic Voltage	1.8V		SH C
Interface	MIPI		OH_2
Brightness	2500 cd/m ²		ompliant
Touchscreen	S CTP	500	mphant
Module Size	115.70 X 177.06 X 5.40mm		
Operating Temperature	-20°C ~ +70°C		
Pinout	40 way FFC	Box Quantity	Weight / Display
Pitch	0.5mm		

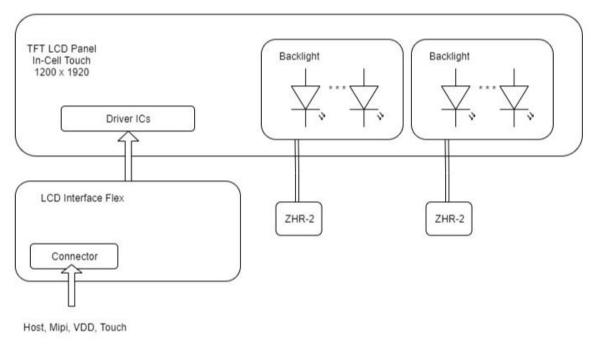
Display Accessories					
Part Number	Description				
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.				
MDIB-CC1	The MDIB-CC1 is a interconnect board for standard pitch pinouts to fine pitch wires. Ideal for prototyping of TFT and COG LCDs.				

Optional Variants						
Appearances	Voltage					

General Specifications

Item	Specification	Unit
Outline Dimensions	115.7 X 177.06 X 5.4	mm
Display Size	7.02 Diagonal	inches
Active Area	94.50 X 151.20	mm
Pixel Pitch	0.07875 X 0.07875	mm
Number of Dots	1200 X 1920	-
LCD Type	ADS 8bit + 2bit FRC	-
Backlight Type	LED White	
Viewing Direction	Free	
Touch Panel	Capacitive Touch (In-Cell) – FT7250	\
Luminance	2500	cd/m^2
Interface	MIPI	-
Surface Treatment	Cover Lens w/AR	RE • SUPPLY
Operating Temperature	-2070	۰С

Block Diagram



DISPLAYS

Pin Out - LCD

External pull up resistor required for TP_SDA and TP_SCL.

Number	Pin Name	I/O	Description
1	NC	-	No connection – Must not connect
2	IOVCC	Р	Power supply for system (1.8V)
3	IOVCC	Р	Power supply for system (1.8V)
4	GND	Р	Ground
5	LCD_RSTN	I	LCD reset signal, Active Low
6	NC	-	No connection
7	GND	Р	Ground
8	MIPI_0N	I	MIPI Negative data inputs
9	MIPI_0P	I	MIPI Positive data inputs
10	GND	Р	Power ground
11	MIPI_1N	I	MIPI Negative data inputs
12	MIPI_1P	M	ANUFACTURE data inputs
13	GND	Р	Power ground
14	MIPI_CKN	I	MIPI Negative clock inputs
15	MIPI_CKP	I	MIPI Positive clock inputs
16	GND	Р	Power ground
17	MIPI_2N	I	MIPI Negative data inputs
18	MIPI_2P	I	MIPI Positive data inputs
19	GND	Р	Power ground
20	MIPI_3N	I	MIPI Negative data inputs

21	MIPI_3P	I	MIPI Positive data inputs				
22	GND	Р	Power ground				
23	TP_SCL	I	TP I2C Clock 1.8V				
24	TP_SDA	I/O	TP I2C Data 1.8V				
25	GND	Р	Power ground				
26	TE	0	Tear output				
27	PWMO	0	PWM control signal for LED driver (CABC)				
28	TP_INT	TP_INT O Touch Interrupt 1.8V					
29	TP_RST	I	TP reset signal 1.8V				
30	GND	Р	Power ground				
31	NC	-	No connection – Must not connect				
32	NC	-	No connection – Must not connect				
33	NC	- 5	No connection				
34	VSN	Р	Analog supply negative voltage (-5~-6V)				
35	VSN	Р	Analog supply negative voltage (-5~-6V)				
36	NC	M	No connection				
37	VSP	Р	Analog supply positive voltage (5~6V)				
38	VSP	Р	Analog supply positive voltage (5~6V)				
39	NC	-	No connection – Must not connect				
40	NC	-	No connection – Must not connect				

Absolute Max Ratings - LCD

Item	Symbol	Value	Unit
Power Supply Voltage for Logic	IOVCC	-0.3 - 4.5	V
Power for Analog Negative	VSN	0 ~ -6.6	V
Power for Analog Positive	VSP	0 ~ +6.6	V
Operating Temperature	Topr	-20 to 70	°C
Storage Temperature	Tstg	-30 to 80	°C

Electrical Characteristics – LCD

LCD includes in-cell touch. IOVCC is the system power for both the LCD IO and the Touch IO.

Item	Symbol	Min	Тур	Мах	Unit	Test Condition
Operating Voltage	IOVCC	1.65	1.8	1.95	V	-
Voltage for Analog Negative	VSN	-6.5	-5.5	-4.5	V	-
Voltage for Analog Positive	VSP	4.5	5.5	6.5	V	-
Supply Current	IDD(IOVCC)	-	-	50	mA	Ta = 25 °C
Supply Current	IDD(VSN)	-	-	75	mA	Ta = 25 °C
Supply Current	IDD(VSP)	-	-	75	mA	Ta = 25 °C
	Vih	0.7IOVCC	-	IOVCC	V	-
Input Voltage	Vil	0	-	0.3IOVCC	V	-
Input Leakage Current	IiL	-1.0	-	1.0	μА	Vin = IOVCC

Backlight Specifications

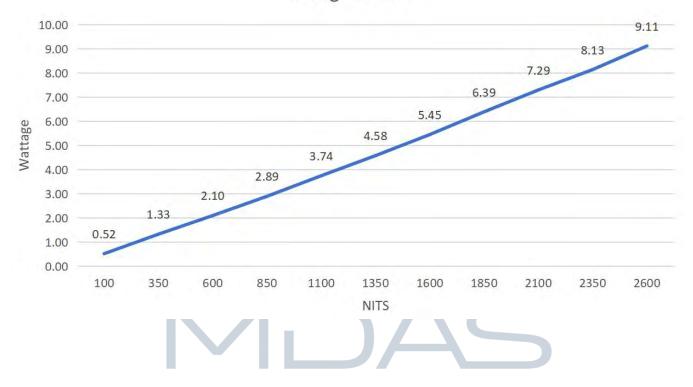
The backlight wiring is 28AWG and has been pinned into a JST-ZH series connector. The part number is ZHR-2. An example mating part number is, S2B-ZR-SM2-TF. The design has 2 LED rails to achieve maximum brightness at high efficiency. The supply current mentioned below is the sum, i.e., 150mA per backlight connector is required for a total of 300mA(typical) for 1500 nits.

Number	Pin Name	I/O	Description			
1	LEDA	Р	LED Anode + connection			
2	LEDK	Р	LED Cathode - connection			

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Supply Voltage	Vf	<u>.</u>	16	19.2	V	If = 240mA
		-	200	-	mA	Ta = 25C°, 1000 NITS
		MANU	J=300	TURE	• 5	Ta = 25C°, 1500
Supply Current	If	-	400	-		Ta = 25C°, 2000 NITS
		-	500*	-		Ta = 25C°, 2500 NITS

^{*}Thermal considerations apply – DUT stable at room temperature, open air in upright position.

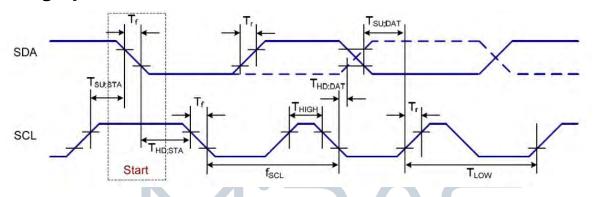
Wattage vs NITS



Timing Specifications - LCD

Refer to Focal Tech FT7250

Timing Specifications – PCAP



Symbol	Parameter	Min	Тур	Max	Unit
f _{SCLK}	SCL clock frequency	10	4 7 9	400	kHz
T _{LOW}	SCL clock LOW period	1.2	-	-	us
T _{HIGH}	SCL clock HIGH period	0.6	-	-	us
T _{SU;DATA}	Data set-up time	250	RE · S	UPPLY	ns
T _{HD;DATA}	Data hold time	0	-	0.9	us
T _r	SCL and SDA rise time	20	-	300	ns
T _f	SCL and SDA fall time	20	-	300	ns
T _f	SDA fall time for read out	20	-	1000	ns
C _b	Capacitive load represented by each bus line	-	-	400	pF
T _{SU;STA}	Setup time for a repeated START condition	0.6	-	-	us
T _{HD;STA}	START condition hold time	0.6	-	-	us

Symbol	Parameter	Min	Тур	Max	Unit
T _{SU;STO}	Setup time for STOP condition	0.6	-	-	us
T _{sw}	Tolerable spike width on bus	-	-	50	ns
T _{BUF}	BUS free time between a STOP and START condition	4.7	-	-	us



ID Register Bit Definitions

Example:

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
ID0	1	0	0	1	1	1	0	1
	Decimal Panel part number (157,0x9D)							
ID1	0	0	1	0	0	0	0	1
דטו	BCD Year code: 0x21							
ID2	0	0	1	1	0	1	0	1
	BCD Week code: 0x01-0x53							
ID3	0	0	0	0	0	0	0	0
כטו	Unused register, for future use in case of major rev.							
ID4	0	0	0	0	0	0	0	W _d
104	$W_d = B$	in code	for LED			•	JF	



MIPI Init

The MIPI initialization sequence consists of 2 commands. This initializes touch and graphics.

DESIGN • MANUFACTURE • SUPPLY

DCS_NoParam(0x11); //Sleep out

delay(300); //Delay 300ms

DCS_NoParam(0x29); //Display On

delay(200); //Delay 200ms

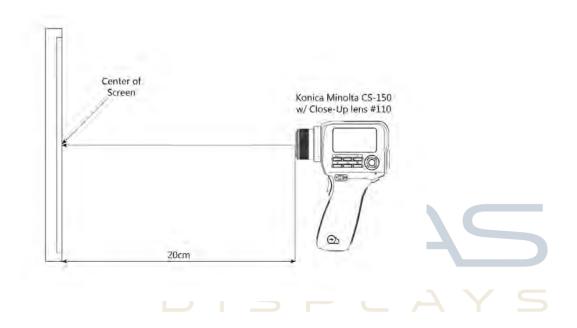
Optical Characteristics

All measurements taken at t=0. Measurements are native with no LUT.

Item Response Time		Symbol Conditions Tr Tf Ta = 25°C	Specification			11:4	Note	
			Conditions	Min	Тур	Max	Unit	Note
			Ta = 25°C	-	25	-	ms	(1)(4)
Contrast Ratio		CR	Normal Viewing Angle	1200	-	-	-	(1)(3)(5)
Hor.		X-		70	80	-	Deg	(3)(5)
Viewing Angle	1101.	X+	CR>10	70	80	-	Deg	
	Ver.	Y+		70	80	-	Deg	
	ver.	Y-		70	80	-	Deg	
	Red	Rx		-	0.6627	-	-	
		Ry		-	0.3391	-	-	
	Green	Gx		-	0.2659	-	-	
Chromaticity		Gy		-	0.6706	-	-	
Chromaticity	Blue	Вх		-	0.1525	-	-	
		Ву		-	0.0956	-	-	
	White	Wx		-	0.3106	-	-	
		Wy		-	0.3584	-	-	
Luminance L		Ta = 25 °C	-	2500	-	cd/m2	(1)	
Color Gamut Coverage - NTSC			-	80	-	%		
Uniformity U			80	90	-	%	(2)	

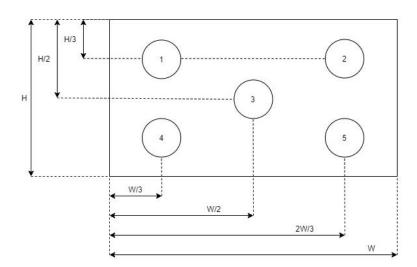
Note 1: Measurement setup

The LCD module should be stabilized at a given temperature for 25 minutes to avoid abrupt temperature change during measurement. After temperature saturation measurement should be executed. Probe is orthogonal to LCD surface.



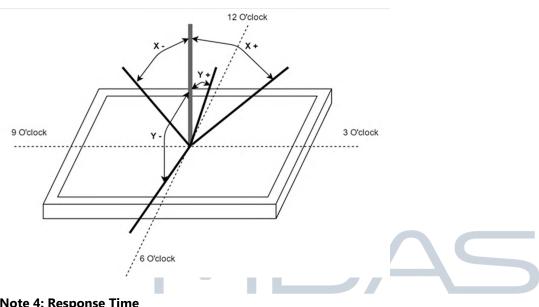
Note 2: Brightness Uniformity

Brightness uniformity = (Minimum Luminance of 5 points / Max Luminance of 5 points) * 100



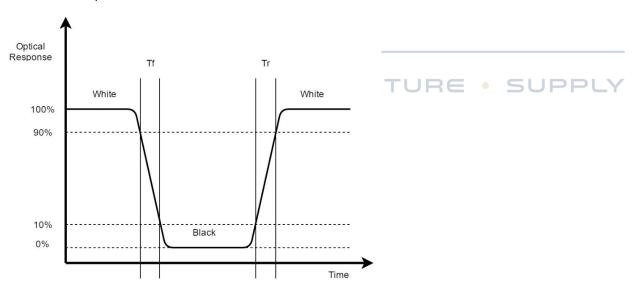
Note 3: Viewing Angle

Definition of viewing angle for Y+/- and X+/- is as follows.



Note 4: Response Time

Definition of response time as follows below.



Note 5: Contrast Ratio

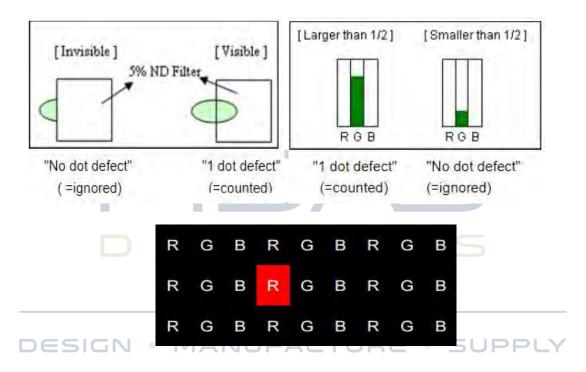
Definition of Contrast Ratio is as follows.

Contrast measurements shall be made at a viewing angle of 0° at the center of the surface.

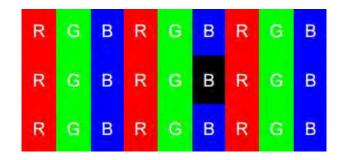
Quality & Inspection Criteria

Terminologies: Liquid Crystal Display; Each pixel contains three dots of R, G, and B (sub-pixel).

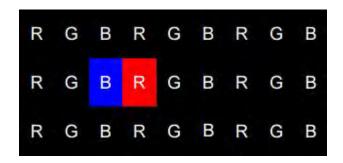
Bright Dot: 1 sub-pixel is a dot. Defects should be larger than 1/2 of a sub-pixel. Dots that are not visible through a 5% ND filter or smaller than 1/2 of sub-pixel size will not be counted as a dot defect.



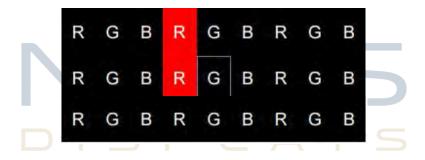
Dark Dot: Any single sub-pixel that does not light up in a white screen or another non-black screen is called a dark dot.



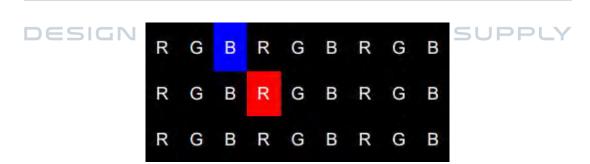
Two adjacent dots (horizontal direction): Use the bright dot illustration as an example to demonstrate two horizontal consecutive dots.



Two adjacent dots (vertical direction): Use the bright spot illustration as an example to demonstrate two vertical consecutive dots.



Two adjacent dots (bevel direction): Use the bright spot illustration as an example to demonstrate two consecutive dots in the bevel direction.



Three or more adjacent dots (horizontal): Use the bright spot illustration as an example to demonstrate three or more consecutive horizontal and vertical dots.

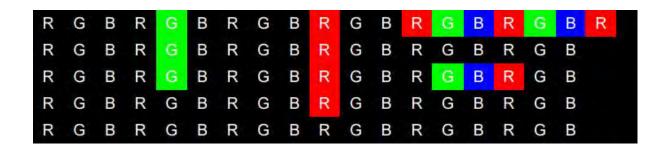
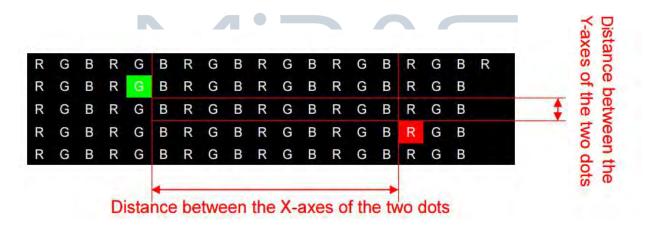


Illustration of spacing between two dots: (Distance is the relative distance between the X-axes of the two dots or the relative distance between the Y-axes of the two dots, whichever is larger)



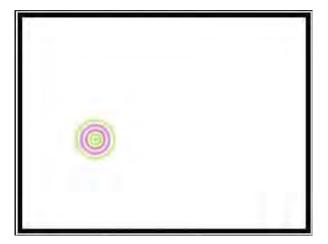
Functional Test

The LCD display testing program should display the following screens in order: all red, all green, all blue, all white, all gray, all black.

Inspection Requirements

After booting the system (single illumination), there are no non-display, unlit backlight, dark backlight, blinking, or other abnormal signs, and there are no bright lines, dark lines, or bright rims/leakage of light close to the LCD bezel.

Newton's Ring



Under high temperature and high humidity conditions, uneven deformations caused by heat in different layers of the LCD module will result in the display of an all-white screen. However, this condition can be recovered when temperature is resumed under normal circumstances. A specific determination can be conducted according to the operating conditions and storage conditions defined in the product's technical specifications. Any exception will be negotiated and mutually agreed by both parties. (Ripples are not permitted at fixed locations. For ripples at non-fixed locations, they are OK if they disappear within two seconds.)

LCD blaze

Uneven internal LCD installation, surface deformation of the LCD polarizer, internal structural interference of the LCD module, damaged LCD backlight plates, and other factors may cause partial fading of color on the LCD display. When observed from a certain incident angle (upper 10° , lower 3° , 40° on both sides), they will appear as white cicatrices, typically about the size of a grain of rice. In serious cases, they accumulate in large patches or stripes, appear in different degrees under various colors (red, blue, green, black, gray, white), and are especially obvious under an all-gray screen. Blazes with diameters ≥ 0.5 mm are not allowed: for those with diameters under 0.5 mm, 2 are acceptable if the space between them is ≥ 15 mm. Card chromatic aberration ratio versus ND Filter: 1.0 + 0.3 standard = 5% ND Filer (see definition of Mura).

Mura

Mura refers to the unevenness and irregularity that is visible in the image. It is difficult for visual inspection to recognize the non-uniform brightness or mura. Mura detection is subjective and therefore doesn't have pass/fail criteria. There are several precautions to take which can avoid mura. Avoid high ambient temperatures around the module, frame warpage and high temperature operation over long periods of time. Utilize screen savers to avoid mura.

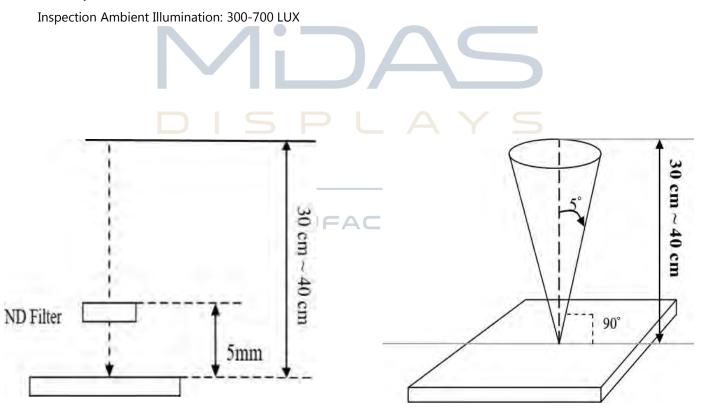
Inspection Conditions

Inspection distance should be $35cm \pm 5cm$ with a FujiFilm ND-LCD 5% filter approximately 5cm from the backlight surface.

Viewing angle: 90° ± 5°.

Room temperature: 23+/- 2°C

Humidity: 60 +/- 10%



Acceptance Criteria Table:

There should be no corrosion or cracking, or an uneven coating layer on LCD display surface, and there should be no sign of coagulation, flaking, cracking, or wear. The definition of minor defects and acceptance criteria are shown in the following table:

Item	Size	Unit	Acceptance qty.
	W < 0.05	mm	Ignore
Unfelt scratch visible with	W > .05 and < .10	mm	4
backlight off.	L > .3 and < 3.0		
	W > .10 or L > 3.0	mm	none
	Visible with backlig	none	
Felt scratch		None allowed	
	D < .2	mm	Ignore
	D > .2 and < .5	A mm S	5
Dent visible with backlight off	Spacing between		
	D > .5	mm	none
DESIGN • N	1 A N Visible with backligh	PLY none	
	D < .2	mm	Ignore
Rubble visible with backlight off	D > .2 and < .5	mm	5
Bubble visible with backlight off	D > .5	mm	none
	Visible with backligh	none	
	W < .05		Ignore

Item	Size	Unit	Acceptance qty.
		mm	
Foreign material (line shape) visible with backlight on	W > .05 and < .10 L > .3 and < 2.0	mm	4
	W > .10 or L > 2.0	mm	none
Foreign material (dot shape)	D < .2	mm	Ignore
visible with backlight on	D> .2 and < .5	mm	5
	D > .5	mm	none
	1 dot	-	4
Bright dot defect(lit)	2 adjacent dots	AYS	0
	1 dot	-	5
Dark dot defect (not lit)	2 adjacent dots	URE • S	UPPLY
	3 adjacent dots	-	0

