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MDT5000R		800 x 480	24-Bit RGB Interface	TFT Module
			Specification	
Version:	2		Date: 21/10/2020	
			Revision	
	1	12/06/2016	First issue.	
	2	21/10/2020	Added Timing information.	

Display F	eatures		
Display Size	5.0"		
Resolution	800 x 480		
VGA Size	WVGA		
Orientation	Landscape		1
Appearance	RGB		<b>2</b> LC
Logic Voltage	3.3V	IVR	OHS
Interface	24-Bit RGB	/ 4 23	mpliant
Brightness	280 cd/m <sup>2</sup>	, ,	mpnant
Touchscreen	RTP		
Module Size	120.70 x 75.90 x 4.40 mm		
Operating Temperature	-20°C ~ +70°C	Box Quantity	Weight / Display
Pinout	40 - Way FFC		

\* - For full design functionality, please use this ACTURE • SUPPLY specification in conjunction with the ILI5960 specification.(Provided Separately)

Display Accessories							
Part Number Description							
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.						

Optional Variants					
Appearances	Voltage				
No Touch Panel Capacitive Touch Panel					

## **General Specifications**

	Feature	Spec
	Size	5.0inch
	Resolution	800(horizontal)*480(Vertical)
	Interface	RGB-24bit
	Connect type	Connector
	Color Depth	16.7M
Characteristics	Technology type	a-Si
	Pixel pitch (mm)	0.045 x 0.135
	Pixel Configuration	R.G.B. Vertical Stripe
R	Display Mode	Normally White
	LCD Driver IC	ILI5960+ILI6122
	Viewing Direction	12 O'clock
	Gray Scale Inversion Direction	6 O'clock
	LCM (W x H x D) (mm)	120.7*75.9*4.4
	Active Area(mm)	108 x 64.80
Mechanical	With /Without TSP	With
	Weight (g)	TBD
DESIGN	LED Numbers	= 12LEDs

Note 1: Viewing direction is following the data which measured by optics equipment.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM weight tolerance: +/- 5%

# **Input/Output Terminals**

No.	Symbol	Description
1	VBL-	Backlight LED Cathode
2	VBL+	Backlight LED Anode.
3	GND	System Ground
4	VCC	Power supply for logic operation
5~12	R0~R7	Data bus
13~20	G0~G7	Data bus
21~28	B0~B7	Data bus
29	GND	System Ground
30	CLK	Pixel clock signal
31	DISP	Display on/off control
32	HSYNC	Horizontal Sync signal
33	VSYNC	Vertical Sync signal
34	DEN	Data Enable
35	NC	No connect
36	GND	System Ground
37	XR(NC)	The right side signal of TP
38	-YD(NC)	The down side signal of TP
39	XL(NC)	The left side signal of TP
40	YU(NC)	The up side signal of TP

### **Absolute Maximum Ratings**

(Note 1)

(1.0.0.7)								
		Values						
Item	Symbol	Min.	Max.	Unit	Remark			
	DV <sub>DD</sub>	-0.3	5	V				
	AVDD	-0.5	13.5	V				
	V <sub>GH</sub>	-0.3	42	V				
	V <sub>GL</sub>	-20	0.3	V				
Power voltage	V <sub>GH</sub> -V <sub>GL</sub>		40	V				
Operation Temperature	Top	-20	70	°C				
Storage Temperature	TsT	-30	80	℃				
LED Reverse Voltage	VR	-	3.3	V	Each LED Note 2			
LED Forward Current	l <sub>E</sub>		20	mA	Each LED			

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA

### **Electrical Characteristics**

### Typical operation conditions

(Note 1)

Item	Symbol		Values			Remark	
	Symbol	Min.	Тур.	Max.	Unit	Kemark	
Power voltage	DV <sub>DD</sub>	3.0	3.3	3.6	٧	Note 2	
	AV <sub>DD</sub>	10.2	10.4	10.6	٧		
	V <sub>GH</sub>	15.3	16.0	16.7	٧		
	V <sub>GL</sub>	-6.7	-6.0	-5.3	٧		
Input signal voltage	V <sub>сом</sub>	3.09	4.09	5.09	٧	Note 4	
Input logic high voltage	VIH	0.7 DV <sub>DD</sub>	(*)	DVDD	٧	Note 3	
Input logic low voltage	VIL	0	•	0.3 DV <sub>DD</sub>	٧		

Note 1: Be sure to apply DVDD and VGL to the LCD first, and then apply VGH.

Note 2: DV<sub>DD</sub> setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

Note 4: Typical V<sub>COM</sub> is only a reference value. It must be optimized according to each LCM. Please use VR and base on below application circuit.

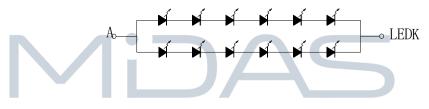
## **Current Consumption**

	0	*. 50	Values			
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	I <sub>GH</sub>	10.5	(0.50)	1	mA	V <sub>GH</sub> =16.0V
	IGL	-	(0.54)	1	mA	V <sub>GL</sub> = -6.0V
Current for Driver	IDV <sub>DD</sub>	(( <b></b> )	(4.2)	10	mA	DV <sub>DD</sub> =3.3V
	IAVDD	•	(19)	50	mA	AV <sub>DD</sub> =10.4V

## **Driving Backlight**

Item	Symbol	MIN	TYP	MAX	Unit	Remark
LED current	l <sub>F</sub>	-	40	1	mΑ	
Power Consumption			-	-	mW	
LED Voltage	V <sub>F</sub>	-	19.2	-	V	Note 1
LED Life Time	$W_{BL}$	25000			Hr	Note 2,3

Note 1: There are 2 Groups LED



Note 2 : Ta =  $25^{\circ}$ 

Note 3: Brightess to be decreased to 50% of the initial value

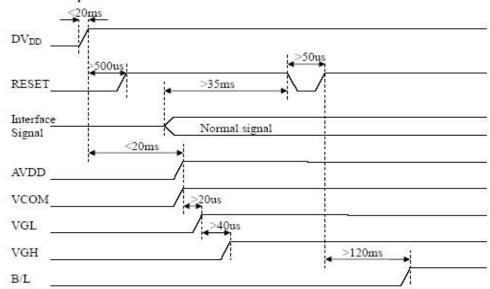


### **Interface Timing**

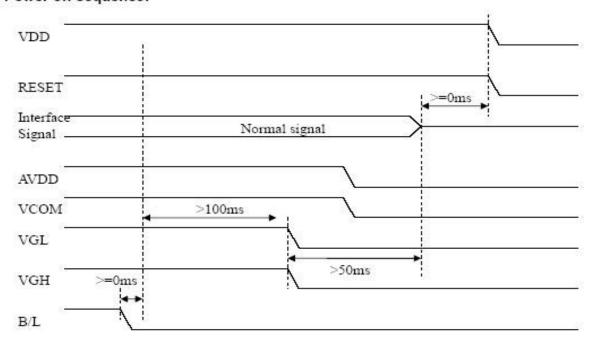
### **Power Sequence**

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

#### a. Power on sequence:



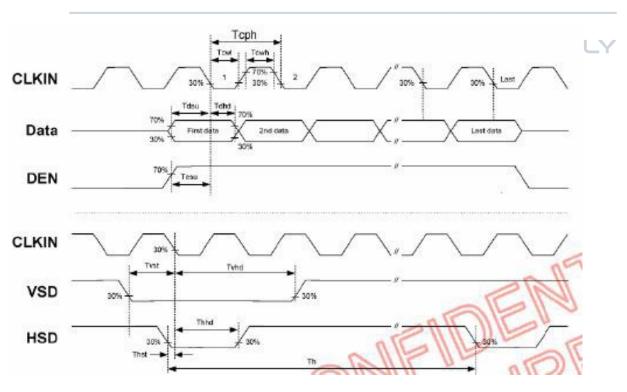
#### b. Power off sequence:



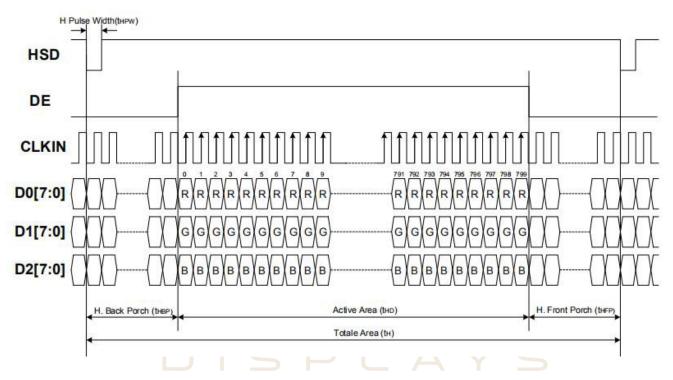
### **Timing Conditions**

. Project	6 1 1		Values		11.24	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
HS setup time	Thst	8	1828	E .	ns	32
HS hold time	Thhd	8	1523	21	ns	
VS setup time	Tvst	8	W58	20	ns	
VS hold time	Tvhd	8	454	5)	ns	
Data setup time	Tdsu	8	835	- 51	ns	
Data hole time	Tahd	8	80 <del>-</del> 00	5.	ns	
DE setup time	Tesu	8	89 <del>-</del> 38	. =	ns	
DE hole time	Tehd	8	12 <del>-</del> 53		ns	
DV <sub>DD</sub> Power On Slew rate	TPOR	10	938	20	ms	From 0 to 90% DV <sub>DD</sub>
RESET pulse width	TRst	1	West	20	ms	
DCLK cycle time	Tooh	20	W23	20	ns	
DCLK pulse duty	Towh	40	50	60	%	

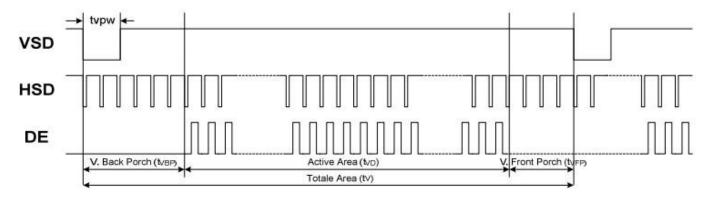
# **Timing Diagram**



## **Timing**



Horizontal Inj	out Timing	g				
Parameter		Cumbal	j .	Value		1124
		Symbol	Min.	Тур.	Max.	Unit
Horizontal dis	olay area	t <sub>HD</sub>		800		CLKIN
CLKIN frequency		f <sub>CLK</sub>	10 <del>00</del>	33.3	50	MHz
1 Horizontal line period		t <sub>H</sub>	862	1056	1200	CLKIN
1100	Min.	t <sub>HPW</sub>	s <del>en</del>	1		CLKIN
HSD pulse width	Typ.			577.5	S <del>118</del> 3	CLKIN
WIGHT	Max.	2 2001/13F4		40		CLKIN
HSD back porch	SYNC	t <sub>HBP</sub>	46	46	46	CLKIN
HSD front porch	SYNC	t <sub>HFP</sub>	16	210	354	CLKIN



Vertical Input Timing					
Parameter	Symbol		I I to ta		
		Min.	Typ.	Max.	Unit
Vertical display area	t <sub>VD</sub>	77	480	-	HSD
VSD period time	t <sub>V</sub>	510	525	650	HSD
VSD pulse width	t <sub>VPW</sub>	1		20	HSD
VSD back porch	t <sub>VBP</sub>	23	23	23	HSD
VSD front porch	t <sub>VFP</sub>	7	22	147	HSD

**DESIGN • MANUFACTURE • SUPPLY** 

## **Optical Characteristics**

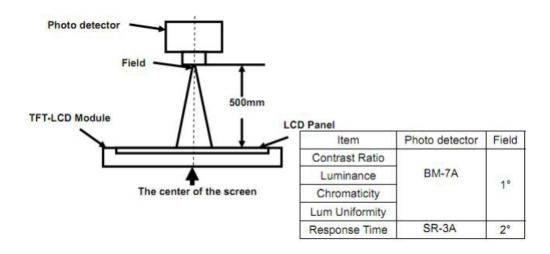
Items		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing angles		$\theta_{T}$		40	50	ı		
		θв	Center	60	70	-	Degree.	Note2
		$\theta_{L}$	CR≥10	60	70	-	Degree.	Notez
		$\theta_{R}$		60	70	-		
Contrast Ra	atio	CR	Θ =0	500	600	-	-	Note1, Note3
Dognonge Time		T <sub>ON</sub>	25°€	ı	20	30	ms	Note1,
Response	Response Time		25°C	-	20	30		Note4
Chromaticity	White	Xw		0.324	0.326	0.328	-	
	vvriite	Yw		0.364	0.366	0.368	-	
	Red	$X_R$		0.611	0.613	0.615	-	
		$Y_R$	Backlight	0.333	0.335	0.337	-	Note1,
	Gree	$\chi_{G}$	is on	0.305	0.307	0.309	ľ	Note5
	n	$Y_{G}$		0.558	0.560	0.562	-	
	Blue	$X_{B}$		0.133	0.135	0.137	-	
	blue	$Y_B$	P	0.158	0.160	0.162	J	
Uniformit	y	U		80	-	ı	%	Note1, Note6
NTSC					50		%	Note5
Luminanc	e N	• MA	NUFA	CT	280	•	SUPI	Note1, Note7

#### **Test Conditions:**

- 1. IF= 20mA(one channel), the ambient temperature is 25°C.
- 2. The test systems refer to Note 1 and Note 2.

#### Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

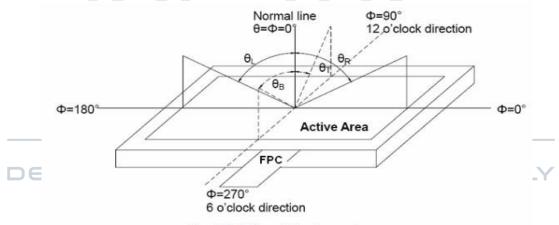


Fig. 1 Definition of viewing angle

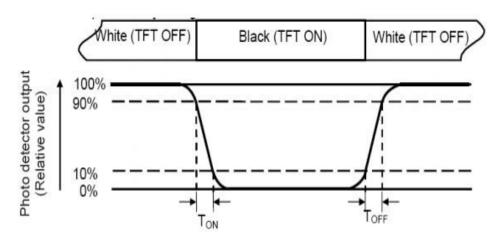
#### Note 3: Definition of contrast ratio

Contrast ratio (CR) = 
Luminance measured when LCD is on the "White" state

Luminance measured when LCD is on the "Black" state

#### Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval Between "White" state and "Black" state. Rise time (TON) is the time between Photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is The time between photo detector output intensity changed from 10% to 90%



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the Center of each measuring area

Luminance Uniformity (U) = Lmin/Lmax X100%

L----- Active area length W----- Active area width

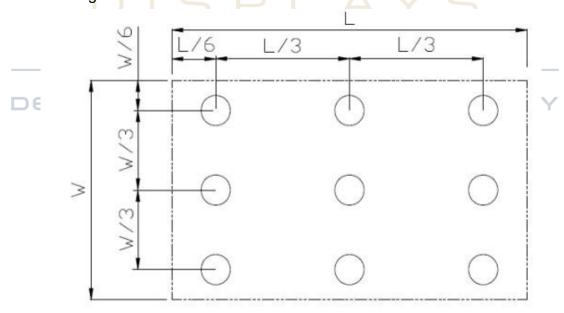


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position. Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

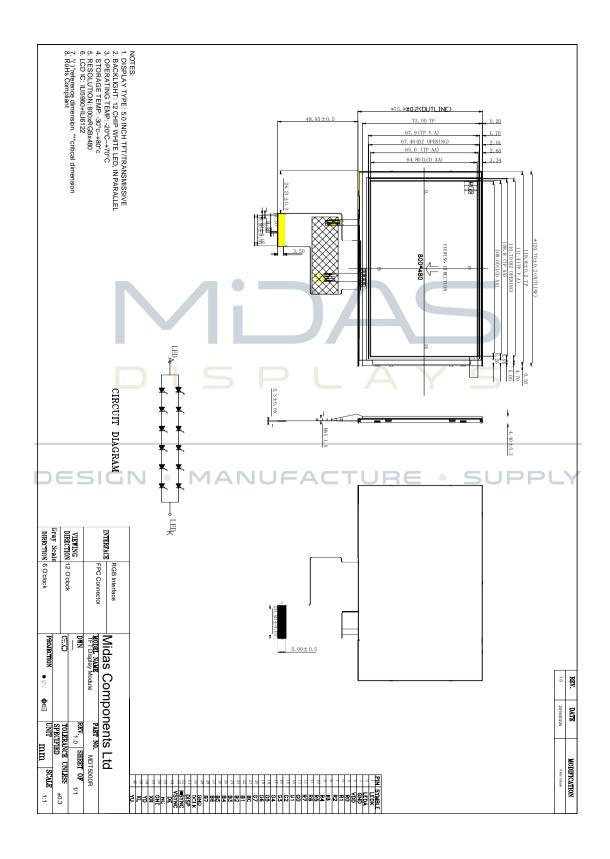
Measure the luminance of white state at center point.

# **Environmental / Reliability Tests**

No	Test Item	Condition	Remarks	
1	High Temperature Operation	Ts= +70°C, 240hrs	Note 1 IEC60068-2-2, GB2423. 2-89	
2	Low Temperature Operation	Ta= -20°C, 240hrs	Note 2 IEC60068-2-1 GB2423.1-89	
3	High Temperature Storage	Ta= +80°C, 240hrs	IEC60068-2-2 GB2423. 2-89	
4	Low Temperature Storage	Ta= -30°C, 240hrs	IEC60068-2-1 GB/T2423.1-89	
5	High Temperature & Humidity Storage	Ta= +60°C, 90% RH max, 160 hours	IEC60068-2-3 GB/T2423.3-2006	
6	Thermal Shock (Non-operation)	-30°C 30 min ~ +80°C 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87	
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15°C ~ 35°C, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998	
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995	
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995	
10	Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995	

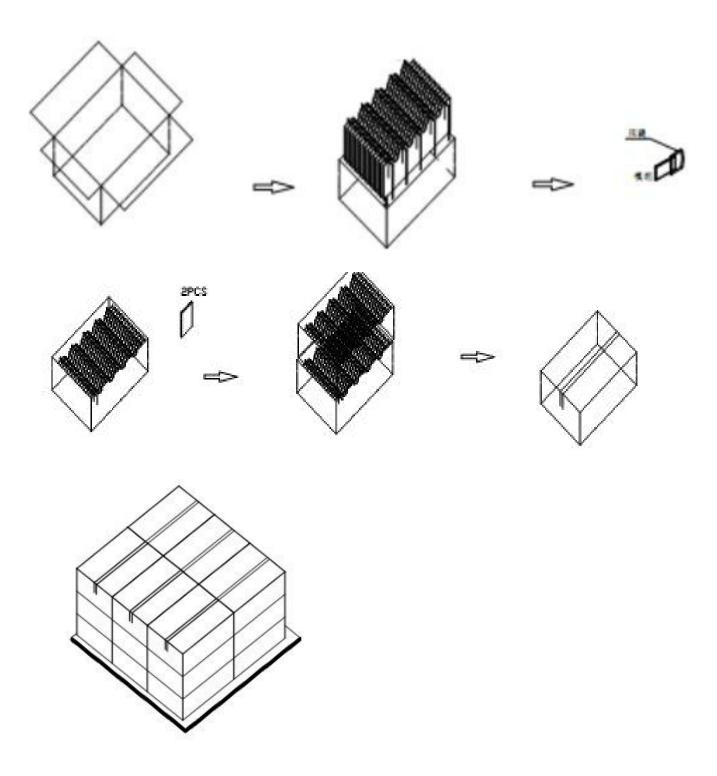
Note: 1.  $T_S$  is the temperature of panel's surface.

2. Ta is the ambient temperature of sample.



# **Packing**

# Packing Method



### **Precautions for Use of LCD modules**

#### **Handling Precautions**

- 1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following: Water; Ketene; Aromatic solvents

- 6. Do not attempt to disassemble the LCD Module.
- 7. If the logic circuit power is off, do not apply the input signals.
- 8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 8.1. Be sure to ground the body when handling the LCD Modules.
- 8.2. Tools required for assembly, such as soldering irons, must be properly ground.
- 8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### **Storage Precautions**

- 1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 2. The LCD modules should be stored under the storage temperature range If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :  $0^{\circ}$   $^{\circ}$   $^{\circ}$  40  $^{\circ}$  Relatively humidity: ≤80%

3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

#### **Transportation Precautions**

1. The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.