USB-6343 Specifications

2024-07-31

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USB-6343 Specifications

These specifications apply to the USB-6343 BNC and USB-6343 Spring Terminal.

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.

Specifications are *Typical* unless otherwise noted.

Conditions

Specifications are valid at 25 °C unless otherwise noted.

Analog Input

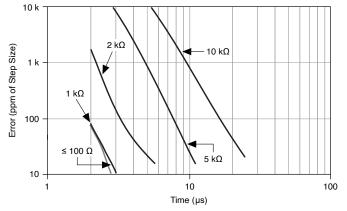
| Number of channels | 32 single ended or 16 differential |
|--------------------|------------------------------------|
| ADC resolution | 16 bits |
| DNL | No missing codes guaranteed |

| INL | | | Refer to AI Absolute Accuracy . | |
|---|-------|---------------------------|--|--|
| Sample rate | | | | |
| Single channel maximum | | | 500 kSample/s | |
| Multichannel maximum (aggreg | gate) | | 500 kSample/s | |
| Minimum | | | No minimum | |
| Timing resolution | | 10 r | ۱S | |
| Timing accuracy | | 50 ppm of sample rate | | |
| Input coupling | | DC | | |
| Input range | | ±0.2 V, ±1 V, ±5 V, ±10 V | | |
| Maximum working voltage for analog inputs (signal + common mode) | | ±11 V of AI GND | | |
| CMRR (DC to 60 Hz) | | 100 dB | | |
| Input impedance | | | | |
| Device on | | | | |
| AI+ to AI GND >10 G Ω in parallel with 100 pF | | | | |

| AI- to AI GND >10 G Ω in parallel with 100 pF | | | | |
|--|---------------------------------|----------------|------------------------|--|
| Device off | | | | |
| AI+ to AI GND | | 1,200 Ω | 1,200 Ω | |
| AI- to AI GND | | 1,200 Ω | | |
| Input bias current | | ±100 pA | | |
| Crosstalk (at 100 kHz) | | | | |
| Adjacent channels | | | -75 dB | |
| Non-adjacent channels | | | -90 dB | |
| Small signal bandwidth (-3 dB) | | 1.2 MHz | | |
| Input FIFO size | | 2,047 sa | 2,047 samples | |
| Scan list memory | | 4,095 entries | | |
| Data transers | | USB Sig I/O | nal Stream, programmed | |
| Overvoltage protection for al | l analog input and sense channe | els | | |
| Device on ±25 V for up to two Al pins | | | | |

| Device off | ±15 V for up to two AI pins | | |
|--|-----------------------------|--------------|-----------------------|
| Input current during overvoltage condition ±20 mA maximum/AI pin | | | ±20 mA maximum/AI pin |
| Settling time for multichanr | nel measurements, accura | acy, full-sc | ale step, all ranges |
| ±90 ppm of step (±6 LSB) | | 2 µs conve | ert interval |
| ±30 ppm of step (±2 LSB) | | 3 μs conve | ert interval |
| ±15 ppm of step (±1 LSB) | | 5 μs conve | ert interval |

Figure 1. Settling Error versus Time for Different Source Impedances



AI Absolute Accuracy (Warranted)

| Nominal Range Positive Full Scale (V) | Nominal Range Negative Full Scale (V) | Residual Gain Error (ppm of Reading) | Residual Offset Error (ppm of Range) | Offset Tempco (ppm of Range/°C) | Random Noise, σ (μV RMS) | Absolute Accuracy at Full Scale (μV) |
|--|---|---|---|--|--------------------------------|---|
| 10 | -10 | 65 | 13 | 23 | 270 | 2,190 |
| 5 | -5 | 72 | 13 | 23 | 135 | 1,130 |

Table 1. AI Absolute Accuracy

| Nominal Range Positive Full Scale (V) | Nominal Range Negative Full Scale (V) | Residual Gain Error (ppm of Reading) | Residual Offset Error (ppm of Range) | Offset Tempco (ppm of Range/°C) | Random Noise, σ (μV RMS) | Absolute Accuracy at Full Scale (µV) |
|--|---|---|---|--|--------------------------------|---|
| 1 | -1 | 78 | 17 | 26 | 28 | 240 |
| 0.2 | -0.2 | 105 | 27 | 39 | 9 | 60 |

For more information about absolute accuracy at full scale, refer to the <u>AI Absolute</u> <u>Accuracy Example</u> section.

| Gain tempco | 7.3 ppm/°C |
|------------------|-----------------|
| Reference tempco | 5 ppm/°C |
| INL error | 60 ppm of range |

Note *Absolute Accuracy at Full Scale* is determined using the following assumptions:

- TempChangeFromLastExternalCal = 10 °C
- TempChangeFromLastInternalCal = 1 °C
- NumberOfReadings = 10,000
- CoverageFactor = 3σ

Note Accuracies listed are valid for up to two years from the device external calibration.

AI Absolute Accuracy Equation

AbsoluteAccuracy = Reading · (GainError) + Range · (OffsetError) +

NoiseUncertainity

- GainError = ResidualGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)
- OffsetError = ResidualOffsetError + OffsetTempco · (TempChangeFromLastInternalCal) + INLError
- NoiseUncertainty = $\frac{\text{Random Noise} \cdot 3}{\sqrt{10,000}}$ for a coverage factor of 3 σ and averaging 10,000 points.

AI Absolute Accuracy Example

For example, on the 10 V range, the absolute accuracy at full scale is as follows:

- GainError: 65 ppm + 7.3 ppm · 1 + 5 ppm · 10 = 122 ppm
- OffsetError: 13 ppm + 23 ppm · 1 + 60 ppm = 96 ppm
- NoiseUncertainty: $\frac{270 \,\mu V \cdot 3}{\sqrt{10,000}}$ = 8.1 μV
- AbsoluteAccuracy: 10 V · (GainError) + 10 V · (OffsetError) + NoiseUncertainty = 2,190 μV

Analog Output

| Number of channels | 4 |
|--------------------|-------------------|
| DAC resolution | 16 bits |
| DNL | ±1 LSB |
| Monotonicty | 16 bit guaranteed |

| Maximum update rat | Maximum update rate (simultaneous) | | | |
|-------------------------|------------------------------------|---------------------------|--|--|
| 1 channel | | 900 kSample/s | | |
| 2 channels | | 840 kSample/s per channel | | |
| 3 channels | | 775 kSample/s per channel | | |
| 4 channels | | 719 kSample/s per channel | | |
| Timing accuracy | 50 ppm | n of sample rate | | |
| Timing resolution | 10 ns | | | |
| Output range | ±10 V | ±10 V | | |
| Output coupling | DC | DC | | |
| Output impedance | 0.2 Ω | | | |
| Output current drive | ±5 mA | | | |
| Overdrive protection | ±15 V | | | |
| Overdrive current | 15 mA | | | |

| Power-on state | ±20 mV | | |
|---|--|-----------|--|
| Power-on/off glitch | 1.5 V for 1.2 s ^[1] | | |
| Output FIFO size | 8,191 samples shared among chanr | iels used | |
| Data transfers | USB Signal Stream, programmed I/0 | 0 | |
| AO waveform modes | Non-periodic waveform, periodic waveform regeneration mode from onboard FIFO, periodic waveform regeneration from host buffer including dynamic update | | |
| Settling time, full- scale step, 15 ppm (1 LSB) | 6 μs | | |
| Slew rate | 15 V/μs | | |
| Glitch energy | | | |
| Magnitude | | 100 mV | |
| Duration | | 2.6 µs | |

AO Absolute Accuracy (Warranted)

Absolute accuracy at full-scale numbers is valid immediately following self calibration and assumes the device is operating within 10 °C of the last external calibration.

Nominal Nominal Residual Residual Absolute INL Offset Gain Offset Range Range Gain Reference Error Accuracy Tempco Positive Negative at Full Error Tempco Tempco Error (ppm (ppm of Full Full (ppm of (ppm/°C) (ppm/°C) (ppm of of Scale Range/°C) Scale Scale Reading) Range) Range) (μV) 5 10 -10 80 11.3 53 4.8 128 3,271

 Table 2. AO Absolute Accuracy



Note *Absolute Accuracy at Full Scale* numbers are valid immediately following self calibration and assumes the device is operating within 10 °C of the last external calibration.

Note Accuracies listed are valid for up to two years from the device external calibration.

AO Absolute Accuracy Equation

AbsoluteAccuracy = OutputValue · (GainError) + Range · (OffsetError)

- GainError = ResidualGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)
- OffsetError = ResidualOffsetError + OffsetTempco · (TempChangeFromLastInternalCal) + INLError

Digital I/O/PFI

Static Characteristics

| Number of channels | 48 total, 32 (P0.<031>), 16 (PFI <07>/P1, PFI <815>/P2) |
|--------------------|---|
| Ground reference | D GND |

| Direction control | Each terminal individually programmable as input or output |
|--------------------------|--|
| Pull-down resistor | 50 k Ω typical, 20 k Ω minimum |
| Input voltage protection | ±20 V on up to two pins |

Caution Stresses beyond those listed under the *Input voltage protection* specification may cause permanent damage to the device.

Waveform Characteristics (Port 0 Only)

| Terminals used | Port 0 (P0.<031>) |
|---------------------------------|---|
| Port/sample size | Up to 32 bits |
| Waveform generation (DO) FIFO | 2,047 samples |
| Waveform acquisition (DI) FIFO | 255 samples |
| DO or DI Sample Clock frequency | 0 to 1 MHz, system and bus activity dependent |
| Data transfers | USB Signal Stream, programmed I/O |
| Digital line filter settings | 160 ns, 10.24 μs, 5.12 ms, disable |

PFI/Port 1/Port 2 Functionality

| Functionality | Static digital input, static digital output, timing input, timing output |
|-----------------------------|--|
| Timing output sources | Many AI, AO, counter, DI, DO timing signals |
| Debounce filter settings | 90 ns, 5.12 μs , 2.56 ms, custom interval, disable; programmable high and low transitions; selectable per input |

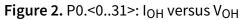
Recommended Operating Conditions

| Input high voltage (V _{IH}) | | | |
|--|--|--------|-------|
| Minimum | | 2.2 V | |
| Maximum | | 5.25 V | |
| Input low voltage (V _{IL}) | | | |
| Minimum | | | 0 V |
| Maximum | | | 0.8 V |
| Output high current (I _{OH}) | | | |
| P0.<031> -24 mA max | | xim | um |
| PFI <015>/P1/P2 -16 mA max | | xim | um |

| Output low current (I _{OL}) | |
|---------------------------------------|---------------|
| P0.<031> | 24 mA maximum |
| PFI <015>/P1/P2 | 16 mA maximum |

Digital I/O Characteristics

| Positive-going threshold (VT+) | 2.2 V maximum |
|--|----------------|
| Negative-going threshold (VT-) | 0.8 V minimum |
| Delta VT hysteresis (VT+ - VT-) | 0.2 V minimum |
| I _{IL} input low current (V _{IN} = 0 V) | -10 μA maximum |
| I _{IH} input high current (V _{IN} = 5 V) | 250 μA maximum |



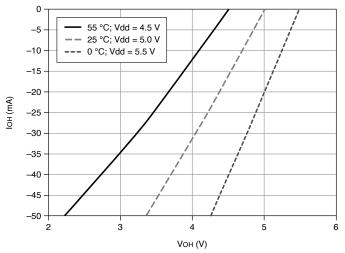


Figure 3. P0.<0..31>: I_{OL} versus V_{OL}

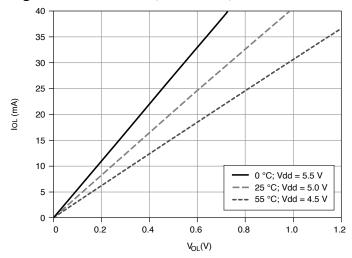


Figure 4. PFI <0..15>/P1/P2: I_{OH} versus V_{OH}

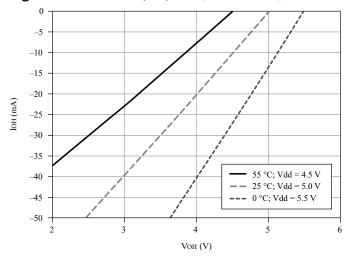
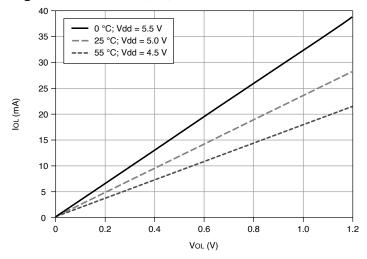


Figure 5. PFI <0..15>/P1/P2: I_{OL} versus V_{OL}



General-Purpose Counters

| Number of counter/ timers | 4 |
|----------------------------------|---|
| Resolution | 32 bits |
| Counter measurements | Edge counting, pulse, pulse width, semi-period, period, two-edge separation |
| Position measurements | X1, X2, X4 quadrature encoding with Channel Z reloading; two-pulse encoding |
| Output applications | Pulse, pulse train with dynamic updates, frequency division, equivalent time sampling |
| Internal base clocks | 100 MHz, 20 MHz, 100 kHz |
| External base clock frequency | 0 MHz to 25 MHz |
| Base clock accuracy | 50 ppm |
| Inputs | Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down, Sample Clock |
| Routing options for inputs | Any PFI, many internal signals |

| FIFO | 127 samples per counter |
|----------------|-----------------------------------|
| Data transfers | USB Signal Stream, programmed I/O |

Frequency Generator

| Number of channels | 1 |
|---------------------|-------------------------|
| Base clocks | 20 MHz, 10 MHz, 100 kHz |
| Divisors | 1 to 16 |
| Base clock accuracy | 50 ppm |

Output can be available on any PFI terminal.

Phase-Locked Loop (PLL)

| Number of PLLs | 1 |
|--|---|
| PFI <015> reference clock locking frequency | 10 MHz |
| Output of PLL | 100 MHz Timebase; other signals derived from 100 MHz Timebase including 20 MHz and 100 kHz Timebases |

External Digital Triggers

| Source | Any PFI |
|---|--|
| Polarity | Software-selectable for most signals |
| Analog input function | Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase |
| Analog output function | Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase |
| Counter/timer functions | Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down, Sample Clock |
| Digital waveform generation (DO) function | Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase |
| Digital waveform acquisition (DI) function | Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase |

Bus Interface

| USB compatibility | USB 2.0 Hi-Speed or full-speed ^[2] |
|----------------------|--|
| USB Signal | 8, can be used for analog input, analog output, digital input, digital output, |
| Stream | counter/timer 0, counter/timer 1, counter/timer 2, counter/timer 3 |

Power Requirements

 $\overline{\mathbb{N}}$

Caution The protection provided by the device can be impaired if the device is used in a manner not described in the *X Series User Manual*.



Caution The USB-6343 must be powered with an NI-offered AC adapter or a National Electric Code (NEC) Class 2 DC source that meets the power requirements for the device and has appropriate safety certification marks for country of use.

| Power supply requirements | 11 V DC to 30 V DC, 30 W, 2 positions 3.5 mm pitch pluggable screw terminal with screw locks similar to Phoenix Contact MC 1,5/2-STF-3,5 BK | |
|------------------------------------|---|--|
| Power input mating connector | Phoenix Contact MC 1,5/2-GF-3,5 BK or equivalent | |

Current Limits

Caution Exceeding the current limits may cause unpredictable device behavior.

| +5 V terminal (connector 0) | 1 A maximum ^[3] |
|--|----------------------------|
| P0/PFI/P1/P2 and +5 V terminals combined | 2 A maximum |

Physical Characteristics

Enclosure dimensions (includes connectors)

| BNC | 20.3 cm × 18.5 cm × 6.8 cm (8.0 in. × 7.3 in. × 2.7 in.) | | | |
|----------------------------------|--|--------------------------------|--|--|
| Screw terminal 26.4 cm × 17.3 cm | | 17.3 cm | × 3.6 cm (10.4 in. × 6.8 in. × 1.4 in.) | |
| Weight | | | | |
| Screw Terminal | | | 1.445 kg (3 lb 3 oz) | |
| BNC | | | 1.803 kg (3 lb 15.6 oz) | |
| I/O Connector | | | | |
| Screw Terminal 128 sc | | 128 scre | screw terminals | |
| BNC 3 | | 30 BNCs and 60 screw terminals | | |
| Screw terminal wiring gauge 0.2 | | 0.20 | 047 mm ² to 1.3087 mm ² (16 AWG to 24 AWG) | |

Note For more information about the connectors used for DAQ devices, refer to the document, *NI DAQ Device Custom Cables, Replacement Connectors, and Screws*, by going to <u>ni.com/info</u> and entering the Info Code rdspmb.

Calibration

| Recommended warm-up time | 15 minutes |
|--------------------------|------------|
| Calibration interval | 2 years |

Maximum Working Voltage

Maximum working voltage refers to the signal voltage plus the common-mode voltage.

| Channel to earth | 11 V, Measurement Category I |
|------------------|------------------------------|
|------------------|------------------------------|

Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as **MAINS** voltage. MAINS is a hazardous live electrical supply system that powers equipment. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels, special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.



Caution Do not connect the system to signals or use for measurements within Measurement Categories II, III, or IV.

Note Measurement Categories CAT I and CAT O are equivalent. These test and measurement circuits are for other circuits not intended for direct connection to the MAINS building installations of Measurement Categories CAT II, CAT III, or CAT IV.

Environmental

| Temperature | | |
|-------------|-----------------|--|
| Operating | 0 °C to 45 °C | |
| Storage | -40 °C to 70 °C | |
| Humidity | | |

| Operating | 10% to 90% RH, noncondensing | |
|------------------|------------------------------|--|
| Storage | | |
| Pollution Degree | 2 | |
| Maximum altitude | 2,000 m | |

Indoor use only.

Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1

Note For safety certifications, refer to the product label or the <u>Product</u> <u>Certifications and Declarations</u> section.

Electromagnetic Compatibility Standards

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- AS/NZS CISPR 11: Group 1, Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-003: Class A emissions

Note Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.

Note In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia and New Zealand (per CISPR 11) Class A equipment is intended for use only in heavy-industrial locations.

Notice For EMC declarations and certifications, and additional information, refer to the <u>Product Certifications and Declarations</u> section.

CE Compliance 🤇 🧲

This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)
- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

Product Certifications and Declarations

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit <u>ni.com/product-certifications</u>, search by model number, and click the appropriate link.

Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the **Engineering a Healthy**

Planet web page at <u>ni.com/environment</u>. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

EU and UK Customers

• X Waste Electrical and Electronic Equipment (WEEE)—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit <u>ni.com/environment/weee</u>.

电子信息产品污染控制管理办法(中国RoHS)

 ●●●中国RoHS-NI符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于NI中国RoHS合规性信息,请登录ni.com/environment/ rohs_china。(For information about China RoHS compliance, go to ni.com/ environment/rohs_china.)