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# PCI/PXI/ USB-6225 Specifications

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2024-07-30



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# NI 6225 Specifications

## Analog Input

Number of channels	40 differential or 80 single ended
ADC resolution	16 bits
DNL	No missing codes guaranteed
INL	Refer to the <a href="#">AI Absolute Accuracy</a> section
<b>Sample rate</b>	
Single channel maximum	250 kS/s
Multichannel maximum (aggregate)	250 kS/s
Minimum	No minimum
Timing accuracy	50 ppm of sample rate
Timing resolution	50 ns
Input coupling	DC

Input range	$\pm 0.2\text{ V}, \pm 1\text{ V}, \pm 5\text{ V}, \pm 10\text{ V}$
Maximum working voltage for analog inputs (signal + common mode)	$\pm 11\text{ V}$ of AI GND
CMRR (DC to 60 Hz)	92 dB
<b>Input impedance</b>	
<b>Device on</b>	
AI+ to AI GND	$>10\text{ G}\Omega$ in parallel with 100 pF
AI- to AI GND	$>10\text{ G}\Omega$ in parallel with 100 pF
<b>Device off</b>	
AI+ to AI GND	820 $\Omega$
AI- to AI GND	820 $\Omega$
Input bias current	$\pm 100\text{ pA}$
<b>Crosstalk (at 100 kHz)</b>	
Adjacent channels	-75 dB
Non-adjacent channels	-90 dB <sup>[1]</sup>
Small signal bandwidth (-3 dB)	700 kHz

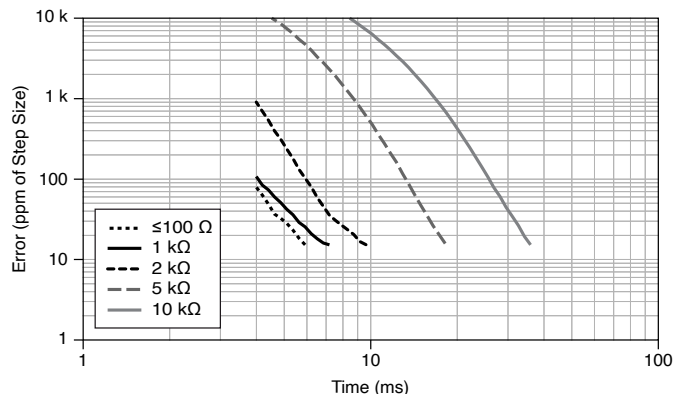
Input FIFO size	4,095 samples
Scan list memory	4,095 entries
<b>Data transfers</b>	
PCI/PXI	DMA (scatter-gather), interrupts, programmed I/O
USB	USB Signal Stream, programmed I/O
<b>Overvoltage protection for all analog input and sense channels</b>	
Device on	$\pm 25$ V for up to two AI pins
Device off	$\pm 15$ V for up to two AI pins
Input current during overvoltage condition	$\pm 20$ mA maximum/AI pin

## Settling Time for Multichannel Measurements

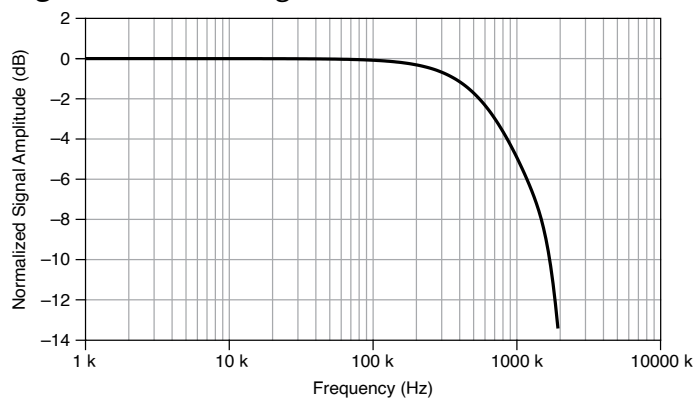
<b>Accuracy, full-scale step, all ranges</b>	
$\pm 90$ ppm of step ( $\pm 6$ LSB)	4 $\mu$ s convert interval
$\pm 30$ ppm of step ( $\pm 2$ LSB)	5 $\mu$ s convert interval
$\pm 15$ ppm of step ( $\pm 1$ LSB)	7 $\mu$ s convert interval

## Typical Performance Graphs

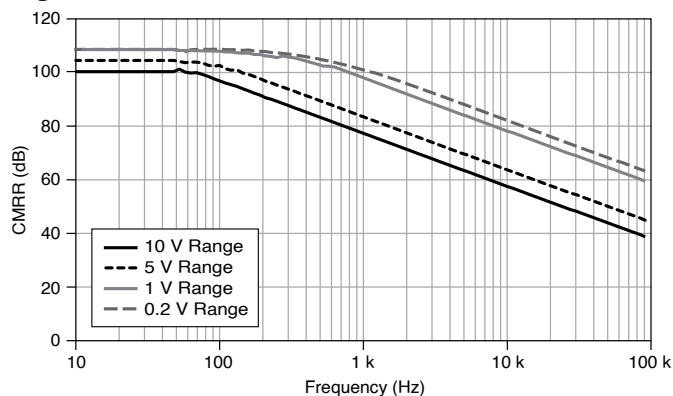
**Figure 1. Settling Error versus Time for Different Source Impedances**



**Figure 2. AI Small Signal Bandwidth**



**Figure 3. AI CMRR**



## AI Absolute Accuracy



**Note** Accuracies listed are valid for up to one year from the device external calibration.

Table 1. AI Absolute Accuracy

Nominal Range Positive Full Scale	Nominal Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	Random Noise, $\sigma$ ( $\mu\text{Vrms}$ )	Absolute Accuracy at Full Scale ( $\mu\text{V}$ )	Sensitivity ( $\mu\text{V}$ )
10	-10	75	20	57	244	3,100	97.6
5	-5	85	20	60	122	1,620	48.8
1	-1	95	25	79	30	360	12.0
0.2	-0.2	135	80	175	13	112	5.2



**Note** Sensitivity is the smallest voltage change that can be detected. It is a function of noise.

Gain tempco	25 ppm/°C
Reference tempco	5 ppm/°C
INL error	76 ppm of range

AI Absolute Accuracy Equation

***AbsoluteAccuracy = Reading · (GainError) + Range · (OffsetError) + NoiseUncertainty***

- ***GainError = ResidualAIGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)***
- ***OffsetError = ResidualAIOffsetError + OffsetTempco · (TempChangeFromLastInternalCal) + INLError***
- ***NoiseUncertainty =***

$$\frac{\text{Random Noise} \cdot 3}{\sqrt{100}}$$

for a coverage factor of 3  $\sigma$  and averaging 100 points.

### AI Absolute Accuracy Example

Absolute accuracy at full scale on the analog input channels is determined using the following assumptions:

- TempChangeFromLastExternalCal = 10 °C
- TempChangeFromLastInternalCal = 1 °C
- number\_of\_readings = 100
- CoverageFactor = 3  $\sigma$

For example, on the 10 V range, the absolute accuracy at full scale is as follows:

- GainError = 75 ppm + 25 ppm · 1 + 5 ppm · 10 = 150 ppm
- OffsetError = 20 ppm + 57 ppm · 1 + 76 ppm = 153 ppm
- NoiseUncertainty =  $\frac{244 \mu\text{V} \cdot 3}{\sqrt{100}}$   
= 73  $\mu\text{V}$
- AbsoluteAccuracy = 10 V · (GainError) + 10 V · (OffsetError) + NoiseUncertainty = 3,100  $\mu\text{V}$

## Analog Output

Number of channels	2
DAC resolution	16 bits
DNL	$\pm 1$ LSB
Monotonicity	16 bit guaranteed
<b>Maximum update rate</b>	



1 channel	833 kS/s
2 channels	740 kS/s per channel
Timing accuracy	50 ppm of sample rate
Timing resolution	50 ns
Output range	$\pm 10$ V
Output coupling	DC
Output impedance	$0.2 \Omega$
Output current drive	$\pm 5$ mA
Overdrive protection	$\pm 25$ V
Overdrive current	10 mA
Power-on state	$\pm 20$ mV
Power-off glitch	400 mV for 200 ms

Output FIFO size	8,191 samples shared among channels used
<b>Data transfers</b>	
Data transfers	DMA (scatter-gather), interrupts, programmed I/O
USB	USB Signal Stream, programmed I/O
AO waveform modes	Non-periodic waveform, periodic waveform regeneration mode from onboard FIFO, periodic waveform regeneration from host buffer including dynamic update
Settling time, full-scale step, 15 ppm (1 LSB)	6 $\mu$ s
Slew rate	15 V/ $\mu$ s
<b>Glitch energy</b>	
Magnitude	100 mV
Duration	2.6 $\mu$ s

## AO Absolute Accuracy

Absolute accuracy at full-scale numbers is valid immediately following internal calibration and assumes the device is operating within 10 °C of the last external calibration.



**Note** Accuracies listed are valid for up to one year from the device external calibration.

**Table 2.** AO Absolute Accuracy

Nominal Range Positive Full Scale	Nominal Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Gain Tempco (ppm/°C)	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	Absolute Accuracy at Full Scale (μV)
10	-10	90	10	40	5	3,230

Reference tempco	5 ppm/°C
INL error	128 ppm of range

AO Absolute Accuracy Equation

$$\text{AbsoluteAccuracy} = \text{OutputValue} \cdot (\text{GainError}) + \text{Range} \cdot (\text{OffsetError})$$

- $\text{GainError} = \text{ResidualGainError} + \text{GainTempco} \cdot (\text{TempChangeFromLastInternalCal}) + \text{ReferenceTempco} \cdot (\text{TempChangeFromLastExternalCal})$
- $\text{OffsetError} = \text{ResidualOffsetError} + \text{AOOffsetTempco} \cdot (\text{TempChangeFromLastInternalCal}) + \text{INLError}$

## Digital I/O/PFI

### Static Characteristics

Number of channels	24 total, 8 (P0.<0..7>), 16 (PFI <0..7>/P1, PFI <8..15>/P2)
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Ground reference	D GND
Direction control	Each terminal individually programmable as input or output
Pull-down resistor	50 k $\Omega$ typical, 20 k $\Omega$ minimum
Input voltage protection	$\pm 20$ V on up to two pins <sup>[2]</sup>

## Waveform Characteristics (Port 0 Only)

Terminals used	Port 0 (P0.<0..7>)
Port/sample size	Up to 8 bits
Waveform generation (DO) FIFO	2,047 samples
Waveform acquisition (DI) FIFO	2,047 samples
DI or DO Sample Clock frequency	0 MHz to 1 MHz, system and bus activity dependent
Data transfers	DMA (scatter-gather), interrupts, programmed I/O
DI or DO Sample Clock	Any PFI, RTSI, AI Sample or Convert Clock, Ctr n Internal Output, and

source <sup>[3]</sup>	many other signals
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## PFI/Port 1/Port 2 Functionality

Functionality	Static digital input, static digital output, timing input, timing output
Timing output sources	Many AI, AO, counter, DI, DO timing signals
Debounce filter settings	125 ns, 6.425 $\mu$ s, 2.56 ms, disable; high and low transitions; selectable per input

## Recommended Operating Conditions

Level	Minimum	Maximum
Input high voltage ( $V_{IH}$ )	2.2 V	5.25 V
Input low voltage ( $V_{IL}$ )	0 V	0.8 V
Output high current ( $I_{OH}$ ) P0.<0..7>	—	-24 mA
Output high current ( $I_{OH}$ ) PFI <0..15>/P1/P2	—	-16 mA
Output low current ( $I_{OL}$ ) P0.<0..7>	—	24 mA
Output low current ( $I_{OL}$ ) PFI <0..15>/P1/P2	—	16 mA

## Electrical Characteristics

Level	Minimum	Maximum
Positive-going threshold ( $V_{T+}$ )	—	2.2 V

Level	Minimum	Maximum
Negative-going threshold (VT-)	0.8 V	—
Delta VT hysteresis (VT+ - VT-)	0.2 V	—
I <sub>IL</sub> input low current (V <sub>in</sub> = 0 V)	—	-10 μA
I <sub>IH</sub> input high current (V <sub>in</sub> = 5 V)	—	250 μA

## Digital I/O Characteristics

Figure 4. DIO Port 0: I<sub>oh</sub> versus V<sub>oh</sub>

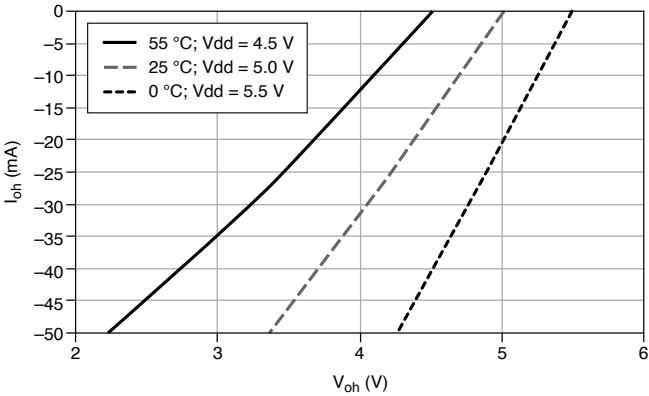


Figure 5. DIO PFI/Port 1/Port 2: I<sub>oh</sub> versus V<sub>oh</sub>

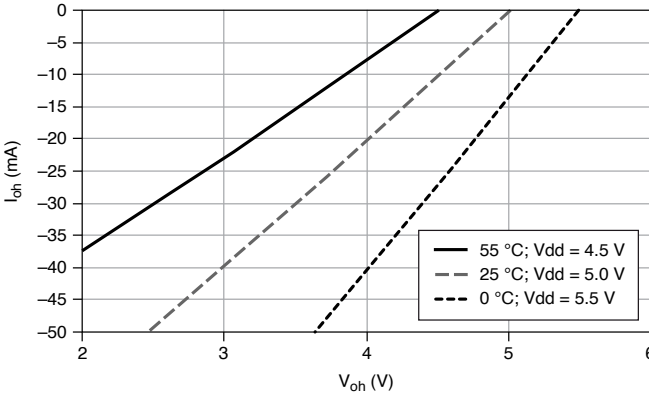


Figure 6. DIO Port 0:  $I_{OL}$  versus  $V_{OL}$

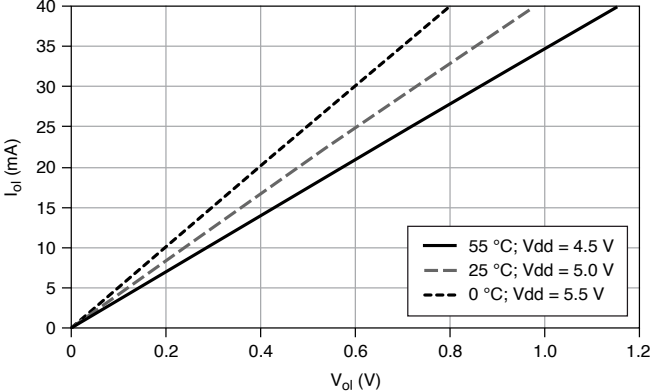
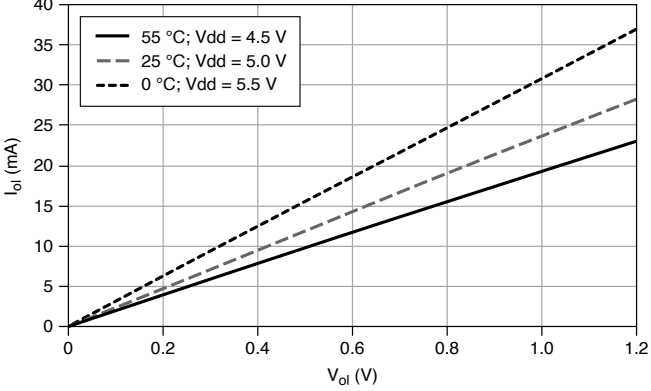


Figure 7. DIO PFI/Port 1/Port 2:  $I_{OL}$  versus  $V_{OL}$



## General-Purpose Counters/Timers

Number of counter/timers	2
Resolution	32 bits
Counter measurements	Edge counting, pulse, semi-period, period, two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with Channel Z reloading; two-pulse encoding
Output applications	Pulse, pulse train with dynamic updates, frequency division, equivalent

	time sampling
Internal base clocks	80 MHz, 20 MHz, 0.1 MHz
External base clock frequency	0 MHz to 20 MHz
Base clock accuracy	50 ppm
Inputs	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Routing options for inputs	Any PFI, RTSI, PXI_TRIG, PXI_STAR, analog trigger, many internal signals
FIFO	2 samples
<b>Data transfers</b>	
PCI/ PXI	Dedicated scatter-gather DMA controller for each counter/timer; interrupts, programmed I/O
USB	USB Signal Stream, programmed I/O

## Frequency Generator

Number of channels	1
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Base clocks	10 MHz, 100 kHz
Divisors	1 to 16
Base clock accuracy	50 ppm

Output can be available on any output PFI or RTSI terminal.

## Phase-Locked Loop (PLL)



**Note** PCI/PXI devices only.

Number of PLLs	1
Reference signal	PXI_STAR, PXI_CLK10, RTSI <0..7>
Output of PLL	80 MHz Timebase; other signals derived from 80 MHz Timebase including 20 MHz and 100 kHz Timebases

## External Digital Triggers

Source	Any PFI, RTSI, PXI_TRIG, PXI_STAR
Polarity	Software-selectable for most signals

Analog input function	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase
Analog output function	Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase
Counter/timer function	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Digital waveform generation (DO) function	Sample Clock
Digital waveform acquisition (DI) function	Sample Clock

## Device-to-Device Trigger Bus

PCI	RTSI <0..7> <sup>[4]</sup>
PXI	PXI_TRIG <0..7>, PXI_STAR
USB source	None
Output selections	10 MHz Clock, frequency generator output, many internal signals
Debounce filter settings	125 ns, 6.425 $\mu$ s, 2.56 ms, disable; high and low transitions; selectable per input

## Bus Interface


PCI/PXI	3.3 V or 5 V signal environment
USB	USB 2.0 Hi-Speed or full-speed <sup>[5], [6]</sup>
DMA channels (PCI/PXI)	6, can be used for analog input, analog output, digital input, digital output, counter/timer 0, counter/timer 1
USB Signal Stream	4, can be used for analog input, analog output, counter/timer 0, counter/timer 1

The PXI device can be installed in PXI Express hybrid slots. It cannot be used to control SCXI in PXI/SCXI combo chassis.

## Power Requirements


<b>Current draw from bus during no-load condition<sup>[7]</sup></b>	
+5 V	0.02 A
+3.3 V	0.25 A
+12 V	0.15 A
<b>Current draw from bus during AI and AO overvoltage condition<sup>[7]</sup></b>	
+5 V	0.02 A

+3.3 V	0.25 A
+12 V	0.25 A

 **Caution** USB devices must be powered with an NI offered AC adapter or a National Electric Code (NEC) Class 2 DC source that meets the power requirements for the device and has appropriate safety certification marks for country of use.

USB power supply requirements	11 to 30 VDC, 20 W, locking or non-locking power jack with 0.080 in. diameter center pin, 5/16-32 thread for locking collars
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## Current Limits

 **Caution** Exceeding the current limits may cause unpredictable behavior by the device and/or PC/chassis.

<b>PCI</b>	
+5 V terminal (connector 0)	1 A maximum <sup>[8]</sup>
+5 V terminal (connector 1)	1 A maximum <sup>[8]</sup>
<b>PXI</b>	
+5 V terminal (connector 0)	1 A maximum <sup>[8]</sup>

+5 V terminal (connector 1)	1 A maximum <sup>[8]</sup>
P0/PFI/P1/P2 and +5 V terminals combined	2 A maximum
<b>USB</b>	
+5 V terminal	1 A maximum <sup>[8]</sup>
P0/PFI/P1/P2 and +5 V terminals combined	2 A maximum
Power supply fuse	2 A, 250 V

## Physical Characteristics

Dimensions	
PCI printed circuit board	10.6 cm × 15.5 cm(4.2 in. × 6.1 in.)
PXI printed circuit board	Standard 3U PXI
Screw Terminal (includes connectors)	26.67 cm × 17.09 cm × 4.45 cm(10.5 in. × 6.73 in. × 1.75 in.)
Mass Termination enclosure (includes connectors)	18.8 cm × 17.09 cm × 4.45 cm (7.4 in. × 6.73 in. × 1.75 in.)
USB OEM	Refer to the <b><i>NI USB-622x/625x/628x OEM User Guide</i></b>

<b>Weight</b>	
PCI	103 g (3.6 oz)
PXI	174 g (6.1 oz)
USB Screw Terminal	1.24 kg (2 lb11 oz)
USB Mass Termination	907 g (2 lb)
USB OEM	162 g (5.7 oz)
<b>I/O connectors</b>	
Mass Termination	2 68-pin SCSI

## Calibration

<b>Recommended warm-up time</b>		
PCI/PXI	15 minutes	
USB	30 minutes	
Calibration interval	1 year	

## Maximum Working Voltage

Connect only voltages that are below these limits.

Channel-to-earth	11 V, Measurement Category I
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Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as MAINS voltage. MAINS is a hazardous live electrical supply system that powers equipment. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels, special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.



**Caution** Do not use for measurements within Categories II, III, or IV.



**Note** Measurement Categories CAT I and CAT O (Other) are equivalent. These test and measurement circuits are not intended for direct connection to the MAINS building installations of Measurement Categories CAT II, CAT III, or CAT IV.

## Environmental

Operating temperature	
PCI/PXI	0 °C to 55 °C
USB	0 °C to 45 °C
Storage temperature	-20 °C to 70 °C
Humidity	10% RH to 90% RH, noncondensing
Maximum altitude	2,000 m

Pollution Degree (indoor use only)	2
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Indoor use only.

## Shock and Vibration (PXI Only)

Operational shock	30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)
<b>Random vibration</b>	
Operating	5 Hz to 500 Hz, 0.3 grms
Nonoperating	5 Hz to 500 Hz, 2.4 grms (Tested in accordance with IEC 60068-2-64. Nonoperating test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)

## Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



**Note** For safety certifications, refer to the product label or the [Product Certifications and Declarations](#) section.

## Electromagnetic Compatibility



## CE Compliance

- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

## Product Certifications and Declarations


Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit [ni.com/product-certifications](http://ni.com/product-certifications), search by model number, and click the appropriate link.

## Environmental Management


NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the ***Engineering a Healthy Planet*** web page at [ni.com/environment](http://ni.com/environment). This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

## EU and UK Customers

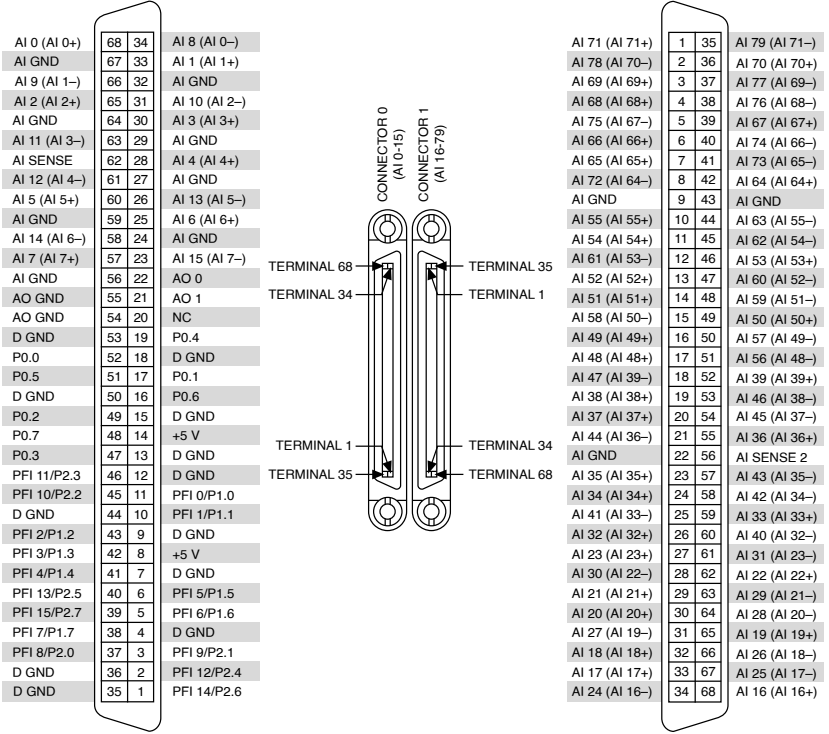
-  **Waste Electrical and Electronic Equipment (WEEE)**—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit [ni.com/environment/weee](http://ni.com/environment/weee).

## 电子信息产品污染控制管理办法（中国RoHS）

-  **中国RoHS**—NI符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于NI中国RoHS合规性信息，请登录 [ni.com/environment/rohs\\_china](http://ni.com/environment/rohs_china)。(For information about China RoHS compliance, go to [ni.com/environment/rohs\\_china](http://ni.com/environment/rohs_china).)

# Device Pinouts

Figure 8. NI PCI/PXI-6225 Pinout



NC = No Connect

Figure 9. NI USB-6225 Screw Terminal Pinout

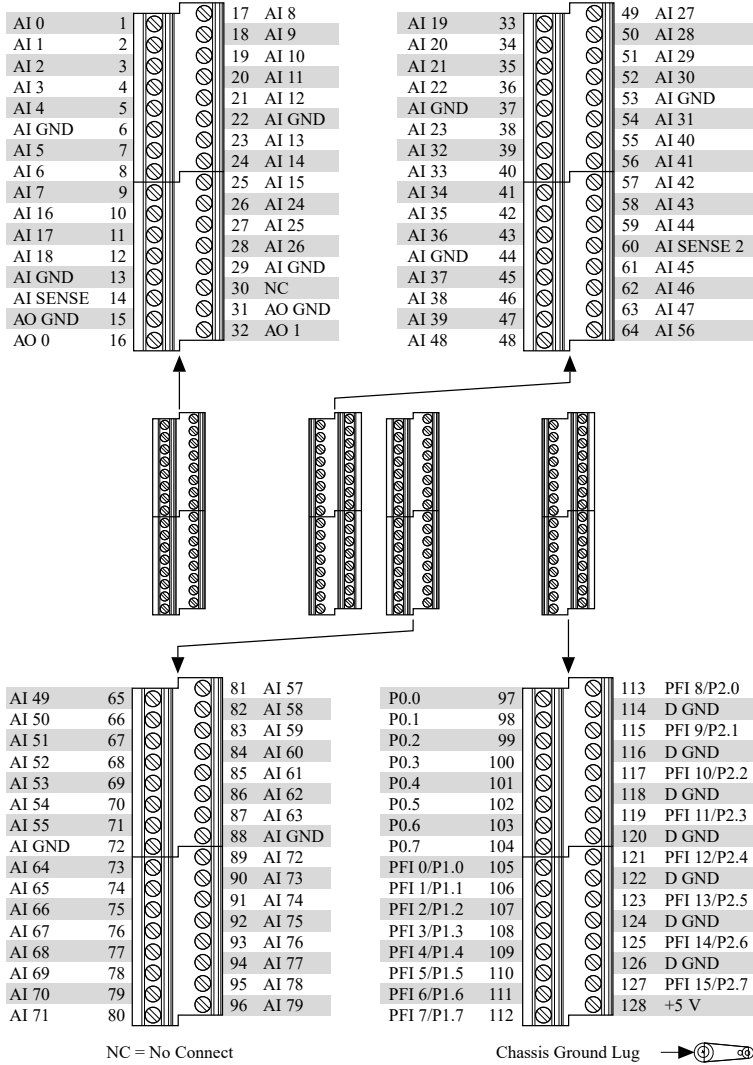
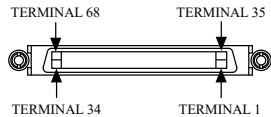


Figure 10. NI USB-6225 Mass Termination Pinout

AI 24	34	68	AI 16	AI 8	34	68	AI 0
AI 17	33	67	AI 25	AI 1	33	67	AI GND
AI 18	32	66	AI 26	AI GND	32	66	AI 9
AI 27	31	65	AI 19	AI 10	31	65	AI 2
AI 20	30	64	AI 28	AI 3	30	64	AI GND
AI 21	29	63	AI 29	AI GND	29	63	AI 11
AI 30	28	62	AI 22	AI 4	28	62	AI SENSE
AI 23	27	61	AI 31	AI GND	27	61	AI 12
AI 32	26	60	AI 40	AI 13	26	60	AI 5
AI 41	25	59	AI 33	AI 6	25	59	AI GND
AI 34	24	58	AI 42	AI GND	24	58	AI 14
AI 35	23	57	AI 43	AI 15	23	57	AI 7
AI GND	22	56	AI SENSE 2	AO 0	22	56	AI GND
AI 44	21	55	AI 36	AO 1	21	55	AO GND
AI 37	20	54	AI 45	NC	20	54	AO GND
AI 38	19	53	AI 46	P0.4	19	53	D GND
AI 47	18	52	AI 39	D GND	18	52	P0.0
AI 48	17	51	AI 56	P0.1	17	51	P0.5
AI 49	16	50	AI 57	P0.6	16	50	D GND
AI 58	15	49	AI 50	D GND	15	49	P0.2
AI 51	14	48	AI 59	+5 V	14	48	P0.7
AI 52	13	47	AI 60	D GND	13	47	P0.3
AI 61	12	46	AI 53	D GND	12	46	PFI 11/P2.3
AI 54	11	45	AI 62	PFI 0/P1.0	11	45	PFI 10/P2.2
AI 55	10	44	AI 63	PFI 1/P1.1	10	44	D GND
AI GND	9	43	AI GND	D GND	9	43	PFI 2/P1.2
AI 72	8	42	AI 64	+5 V	8	42	PFI 3/P1.3
AI 65	7	41	AI 73	D GND	7	41	PFI 4/P1.4
AI 66	6	40	AI 74	PFI 5/P1.5	6	40	PFI 13/P2.5
AI 75	5	39	AI 67	PFI 6/P1.6	5	39	PFI 15/P2.7
AI 68	4	38	AI 76	D GND	4	38	PFI 7/P1.7
AI 69	3	37	AI 77	PFI 9/P2.1	3	37	PFI 8/P2.0
AI 78	2	36	AI 70	PFI 12/P2.4	2	36	D GND
AI 71	1	35	AI 79	PFI 14/P2.6	1	35	D GND

NC = No Connect

CONNECTOR 1  
(AI 16-79)



CONNECTOR 0  
(AI 0-15)

