

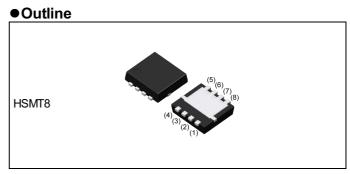
RH6N040BH

Nch 80V 40A Power MOSFET

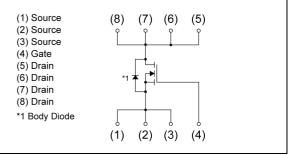
V _{DSS}	80V	
R _{DS(on)} (Max.)	8.3mΩ	
I _D	±65A	
P _D	59W	

Features

- 1) Low on resistance
- 2) High power small mold package (HSMT8)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested



●Inner circuit



Packaging specifications

		Packing	Embossed Tape
		Reel size (mm)	330
● Application	Туре	Tape width (mm)	12
Switching		Quantity (pcs)	3000
Motor drives		Taping code	TB1
DC/DC converter		Marking	N040BH

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	80	V
Continuous dusis sumont	Silicon limit (V _{GS} =10V)	۱ _D *1	±65	А
Continuous drain current	$T_{c} = 25^{\circ}C (V_{GS} = 10V)$	۱ _D *2	±40	А
Pulsed drain current	I _{DP} *3	±260	А	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single p	I_{AS}^{*4}	15	А	
Avalanche energy, single p	ulse	E _{AS} *4	18	mJ
Dower discipation		P _D *2	59	W
Power dissipation		P _D ^{*5}	2.0	W
Junction temperature	Tj	150	°C	
Operating junction and stor	T _{stg}	-55 to +150	°C	

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *2	-	-	2.1	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*5}	-	-	62.5	°C/W

•Electrical characteristics (T_a = 25°C)

Devenuetor	Currente e l	Canditiana	Values			1.1:4
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		80	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1mA$ referenced to 25°C		-	58	-	mV/°C
Zero gate voltage drain current	I_{DSS} V_{DS} = 80V, V_{GS} = 0V		-	-	5	μA
Gate - Source leakage current	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±500	nA
Gate threshold voltage	V _{GS(th)}	$V_{GS(th)}$ $V_{DS} = V_{GS}$, $I_D = 1mA$		-	4.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		-	-5.0	-	mV/°C
Static drain - source	D *6	V _{GS} = 10V, I _D = 40A	-	6.9	8.3	
on - state resistance	${R_{DS(on)}}^{*6}$	V _{GS} = 6V, I _D = 20A	-	8.8	12.3	mΩ
Gate resistance	R _G	R _G -		1.6	-	Ω
Forward Transfer Admittance	Y _{fs} * ⁶	$ Y_{fs} ^{*6}$ $V_{DS} = 5V, I_D = 20A$		-	-	S

*1 Limited by silicon chip capability.

- *2 T_c=25°C, Limited only by maximum temperature allowed.
- *3 Pw≤ 10 μ s , Duty cycle≤ 1%
- *4 L \simeq 0.1mH, V_{DD} = 40V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2
- *5 Mounted on a Cu board (40×40×0.8mm)
- *6 Pulsed





• Electrical characteristics (T_a = 25°C)

Deremeter	Cumph of	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1530	-		
Output capacitance	C _{oss}	V _{DS} = 40V	-	325	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	16	-		
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 40V, V_{GS} = 10V$	-	20	-		
Rise time	t _r *6	I _D = 20A	-	11	-	-	
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 2.5\Omega$	-	41	-	ns	
Fall time	t _f *6	R _G = 10Ω	-	19	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditiono		Values			1 1
Parameter	Symbol Conditions		UNS	Min.	Тур.	Max.	Unit
Tatal water also was	O *6	V _{DD} ≃ 40V	V _{GS} = 10V	-	23.0	-	
Total gate charge	Q_g^{*6}		$V_{DD} \simeq 40V$		-	14.5	-
Gate - Source charge	Q _{gs} *6	I _D = 40A	V _{GS} = 6V	-	5.9	-	nc
Gate - Drain charge	Q_{gd}^{*6}			-	4.2	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	ا _S *2		-	-	40	А
Pulse forward current	ا _{SP} *3	-	-	-	260	А
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 40A	-	-	1.2	V
Reverse recovery time	t _{rr} *6	I _S = 40A, V _{GS} =0V	-	39	-	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A/µs	-	19	-	nC



• Electrical characteristic curves

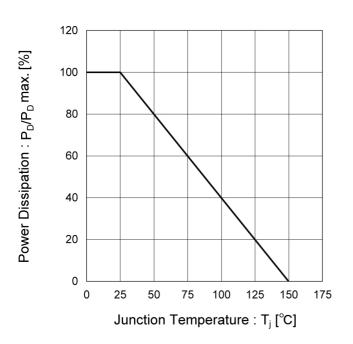


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

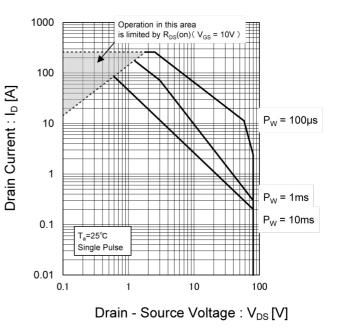


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

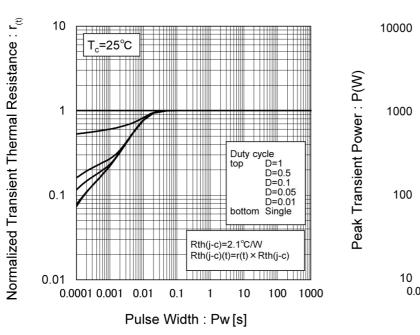
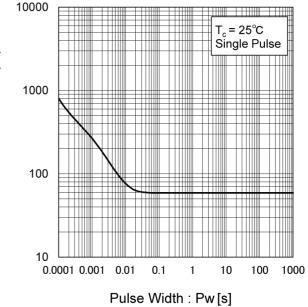


Fig.4 Single Pulse Maximum Power Dissipation





T_a=25°C

Pulsed

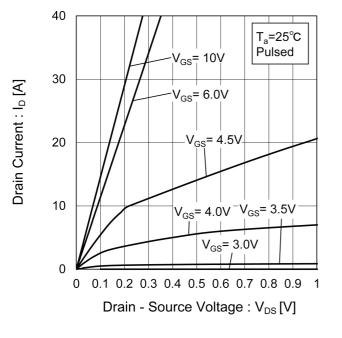


Fig.5 Typical Output Characteristics(I)

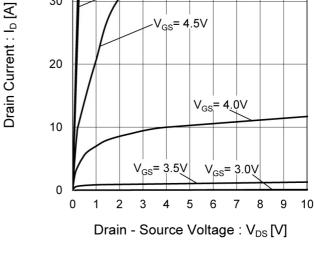


Fig.8 Typical Transfer Characteristics

Fig.6 Typical Output Characteristics(II)

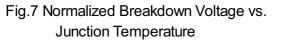
′_{GS}= 6.0∨

V_{GS}= 4.5V

40

30

V_{GS}= 10V



Junction Temperature : T_i [°C]

1.2 100 Normalized Breakdown Voltage : V_{(BR)DSS} $V_{GS} = 0V$ V_{DS}=V_{GS} $I_D = 1mA$ Pulsed Pulsed 10 1.1 Drain Current : I_D [A] 1 T_a= 125°C $T_a = 75^{\circ}C_{2}$ 1.0 $T_a = 25^{\circ}C_s$ T_a= - 25°C 0.1 0.9 0.01 0.8 0.001 -50 -25 0 25 50 75 100 125 150 2 1 3 4 0

Gate - Source Voltage : V_{GS} [V]

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• Electrical characteristic curves

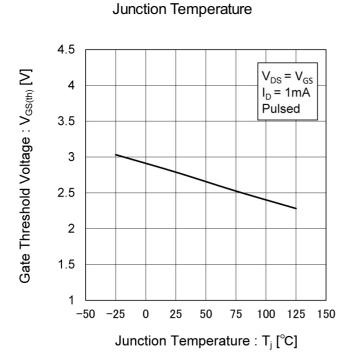


Fig.9 Gate Threshold Voltage vs.

Fig.10 Forward Transfer Admittance vs. Drain Current

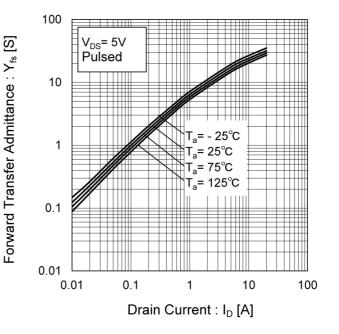
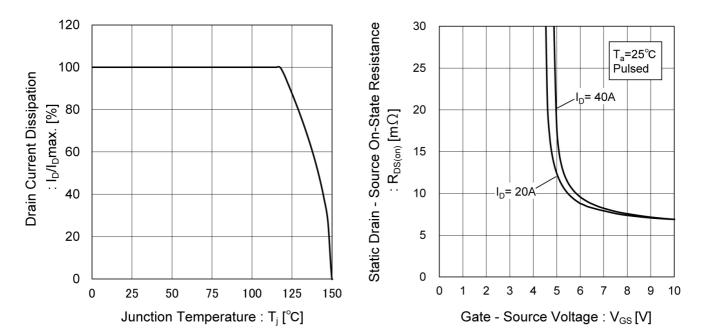


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





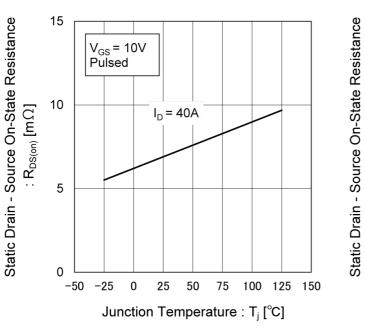


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

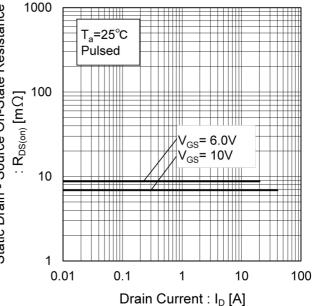
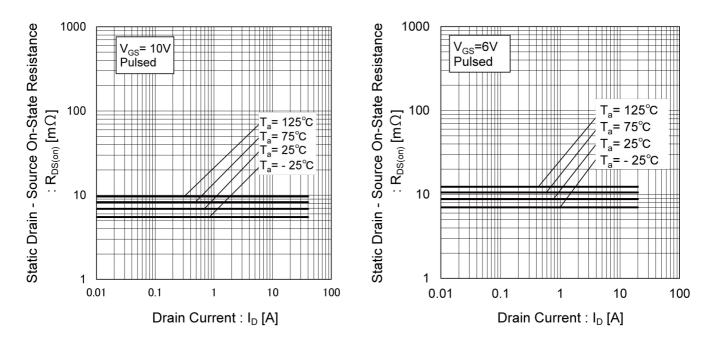


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II) Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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• Electrical characteristic curves

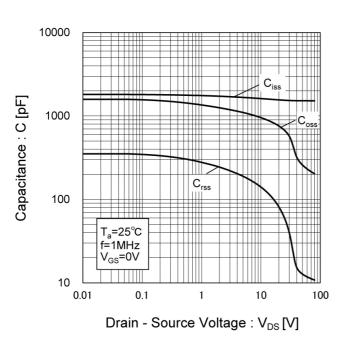


Fig.17 Typical Capacitances vs. Drain - Source Voltage

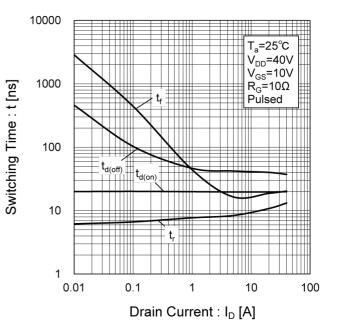


Fig.18 Switching Characteristics

Fig.19 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]

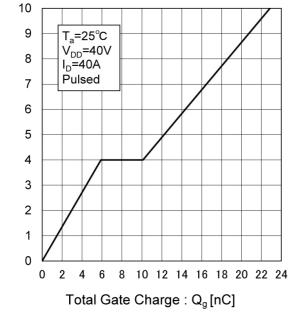
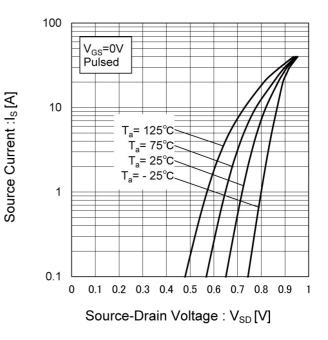


Fig.20 Source Current vs. Source Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

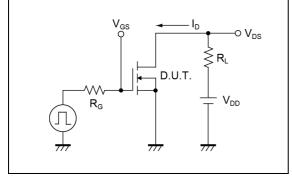


Fig.2-1 Gate Charge Measurement Circuit

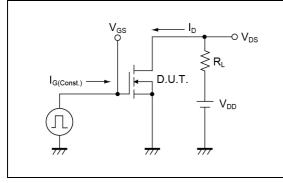


Fig.3-1 Avalanche Measurement Circuit

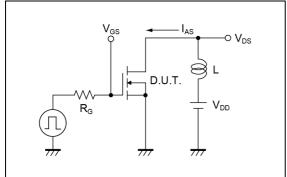


Fig.1-2 Switching Waveforms

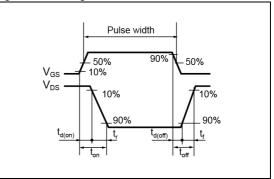


Fig.2-2 Gate Charge Waveform

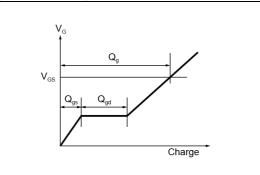
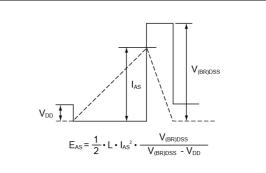


Fig.3-2 Avalanche Waveform



Notice

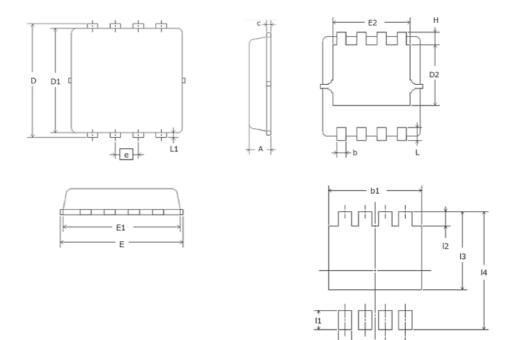
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

HSMT8 (TB1)

(3.3x3.3)



e Refarenced footprint dimensions

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b2

0.118

0.017

DIM	Milimeters		Inc	hes	
DIM	Min.	Max.	Min.	Max.	
Α	0.70	0.80	0.028	0.031	
b	0.25	0.35	0.010	0.014	
с	0.10	0.25	0.004	0.010	
D	3.25	3.45	0.128	0.136	
D1	3.00	3.20	0.118	0.126	
D2	1.78	1.98	0.070	0.078	
E	3.20	3.40	0.126	0.134	
E1	3.00	3.20	0.118	0.126	
E2	2.39	2.59	0.094	0.102	
е	0.	0.65)26	
Н	0.30	0.50	0.012	0.020	
L	0.30	0.50	0.012	0.020	
L1	0.	13	0.005		
DIM	Milimeters			hes	
	Nom.		Nom.		
1	0.	60	0.024		
12	0.4	45	0.018		
13	2.	45	0.096		
14	3.	70	0.1	46	

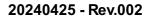
Dimension in mm/inches

3.00

0.43

b1

b2



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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