# SCS312AG

# SiC Schottky Barrier Diode

Datasheet

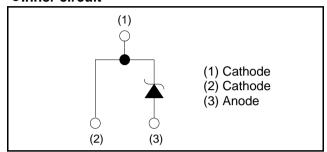
$V_{R}$	650V
l <sub>F</sub>	12A
$Q_{C}$	28nC

# Outline TO-220ACGE (1) (2) (3)

#### Features

- 1) Shorter recovery time
- 2) Reduced temperature dependence
- 3) High-speed switching possible
- 4) High surge current capability

### ●Inner circuit



#### Packaging specifications

	Packaging	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	50
	Packing code	C16
	Marking	SCS312AG

# ●Construction

Silicon carbide epitaxial planar type

## ● **Absolute maximum ratings** (T<sub>vi</sub>=25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Reverse voltage (repetitive peak)		$V_{RM}$	650	V
Reverse voltage	(DC)	V <sub>R</sub>	650	V
Continuous forwa	ard current $(T_c= 130^{\circ}C)^{*1}$	I <sub>F</sub>	12	А
Surge non-	PW=10ms sinusoidal, T <sub>vj</sub> =25°C		96	А
repetitive			81	А
forward current	PW=10μs square, T <sub>vj</sub> =25°C		350	А
Repetitive peak forward current		I <sub>FRM</sub>	52*2	А
1 <u>&lt;</u> PW <u>&lt;</u> 10ms, T <sub>vj</sub> =25°C		<b>f</b> .2	46	A <sup>2</sup> s
i <sup>2</sup> t value 1≤PW≤10ms, T <sub>vj</sub> =150°C		∫ i²dt	32	A <sup>2</sup> s
Total power disspation		$P_{D}$	78* <sup>3</sup>	W
Virtual junction temperature		$T_{vj}$	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Limited by maximum  $T_{vi}$  and for Max.  $R_{thJC}$ . \*2  $T_c$ =100°C,  $T_{vi}$ =150°C, Duty cycle=10% \*3  $T_c$ =25°C

# ● Electrical characteristics (T<sub>vj</sub>=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			l loit
			Min.	Тур.	Max.	Unit
DC blocking voltage	$V_{DC}$	I <sub>R</sub> =60μA	650	-	-	V
	V <sub>F</sub>	I <sub>F</sub> =12A,T <sub>vj</sub> =25°C	-	1.35	1.50	V
Forward voltage		I <sub>F</sub> =12A,T <sub>vj</sub> =150°C	-	1.44	1.71	V
		I <sub>F</sub> =12A,T <sub>vj</sub> =175°C	-	1.50	-	V
	I <sub>R</sub>	V <sub>R</sub> =650V,T <sub>vj</sub> =25°C	-	0.036	60	μΑ
Reverse current		V <sub>R</sub> =650V,T <sub>vj</sub> =150°C	-	2.4	240	μА
		V <sub>R</sub> =650V,T <sub>vj</sub> =175°C	-	7.2	-	μΑ
Total capacitance	С	V <sub>R</sub> =1V,f=1MHz	-	600	-	pF
		V <sub>R</sub> =650V,f=1MHz	-	55	-	pF
Total capacitive charge	$Q_{C}$	V <sub>R</sub> =400V,di/dt=350A/μs	-	28	-	nC
Switching time	t <sub>C</sub>	V <sub>R</sub> =400V,di/dt=350A/μs	-	18	-	ns
Non-repetetive Avaranche Energy	E <sub>ava</sub>	L=1mH	-	150	-	mJ

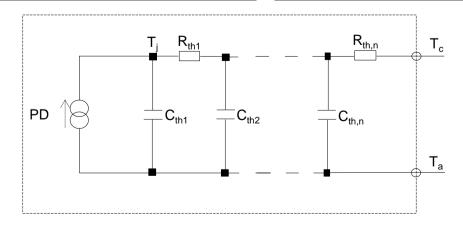
#### Thermal characteristics

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
Thermal resistance	$R_{thJC}$	-	-	1.3	1.9	K/W

# ● Typical Transient Thermal Characteristics

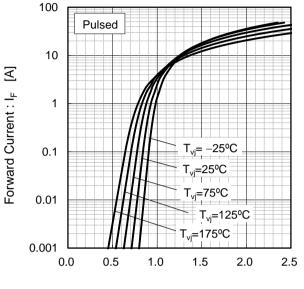
Symbol	Value	Unit
R <sub>th1</sub>	1.36×10 <sup>-2</sup>	
R <sub>th2</sub>	9.66×10 <sup>-2</sup>	K/W
R <sub>th3</sub>	1.19×10 <sup>0</sup>	

Symbol	Value	Unit
$C_{th1}$	3.33×10 <sup>-4</sup>	
$C_{th2}$	2.75×10 <sup>-4</sup>	Ws/K
C <sub>th3</sub>	9.28×10 <sup>-4</sup>	



#### •Electrical characteristic curves

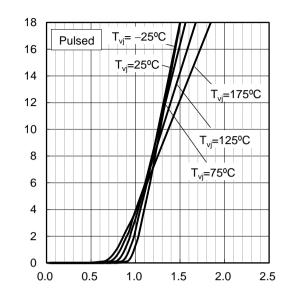
Fig.1 V<sub>F</sub> - I<sub>F</sub> Characteristics



Forward Voltage : V<sub>F</sub> [V]

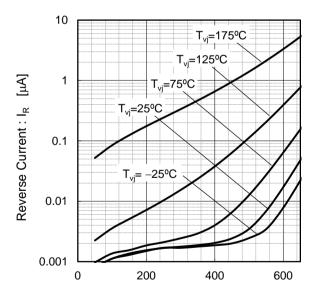
Fig.2 V<sub>F</sub> - I<sub>F</sub> Characteristics

Forward Current : I<sub>F</sub> [A]



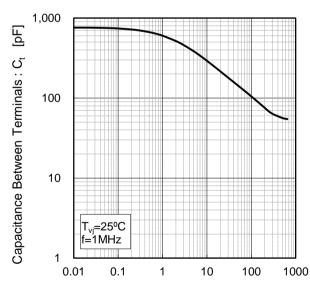
Forward Voltage : V<sub>F</sub> [V]

Fig.3 V<sub>R</sub> - I<sub>R</sub> Characteristics



Reverse Voltage : V<sub>R</sub> [V]

Fig.4 V<sub>R</sub>-C<sub>t</sub> Characteristics



Reverse Voltage : V<sub>R</sub> [V]

#### Electrical characteristic curves

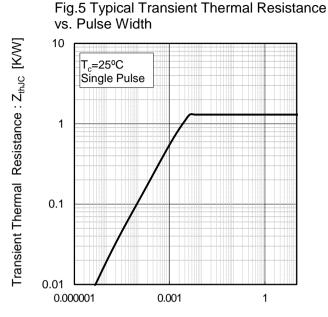
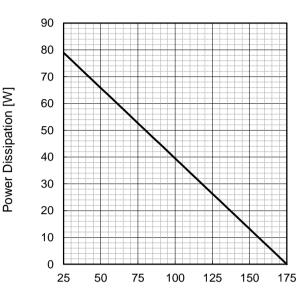


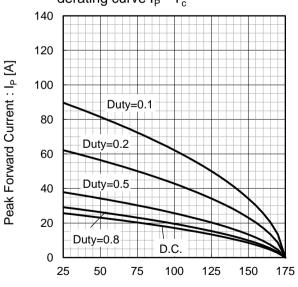
Fig.6 Power Dissipation



Case Temperature : T<sub>c</sub> [°C]

Pulse Width : P<sub>w</sub> [s]

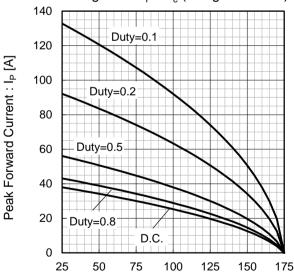
Fig.7\*4 Maximum peak forward current derating curve  $\rm I_{\rm P}$  -  $\rm T_{\rm c}$ 



Case Temperature :  $T_c$  [ ${}^{\circ}$ C]

 $^{*}4$  Based on max Vf, max  $\rm R_{thJC}$  Valid for switching of above 10kHz, excluding D.C. curve.

Fig.8\*5 Typical peak forward current derating curve I<sub>P</sub> - T<sub>c</sub> (Not guaranteed)

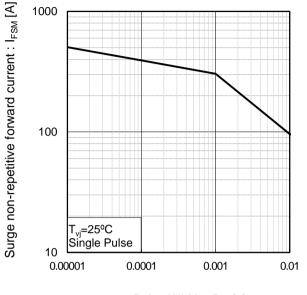


Case Temperature :  $T_c$  [°C]

\*5 Based on typ Vf, typ R<sub>thJC</sub> Typical value, not guaranteed Valid for switching of above 10kHz, excluding D.C. curve

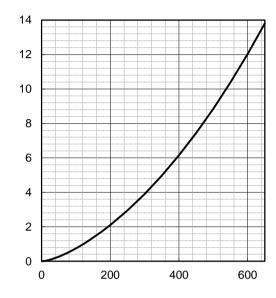
#### • Electrical characteristic curves

Fig.9 Surge non-repetitive forward current vs. Pulse width (Sinusoidal waveform)



Pulse Width: Pw [s]

Fig.10 Typical capacitance store energy

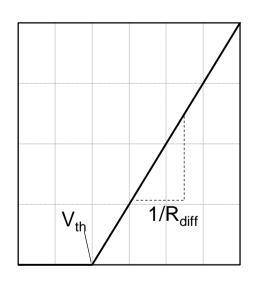


Capacitance stored energy :  $E_C[\mu J]$ 

Reverse Voltage : V<sub>R</sub> [V]

# Symplified forward characteristic model

Fig.11 Equivalent forward current curve



Forward Voltage: V<sub>F</sub>

$$V_F = V_{th} + R_{diff} I_F$$

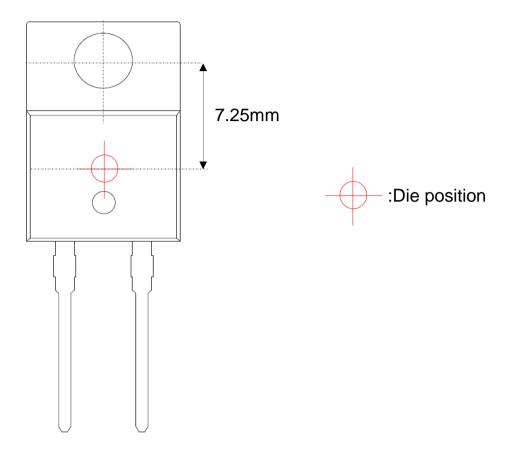
$$\begin{aligned} &V_{th} \left( \ T_{vj} \ \right) = a_0 + a_1 \, T_{vj} \\ &R_{diff} \left( \ T_{vj} \ \right) = b_0 + b_1 \, T_{vj} + b_2 \, T_{vj}^2 \end{aligned}$$

Symbol	Typical Value	Unit
a <sub>0</sub>	9.66×10 <sup>-1</sup>	V
a <sub>1</sub>	-1.1×10 <sup>-3</sup>	V/°C
b <sub>0</sub>	2.93×10 <sup>-2</sup>	Ω
b <sub>1</sub>	6.22×10 <sup>-5</sup>	Ω/°C
b <sub>2</sub>	6.40×10 <sup>-7</sup>	Ω/°C <sup>2</sup>

 $T_{vi}$  in  ${}^{\circ}C$ ; -55  ${}^{\circ}C$  <  $T_{vi}$  < 175 ${}^{\circ}C$  ;  $I_F$  < 24 A

Forward Current: IF

# **●**Die Bonding Layout



- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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