



#### **Description**

This series is a high accuracy, high input voltage low quiescent current, high speed, and low dropout Liner regulator with high ripple rejection. The device is manufactured with Bi-CMOS process.

This offers over-current limit and over temperature protection to ensure the device working in well conditions.

### **Specification**

Supply Voltage : 4.75V to 40V
Output Range : 1.8V to 10V
Output Accuracy : <+/-2%

Output Current : 250mA (Up to 500mA Typ.)

PSRR : 50dB @ 100Hz

 $\begin{array}{lll} \mbox{Dropout Voltage} & : 850\mbox{mV} \ \mbox{@ lout=}250\mbox{mA} \\ \mbox{Quiescent Current} & : 6\mbox{$\mu$A}\mbox{@Vin=}7V(\mbox{Typ.}) \\ \end{array}$ 

Recommend Capacitor : 10µF

### Absolute Maximum Ratings Ta = 25°C

Parameter	Rating	
Power Dissipation	Internal limited (mW)	
V <sub>IN</sub> Range	-0.3V to 45V	
Vouт Range	-0.3V to 10V	
Lead Temperature Range	260	
Storage Temperature Range	-55°C to 150°C	
Operating Junction Temperature Range	125°C	
ESD MM	400V	
ESD HBM	4K	

### Recommended Operating Conditions Ta = 25°C

Parameter	Rating
Operating Supply Voltage	4.75V to 40V
Operating Temperature Range	-40°C to 85°C
Thermal Resistance(On PCB),R JA	43.5°C/W
Power Dissipation	1000mW

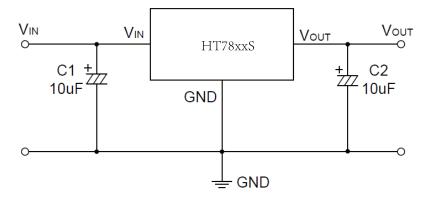
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#### Electrical Characteristics (Ta=25 C,VIN=12V,CIN=COUT=10uF,unless otherwise noted)

Parameter Name	Symbol	Test Co	nditions	Min	Тур	Max	Unit
Input Range	Vin	Ιουτ=10mA		4.75		40	
Output Voltage	Vouт	V <sub>IN</sub> =12V, Іоит=10m	HT7830S mA HT7833S HT7850S	2.94	3	3.06	V
				3.234	3.3	3.366	
				4.9	5	5.1	
Maximum Output Current	Іоит_рк	Vin=12V, RL=1Ω			500		mA
Quiescent Current	lα	V <sub>IN</sub> =7V, No load 6		8			
		V <sub>IN</sub> =24V, No load			7.5	10	μА
		V <sub>IN</sub> =40V, No load			10	15	
Dropout Voltage	Vdrop	Ιουτ=1mA			2	12	mV
		Ιουτ=1mA			300	400	
		Іоит=250mA			850	1200	
Line Population	Lnr	V1=7~24V, V0UT=5V, I0UT=1mA		0.02		%/V	
Line Regulation		VIN=7~45V, VOUT=5V, IOUT=1mA			0.1		70/ V
Load Population	LDR	VIN=12V, IOUT=1~100mA		0.6		%	
Load Regulation	LDK	V <sub>IN</sub> =7V, I <sub>OUT</sub> =1~250mA			2		
Output Noise	eno	Іоит=10mA		-100		100	μV
Ripple Rejection	PSRR	V <sub>IN</sub> =10V V <sub>PP</sub> =0.5V I <sub>OUT</sub> =1mA	f=100Hz		50		dB
			f=1KHz		40		
			f=10KHz		30		
Thermal Protection	Tsp	VIN=12V, IOUT=1mA			155		°C
Thermal Protection Hys	Tsd_hys	VIN=12V, IOUT=1mA			30		
Temperature Cofficient	ΔVο/ΔΤ	VIN=12V, IOUT=1mA			±0.1		mV/°C

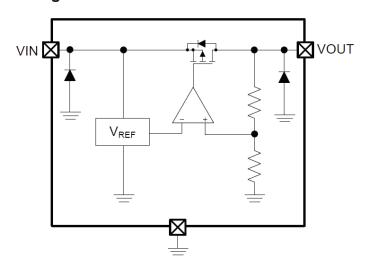
### **Typical Application**



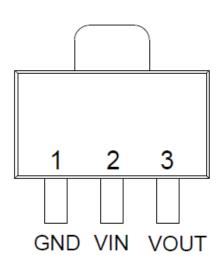
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### **Functional Block Diagram**



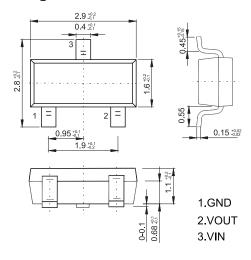
### **Pin Configuration**



### **Pin Description**

Pin Number	Pin Name	Function Description
1	GND	Ground
2	Vin	Voltage Input
3	Vouт	Voltage Output

#### **Diagram**



#### **Part Number Table**

Description	Part Number		
Low Quiescent Current LDO, 3.0V, SOT-23-3	HT7830S		
Low Quiescent Current LDO, 3.3V, SOT-23-3	HT7833S		
Low Quiescent Current LDO, 5.0V, SOT-23-3	HT7850S		

Dimensions: Millimetres

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