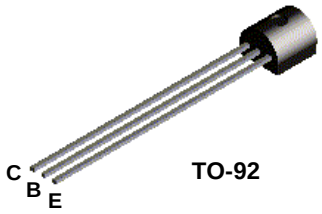
FAIRCHILD
SEMICONDUCTOR TM

2N5400



TO-92

PNP General Purpose Amplifier

This device is designed for use as general purpose amplifiers and switches requiring high voltages.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	120	V
V_{CBO}	Collector-Base Voltage	130	V
V_{EBO}	Emitter-Base Voltage	5.0	V
I_C	Collector Current - Continuous	600	mA
T_J, T_{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:
1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		2N5400	
P_D	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

PNP General Purpose Amplifier
(continued)

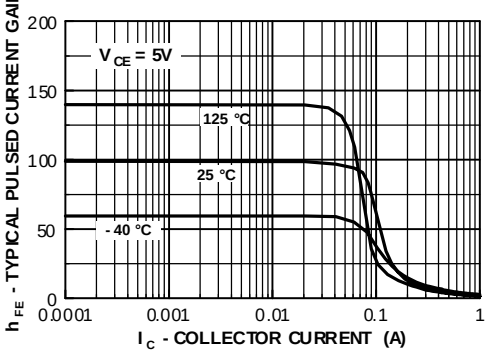
Electrical Characteristics
TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHARACTERISTICS					
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage*	I_C = 1.0 mA, I_B = 0	120		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	I_C = 100 µA, I_E = 0	130		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	I_E = 10 µA, I_C = 0	5.0		V
I_{CBO}	Collector Cutoff Current	V_{CB} = 100 V, I_E = 0 V_{CB} = 100 V, I_E = 0, T_A = 100 °C		100 100	nA µA
I_{EBO}	Emitter Cutoff Current	V_{EB} = 3.0 V, I_C = 0		50	nA
ON CHARACTERISTICS*					
h_{FE}	DC Current Gain	V_{CE} = 5.0 V, I_C = 1.0 mA V_{CE} = 5.0 V, I_C = 10 mA V_{CE} = 5.0 V, I_C = 50 mA	30 40 40	 180 	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C = 10 mA, I_B = 1.0 mA I_C = 50 mA, I_B = 5.0 mA		0.2 0.5	V V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I_C = 10 mA, I_B = 1.0 mA I_C = 50 mA, I_B = 5.0 mA		1.0 1.0	V V
SMALL SIGNAL CHARACTERISTICS					
C_{ob}	Output Capacitance	V_{CB} = 10 V, f = 1.0 MHz		6.0	pF
f_T	Current Gain - Bandwidth Product	I_C = 10 mA, V_{CE} = 10 V, f = 100 MHz	100	400	
h_{fe}	Small-Signal Current Gain	I_C = 1.0 mA, V_{CE} = 10 V, f = 1.0 kHz	30	200	
NF	Noise Figure	V_{CE} = 5.0 V, I_C = 250 µA, R_S = 1.0 kΩ, f = 10 Hz to 15.7 kHz		8.0	V

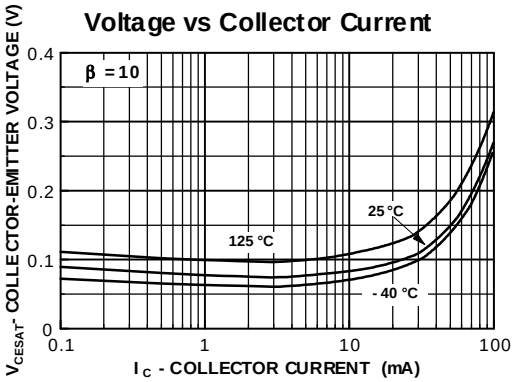
*Pulse Test: Pulse Width £ 300 ms, Duty Cycle £ 2.0%

Typical Characteristics

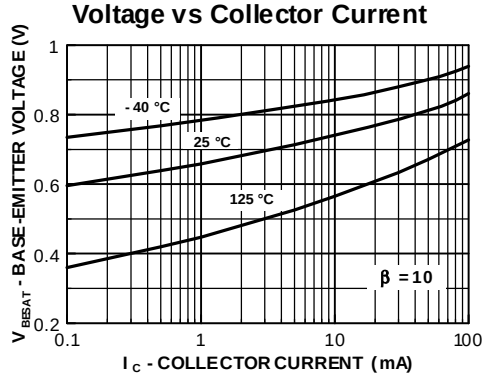
Typical Pulsed Current Gain vs Collector Current



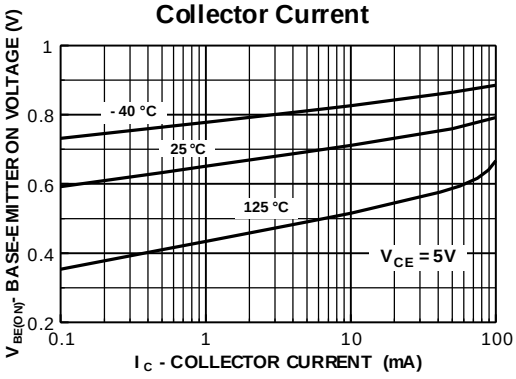
Collector-Emitter Saturation Voltage vs Collector Current



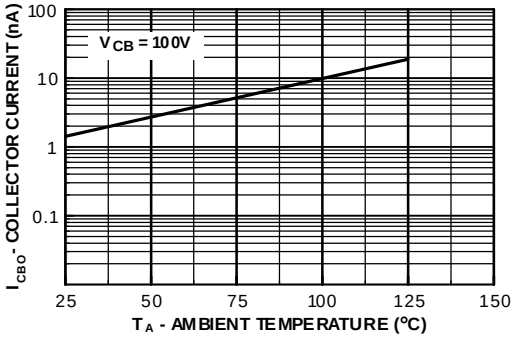
Base-Emitter Saturation Voltage vs Collector Current



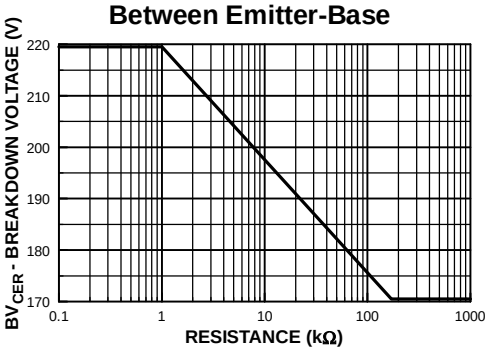
Base-Emitter ON Voltage vs Collector Current



Collector-Cutoff Current vs Ambient Temperature

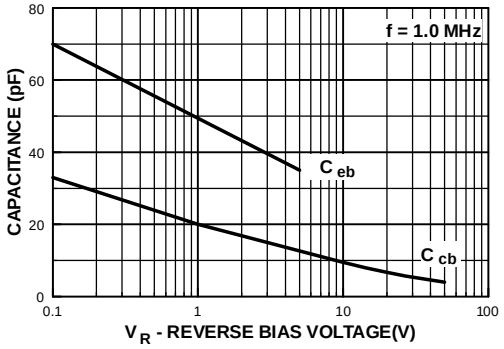


Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

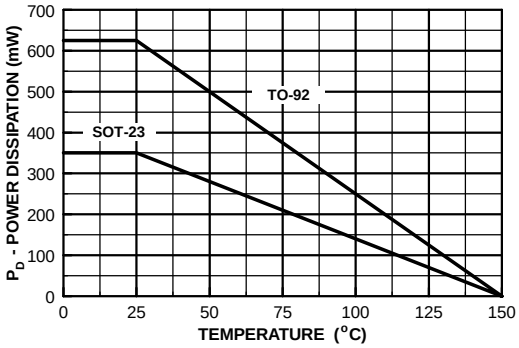


Typical Characteristics (continued)

Input and Output Capacitance vs Reverse Voltage



Power Dissipation vs Ambient Temperature



TO-92 Ta

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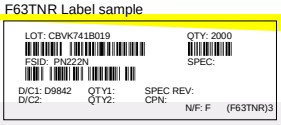
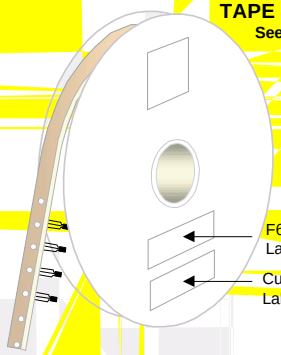
FSCINT Label sample

F63TNR Label sample

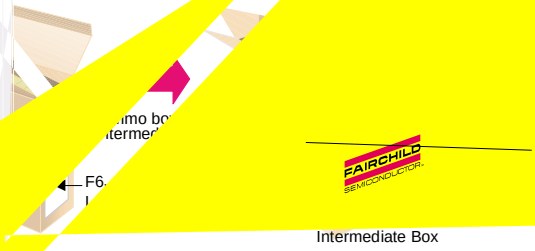


TAPE and REEL OPTION

See Fig 2.0 for various Reeling Styles



A

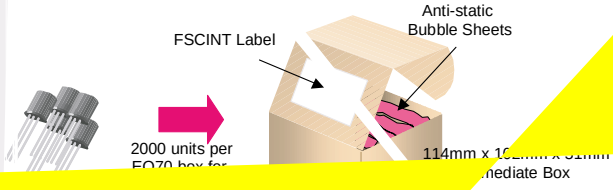


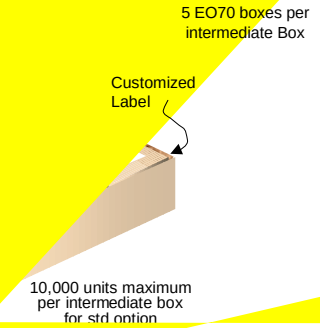
(TO-

EOL CODE			
J18Z			
J05Z			
NO F			
L34Z	94 (NON PROELECTRON SERIES), 96 TO-92 STANDARD STRAIGHT FOR: PKG 94 (PROELECTRON SERIES BCXXX, BFXXX, BSRXXX), 97, 98	NO LEADCLIP	2.0 K / BOX

BULK OPTION

See Bulk Packing Information table





TO-92 Tape and Reel Data, continued

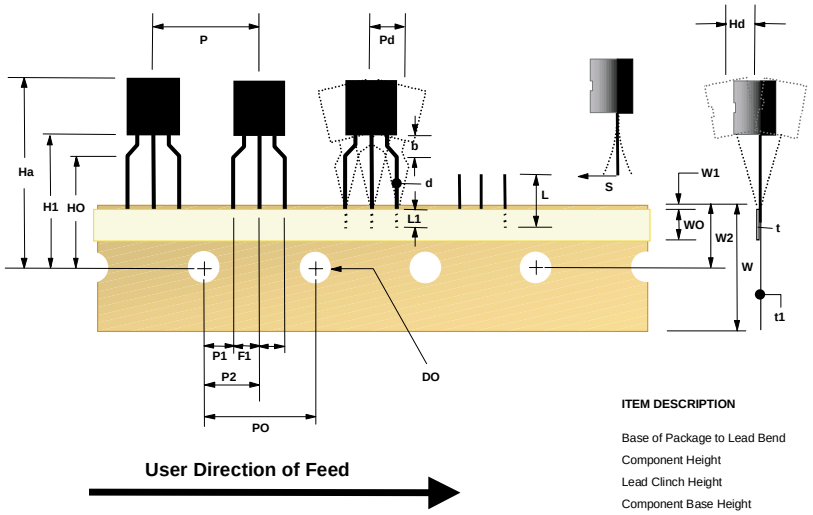
TO-92 Reeling Style
Configuration: Figure 2.0

Machine Option “A” (H)

Style “A”, D26Z, D70Z (s/h)

TO-92 Tape and Reel Data, continued

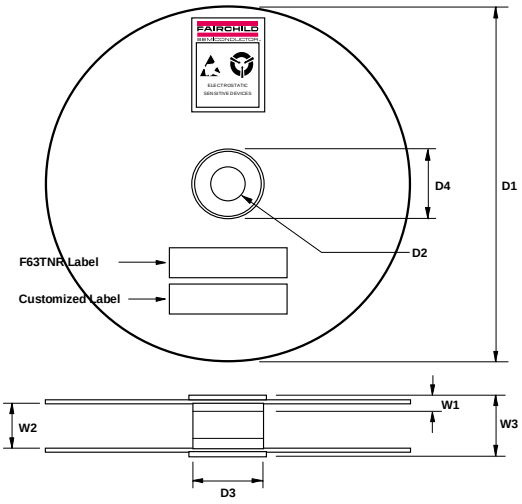
TO-92 Tape and Reel Taping
Dimension Configuration: Figure 4.0



ITEM DESCRIPTION	SYMBOL	DIMENSION
Base of Package to Lead Bend	b	0.098 (max)
Component Height	Ha	0.928 (+/- 0.025)
Lead Clinch Height	HO	0.630 (+/- 0.020)
Component Base Height	H1	0.748 (+/- 0.020)
Component Alignment (side/side)	Pd	0.040 (max)
Component Alignment (front/back)	Hd	0.031 (max)
Component Pitch	P	0.500 (+/- 0.020)
Feed Hole Pitch	PO	0.500 (+/- 0.008)
Hole Center to First Lead	P1	0.150 (+0.009, -0.010)
Hole Center to Component Center	P2	0.247 (+/- 0.007)
Lead Spread	F1/F2	0.104 (+/- 0 .010)
Lead Thickness	d	0.018 (+0.002, -0.003)
Cut Lead Length	L	0.429 (max)
Taped Lead Length	L1	0.209 (+0.051, -0.052)
Taped Lead Thickness	t	0.032 (+/- 0.006)
Carrier Tape Thickness	t1	0.021 (+/- 0.006)
Carrier Tape Width	W	0.708 (+0.020, -0.019)
Hold - down Tape Width	WO	0.236 (+/- 0.012)
Hold - down Tape position	W1	0.035 (max)
Feed Hole Position	W2	0.360 (+/- 0.025)
Sprocket Hole Diameter	DO	0.157 (+0.008, -0.007)
Lead Spring Out	S	0.004 (max)

Note : All dimensions are in inches.

TO-92 Reel
Configuration: Figure 5.0



ITEM DESCRIPTION	SYSMBOL	MINIMUM	MAXIMUM
Reel Diameter	D1	13.975	14.025
Arbor Hole Diameter (Standard)	D2	1.160	1.200
(Small Hole)	D2	0.650	0.700
Core Diameter	D3	3.100	3.300
Hub Recess Inner Diameter	D4	2.700	3.100
Hub Recess Depth	W1	0.370	0.570
Flange to Flange Inner Width	W2	1.630	1.690
Hub to Hub Center Width	W3		2.090

Note: All dimensions are inches

TO-92 Package Dimensions

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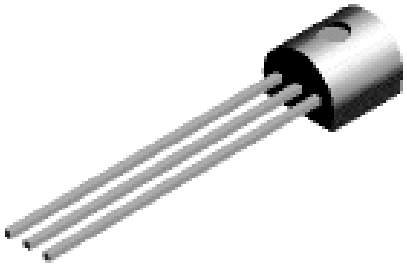
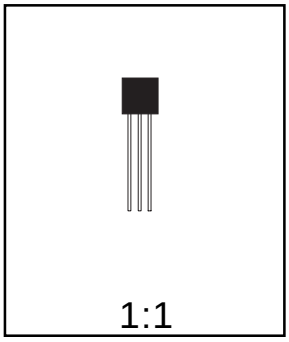
TO-92 (FS PKG Code 92, 94, 96)





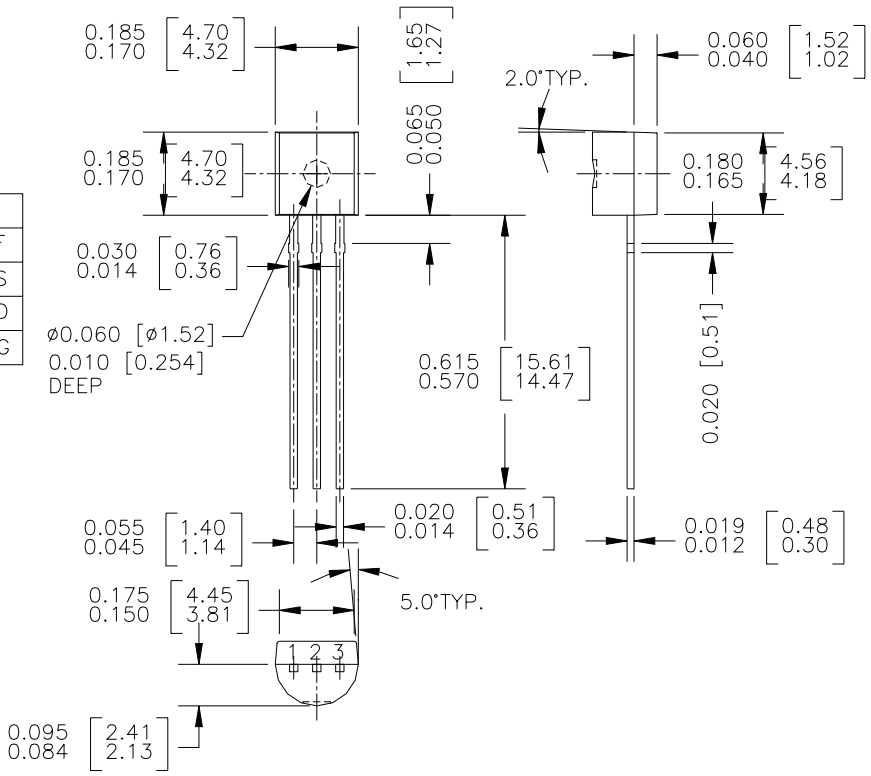
Scale 1:1 on letter size paper

Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977

TO−92 (92,94,96)

PIN	92		94		96	
	B	F	B	F	B	F
1	E	D	E	D	B	S
2	B	S	C	G	E	D
3	C	G	B	S	C	G



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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